

# Innovating Energy Technology

# Fuji Integrated Controllers Programmable Controllers MICREX-SX Series











2



# **Realizes High-Speed Advanced Machine Control**

I/O control with a program capacity of up to 512 K steps and up to 65,536 points enables a suitable system configuration ranging from small through to large scale. 0.25 ms program scan and I/O refresh are possible. Function and performance distribution are possible in a multi-CPU system configuration with up to 8 CPUs.

## **Open Network Oriented**

MICREX-SX series

Both the hardware and software conform to the IEC61131 international standard for programmable controllers. Compatible with Ethernet, EtherCAT, BACnet MS/TP\*, DeviceNet, PROFIBUS-DP, and other diverse open networks.

## Integration of Control, Information, and Communication

With the aid of an upgraded data processing function, mass memory storage, and a built-in Ethernet function, the SPH is capable of monitoring the operation of production systems and devices and recording operation history and errors in addition to conventional FA control. It thus enables you to use the controller for wider applications of IT-based remote monitoring, maintenance support, and preventive maintenance.

CPU and power supply redundancy can also be achieved in response to the growing demand for higher reliability.

## Highly Reliable Duplex System Allows Stable **Continuous Operation**

Redundant CPU, duplexed control network, and duplexed I/O network allow stable continuous operation. Control systems that require high reliability such as infrastructure equipment can be constructed.

#### Evolution from the SX bus to the E-SX bus SPH5000M/H

The released E-SX bus has evolved from the SX bus, a system bus.

4096 words of the direct connection I/O capacity or 8 times the previous capacity, 2048 words/ms of the refresh performance or 16 times the previous performance, and 100 Mbps/100 m of the transmission speed and the station-to-station distance, 4 times the previous values, allow the bus to be applied to more complicated and large-scale device and facilities.

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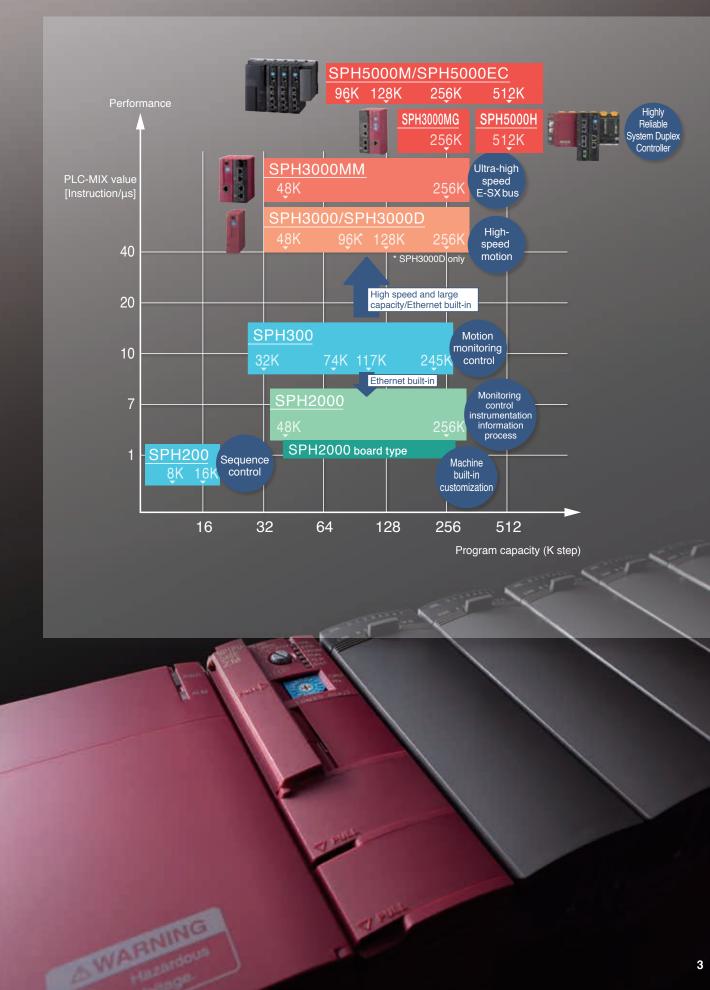
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SPH3000MM/MG

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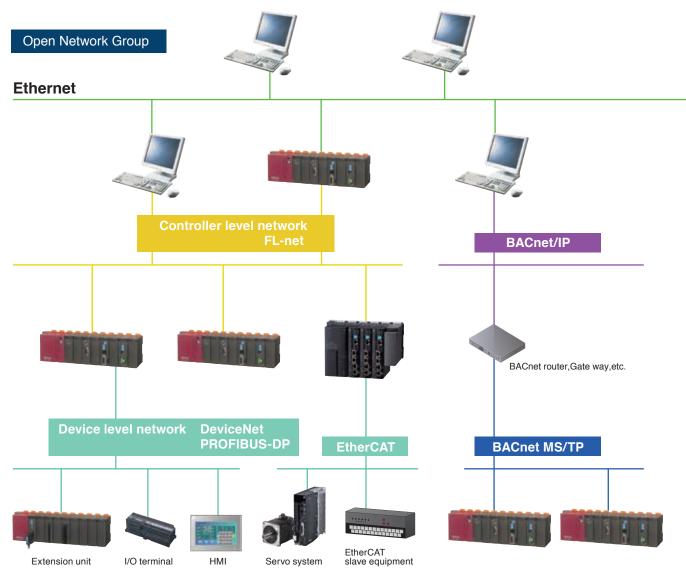
\*Only for Japan's doemestic market

# **Programmable Controller**



# SX bus Diverse Network Systems Enabling Seamless Access

High-speed process and distributed arrangement of the E-SX bus and the SX bus allow seamless connections with human machine interfaces (HMIs), inverters, and servos. Various open network systems such from a small-scale application built in a machine to a hierarchical distributed system of large-scale line and facility devices can be constructed.



#### BACnet MS/TP\*

BACnet is an open network that comprehensively monitors, controls, and manages the various facilities of building management systems, including their air conditioners, heaters, lighting, and emergency and security equipment. In particular, BACnet MS/TP is a BACnet communication protocol for field devices. "Only for Japan's doemestic market

#### FL-net

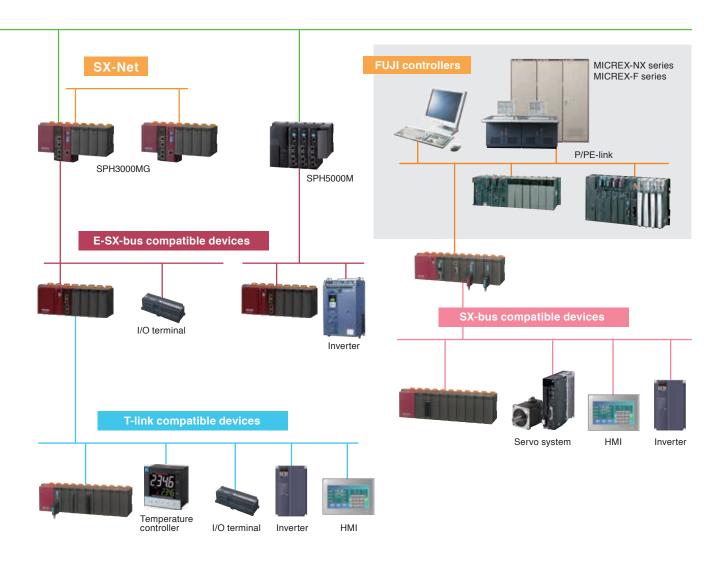
Open network at the FA application type controller level established by the Japan Electrical Manufacturers Association. Allows inter-connection with PLC, CNC, and robots beyond the frame of a single manufacturer. The communication physical layer employs Ethernet.

#### PROFIBUS-DP

Device-level open network established by the EN50170 European standard. It best suits time-critical applications between an automation system and distributed devices (remote I/O, inverters, etc.).



#### **Original Network Group**



#### **OPCN-1**

Device-level open network established by Japan Electrical Manufacturers Association. Allows connection with PLC and robots using the same signal line beyond the frame of a single manufacturer, very effective in open system improvement and optimization.

#### DeviceNet

Open device-level network which facilitates inter-connection of control equipment such as PLCs, personal computers, sensors, and actuators. Wiring cost reduction by minimizing wiring, and multi-vendor equipment connection simplify an economical system configuration.

#### EtherCAT

An open network based on Ethernet, developed by Beckhoff Automation GmbH in Germany. Its ability to quickly transmit Ethernet frames with highly accurate time synchronization enables it to facilitate the construction of high-speed, high-precision control systems.

## **Ultra-High-Speed 1 ms Controller**

#### 1 ms scan

- Program scan time of 1ms is implemented by increased instruction processing speed.
- Real number op eration and high-precision positioning control have been put to practical use by dramatically improved floating-point operation speed.

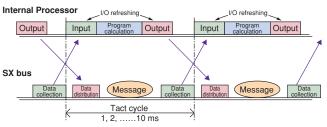
#### 1 ms I/O refreshing

- · 4096 points of I/O is refreshed in 1 ms
- Tact control assures a fixed I/O refresh interval. The I/O refresh cycle can be set to 1 ms, 2 ms, or up to 10 ms, which is suitable for processing requiring strict tact time.
- The minimum tact time of, SPH5000M, SPH300, and SPH2000/SPH3000 can be set at 0.25 ms, 0.5 ms, and 1 ms respectively.

|                                   | SPH5000M/EC | SPH5000H | SPH3000(D) | SPH300 | SPH2000 | SPH200  |
|-----------------------------------|-------------|----------|------------|--------|---------|---------|
| Basic instruction LD              | 4ns         | 6ns      | 9ns        | 20ns   | 30ns    | 70ns    |
| MOV                               | 4.4ns       | 5ns      | 8ns        | 40ns   | 40ns    | 140ns   |
| Floating<br>Operation instruction | 25.3ns      | 66ns     | 88ns       | 80ns   | 270ns   | 56000ns |

 $^{\star}\,$  For details on each instruction word's processing speed and tact cycle, see the User's Manual (FEH200).

#### Operating timing



## **Tact Cycle**

| F-S) | (h | IIC |
|------|----|-----|

| Tact cycle               |             | 0.25ms | 0.25ms 0.375ms 0.5ms 1ms |         | 1ms      | 1.5ms    | 2ms      |
|--------------------------|-------------|--------|--------------------------|---------|----------|----------|----------|
| Max. I/O size            | 4 stations  | 67word | 256word                  | 512word | 2048word | 2048word | 4096word |
| (Number of I/O stations) | 16 stations | —      | —                        | 256word | 1024word | 1024word | 1024word |
|                          | 32 stations | —      | —                        | —       | 512word  | 2048word | 2048word |
|                          | 64 stations | —      | —                        | -       | —        | 512word  | 1024word |

#### SX bus

| Tact cycle    | 0.25ms | 0.375ms | 0.5ms  | 1ms     | 1.5ms   | 2ms     |
|---------------|--------|---------|--------|---------|---------|---------|
| Max. I/O size | —      | —       | 64word | 128word | 256word | 512word |

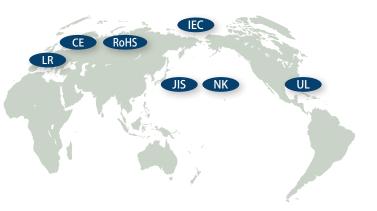
## **Controller Conforms to International Standard**

#### Conforms to IEC 61131 international standard

- Both the hardware and software conform to the IEC 61131 international standard for programmable controllers.
- The programming language conforms to the IEC 61131-3 international standard.

#### Conforming to international standard

- Conforms to the CE marking, UL standards and RoHS directive (conforming one after another) as well as IEC standard.
- Also complies with maritime classification societies such as NK (Japan's Nippon Kaiji Kyokai) and LR (UK's Lloyd's Register).



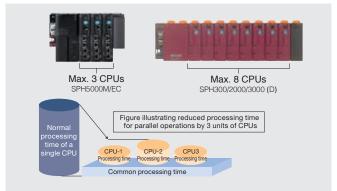
# Programmable Controller

## **Multi-CPU System**

#### **Parallel processing**

#### (SPH300/SPH2000/SPH3000/SPH3000D/SPH5000M/SPH5000EC)

 Alleviates the load for each CPU allowing high-speed processing of a large application program. For example, the load can be distributed for advanced processing and sequence control processing with additional CPUs. I/O refresh control is performed automatically even if parallel processing by multiple CPUs is performed.



## **Redundant System Brings System Safety and Reliability**

#### 1:1 warm-standby feature (SPH300/SPH2000)

- This redundancy configuration enables continued operation without system downtime if a CPU fails. (Control may temporarily stop due to fault detection and CPU changeover.)
- The same program is stored in CPUs for the active and backup systems, allowing constant data value equalization.

#### N:1 cold-standby feature (SPH300)

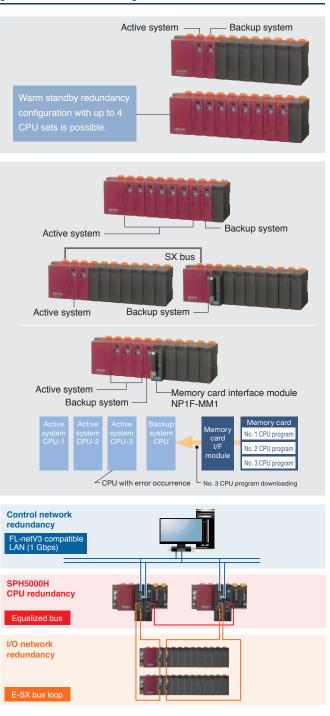
- N:1 backup feature enables reduction of the number of standby system CPUs to one, though when a CPU fails, data retained in the active system and that in the standby system are not equalized.
- Data retained by the active system is not taken over. The backup system CPU performs initial start.
- Programs can be intensively controlled by a memory card. Programs for N units of systems can be stored on a memory card, which is installed in the memory card interface module for centralized control of the programs. The same processing programs as on the down CPU are downloaded to the backup system CPU.

# Highly reliable duplex system feature (SPH5000H)

 Allows you to construct control systems that support redundant CPU, duplexed control network, duplexed I/O network, and loop network.



Note 2: For a redundancy configuration buildup with a DC power supply, contact our sales section.



## **Basic Configuration of SX bus**

Ultra-high-speed SX bus preserves distributed installation and expandability up to 254-module direct bus connection.

# Distributed placement is enabled by SX buses extended up to 25 m in total.

Up to 25 extension base boards, HMI and other SX-busbased devices can be connected within 25 m. (Up to 25.6 km for optical transmission)

#### Free topology is implemented by T-branches.

Use of T branches allows detailed, distributed installation of

the SX bus. Expansion units and diverse equipment arranged in a tree structure can be connected in the optimum way.

#### SX bus implements connecting max. 254 modules.

The number of modules that can be connected to the SX bus is a max. of 254 units. CPU modules, the communication modules, the positioning modules, the function modules, and the standard I/O modules can be connected up to 254 units.

## Features of E-SX bus

Supports large-scale, high-speed control through its enhanced SX bus transmission speed and larger direct I/O capacity

#### Suitable for distributed large-scale machinery and equipment

Its total length of 1 km and station-to-station length of 100 m facilitates larger systems than the SX bus.

#### Compatible with large-scale, high-speed control systems

It increases direct I/O capacity to 4096 words and bus communication speed to 100 Mbps, four times faster than the SX bus. This enables faster control.

# Contributes to the stable operation of control systems

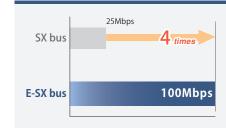
It comes with loopback and signal bypass functions that make it possible to build systems resistant to equipment failure.

## Comparison of Functions and Performances between the E-SX bus and the SX bus

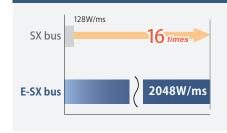
| Function and performance                            | SPH3000(D)   | SPH5000M/    | Н, 3000ММ/МG                  |
|---|--------------|--------------|-------------------------------|
| System bus  | SX bus       | SX bus       | E-SX bus                      |
| Direct connection I/O capacity                      | 512 words    | 512 words    | 4096 words                    |
| Refresh performance                                 | 128 words/ms | 128 words/ms | 2048 words/ms                 |
| Transmission speed                                  | 25 Mbps      | 25 Mbps      | 100 Mbps                      |
| Tact fluctuation                                    | 100 μs       | 100 μs       | ±1µs or less                  |
| Synchronization between stations                    | None         | None         | Provided (±1 $\mu$ s or less) |
| Distance (between stations/total distance)          | 25 m/25 m    | 25 m/25 m    | 100 m/1 km                    |
| Continued operation with the line broken (Loopback) | None         | None         | Provided                      |

#### Direct connection I/O capacity

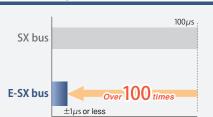




#### Refresh performance

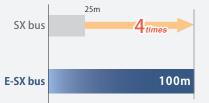


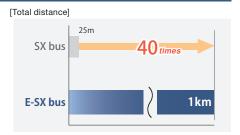




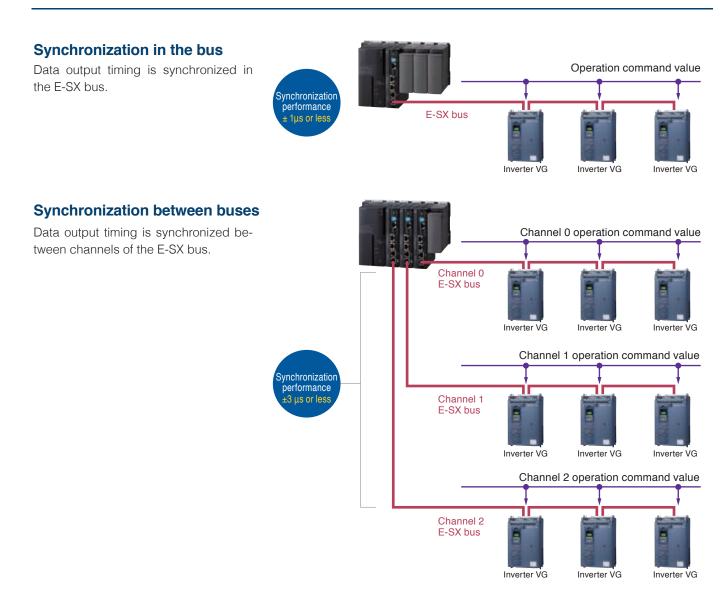


Transmission speed





## Synchronization Control of E-SX bus



## **Connection Function of the E-SX bus**

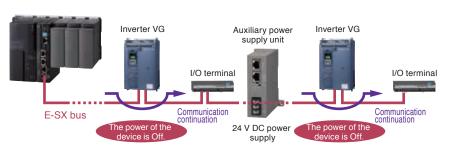
#### Loopback function

Communication is continued by the signal repeater function even when a wire is broken.



#### Signal bypass function

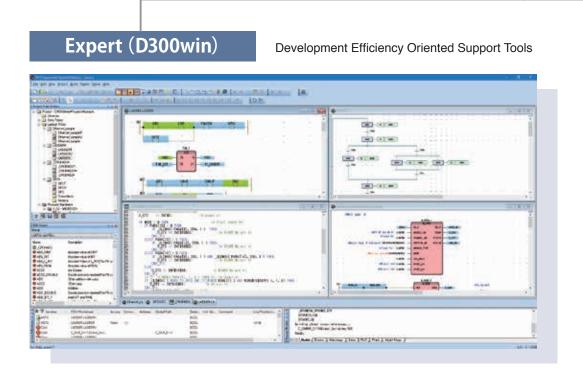
Even when a power of some devices is not turned on, the communication is continued by the auxiliary power unit.



## Two Types of Programming Support Tools in Accordance with Development Style

These are Windows-compatible programming support tools conforming to the IEC61131-3 International Standard.

# SX-Programmer



#### Usage

# Improvement of software development efficiency

Programming in units of POU or worksheets allows the use of the structured design method by which a program is created by dividing it by functionality or process. This method enables multiple designers to divide the program design among them so that a substantial reduction in the program creation time can be achieved.

# Programming of the same techniques as those of microcomputers and personal computers

The ST language is similar to the C language so that programs can be created using the same techniques as those of microcomputers and personal computers for complex calculations that are hard to implement using the Ladder language. Programs and circuits that are frequently used can easily be reused by making them FB (function blocks).

## Features

#### Writing in multiple languages

- The Expert (D300win) completely supports five types of program representations specified by the standards.
- It allows the programmer to code the proper combination of representations for the control target.

#### Supported representations

IL (Instruction List) LD (Ladder Diagram) FBD (Function Block Diagram) ST (Structured Text) SFC (Sequential Function Chart)

#### **Excellent documentation function**

 The documentation preparation function has been substantially improved. Not only can it print drawing numbers, dates, page, and drawing borders, but also company logos and comments.

#### Simulation function

• This tool enables program test runs using the simulation function built in Expert (D300win), without using the actual unit.

# Function module support function/ HMI cooperation function

- The Expert (D300win) has implemented function module support and HMI cooperation support functions as common support tools.
- The function module support can be operated with the programming supporting tool connecting CPU module.



**Operability Oriented Support Tools** 

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# Ladder operation for on-site maintenance personnel

Supports the full keyboard operations useful for on-site maintenance personnel.

Standard

Editing and download can be performed immediately after activation.

#### Utilization of programming resources

Program and comment resources of the models MICREX-F series and FLEX-PC series of Fuji Electric can be reused. Screens, operability, and programming can be handled as if you were using a personal computer loader with which you are already familiar.

## Features

#### **Multi-language support**

- The SPH supports not only ladder diagrams but also ST and FBD.
- It allows the programmer to select the proper programming language for the control target.

#### Intuitive screen operation

- Through guidance display and a command word candidate narrowing-down function based on a keyword search, you can input data without referring to the manual.
- You can select the proper input mode according to the situation from functions such as mouse wheel + click input, keyword search input, and Intellisense function input.

#### **Simulation function**

• Provided with built-in Standard, the SPH is capable of testing the operation of programs without using an actual system.

#### **Resume function**

- · When the SPH starts to run, it automatically displays the position last edited or monitored.
- In online mode, the SPH displays the position last monitored and starts monitoring.
- · In offline mode, the SPH displays the position last monitored and enters Edit mode.

#### **Device editor and collation function**

- Device information is displayed on a single screen, for example, in the form of a list of the operating states of devices, enabling you to save time in memory management.
- You can display details of different points on programs and edit by referring to collation results.

## MEMO

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|------------------------------------|----|
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#### **Programming Support Tool**

| Programming Support Tool: NP4H-SEDBV3<br>SX-Programmer Expert (D300win) |             |
|---|-------------|
| Related DevicesPCI-Bus-Based FL-net (OPCN-2) Ver. 2.0 Board             | 6<br>7<br>3 |
| Dimensions  |             |

\*Only for Japan's doemestic market



KD03-041A

## Programmable Controllers MICREX-SX series General Specifications

#### General specifications

| Item                |                                       | Specifications   |                 |  |
|---------------------|---------------------------------------|--|-----------------|--|
| Physical            | Operating ambient temperature         | 0 to +55°C   | IEC 61131-2     |  |
| environment         | Storage temperature                   | -25 to +70°C   | JIS B 3502      |  |
|                     | Relative humidity                     | 20 to 95%RH (without condensation)   |                 |  |
|                     | Contamination degree                  | Contamination degree 2 (free from conductive dust)   | 7               |  |
|                     | Corrosion resistance                  | No corrosive gas is present, no organic solvent adhesion   |                 |  |
|                     | Operating altitude                    | Altitude of 2000 m or less (air pressure of 70 kPa or higher during transportation)                              |                 |  |
| Mechanical          | Resistance to vibration               | One amplitude: 0.15 mm, constant acceleration: 19.6 m/s <sup>2</sup> , 2 hours for each direction, 6 hours total |                 |  |
| operating condition | Resistance to shock                   | Peak acceleration: 147 m/s <sup>2</sup> , 3 times for each direction   |                 |  |
| Electrical          | Electrostatic discharge               | Contact discharge ±6 kV  | IEC 61000-4-2   |  |
| operating           |                                       | Aerial discharge ±8 kV   | JIS C 61000-4-2 |  |
| ondition            | Radiative radio frequency             | 80 to 1000 MHz 10 V/m  |                 |  |
|                     | electromagnetic field                 | 1.4 to 2.0GHz 3 V/m  | JIS C 61000-4-3 |  |
|                     |                                       | 2.0 to 2.7GHz 1V/m   |                 |  |
|                     | Fast transient burst                  | Power supply line and I/O signal line (AC non-shield line): ±2 kV  | IEC 61000-4-4   |  |
|                     |                                       | Communication line and I/O signal line (except for AC non-shielded line): ±1 kV                                  | JIS C 61000-4-4 |  |
|                     | Surge                                 | AC power supply: Common mode ±2 kV, normal mode: ±1 kV   | IEC 61000-4-5   |  |
|                     |                                       | DC power supply: Common mode ±0.5 kV, normal mode: ±0.5 kV   | JIS C 61000-4-5 |  |
|                     | Radio frequency electromagnetic field | 150 kHz to 80 MHz, 10 V  | IEC 61000-4-6   |  |
|                     | Conducted interference                |  | JIS C 61000-4-6 |  |
|                     | Power frequency magnetic field        | 50 Hz, 30 A/m  | IEC 61000-4-8   |  |
|                     |                                       |  | JIS C 61000-4-8 |  |
|                     | Square wave impulse noise             | ±1.5 kV, 1ns rising edge, 1 µs pulse width, 50 Hz  |                 |  |
| Structure           |                                       | Open Type device (Built-in control panel type)   |                 |  |
| Cooling method      |                                       | Natural cooling  |                 |  |

## Programmable Controllers MICREX-SX series Power Supply Module

#### Power Supply Module: NP1S-

#### Features

- Power supply module redundancy Redundancy of the power supply has been realized by supplying the power from multiple power supply modules. Redundant power supply units allow you to improve system reliability.
- Small capacity power supply module (NP1S-81/NP1S-91) The use of the 100 V AC or 200 V AC small capacity power supply module (single slot) on a 3-slot and 6-slot basis allows effective use of one slot.
- Large capacity power supply module (NP1S-22S/NP1S-62S)

The module achieves twice the output current of the NP1S-22 using the same number of slots. Nearly all modules can be fully installed on the 13-slot base without the need of extra power supply modules to increase capacity.



#### Power supply specifications

| Item  | Specifications   |  |   |  |
|---|--|--|---|--|
| Model   | NP1S-22  | NP1S-42  | NP1S-81                                 | NP1S-91                                |
| Rated input voltage   | 100 to 120/200 to 240 V AC   | 24 V DC  | 200 to 240 V AC                         | 100 to 120 V AC                        |
| Voltage tolerance   | 85 to 132 V AC, 170 to 264 V AC  | 19.2 to 30V DC   | 170 to 264 V AC                         | 85 to 132 V AC                         |
| Rated frequency   | 50/60 Hz   | _  | 50/60 Hz                                |  |
| Dropout tolerance   | 1 cycle or less(Rated voltage, rated load)   | 10 ms or less (Rated voltage, rated load)  | 1 cycle or less(Rated voltage, rated    | load)                                  |
| AC waveform distortion factor   | 5% or less   | _  | 5% or less                              |  |
| Ripple factor tolerance   | _  | Three-phase full-wave rectification 5% or less   | -                                       |  |
| Leakage current   | 0.25mA or less   | I  |   |  |
| Inrush current  | 22.5 Ao-p or less (ambient temperature = 25°C not repeated   | ) 150 Ao-p or less 2 ms or less  | 22.5 Ao-p or less (ambient temperate    | ure = 25°C not repeated)               |
| Power consumption   | 110 VA or less   | 45 W or less   | 50 VA or less                           |  |
| Rated output voltage  | 24 V DC (22.8 to 26.4 V DC)  | 1  | 1                                       | 40 VA or less                          |
| Output current  | 0 to 1.46 A  |  | 0 to 0.625 A                            |  |
| Insulation method   | Transducer   |  |   |  |
| Dielectric strength   | 2300 V AC, 1 minute  | 510 V AC, 1 minute   | 2300 V AC, 1 minute                     | 1400 V AC, 1 minute                    |
| ·   | Between power input terminal and ground  | Between power input terminal and ground  | Between power input terminal and ground | Between power input terminal and groun |
| Insulation resistance   | 10 M $\Omega$ or more with 500 V DC megger   |  |   |  |
| No. of occupied slots   | 2 slots  |  | 1 slot (specialized for the 3-slot and  | 6-slot basis)                          |
| Alarm output  | Relay NC contact output (Monitoring of output  | voltage: 24 V DC. 0.3 A or less)   | None                                    | ,                                      |
| Multiple power supply   | Compatible (Up to 3 units mountable on the ba  |  |   |  |
| Weight  | Approx. 360 g  |  | Approx. 180 g                           |  |
|   |  |  |   |  |
| Item  | Specifications   | T  |   |  |
| Model   | NP1S-22S   | NP1S-62S   | -                                       |  |
| Rated input voltage   | 100 to 240 V AC  | 110 V AC   | -                                       |  |
| Voltage tolerance   | 85 to 264 V AC   | 85 to 140 V AC   | -                                       |  |
| Rated frequency   | 50/60 Hz   | -  | _                                       |  |
| Dropout tolerance   | 20ms or less (Rated voltage, rated load)   | 10ms or less (Rated voltage, rated load)   | -                                       |  |
|   |  |  |   |  |
| AC waveform distortion factor   | 5% or less   | -  | -                                       |  |
| AC waveform distortion factor<br>Ripple factor tolerance  | 5% or less<br>—  | Three-phase full-wave rectification 5% or less   | -                                       |  |
|   | 5% or less<br>   | Three-phase full-wave rectification 5% or less   | -                                       |  |
| Ripple factor tolerance   | <u> </u>   | Three-phase full-wave rectification 5% or less 20 Ao-p or less (at 110 V DC)   | -                                       |  |
| Ripple factor tolerance<br>Leakage current  |  | 1 ·  | -                                       |  |
| Ripple factor tolerance<br>Leakage current  |  | 20 Ao-p or less (at 110 V DC)  | -                                       |  |
| Ripple factor tolerance<br>Leakage current  |  | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)   | -                                       |  |
| Ripple factor tolerance<br>Leakage current  | O.25mA or less     O.25mA or less     O.25mA or less     (at 100 V AC)     40 Ao-p or less (at 240 V AC)     (ambient temperature = 25°C not repeated)   | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)   | -                                       |  |
| Ripple factor tolerance<br>Leakage current<br>Inrush current  | O.25mA or less     O.25mA or less     O.25mA or less     (at 100 V AC)     40 Ao-p or less (at 240 V AC)     (ambient temperature = 25°C not repeated)     1 ms or less  | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)<br>1 ms or less   | -                                       |  |
| Ripple factor tolerance<br>Leakage current<br>Inrush current<br>Power consumption   | O.25mA or less     O.25mA or less     Ao-p or less (at 100 V AC)     40 Ao-p or less (at 240 V AC)     (ambient temperature = 25°C not repeated)     1 ms or less     220 VA or less   | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)<br>1 ms or less   | -                                       |  |
| Ripple factor tolerance<br>Leakage current<br>Inrush current<br>Power consumption<br>Rated output voltage   |  | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)<br>1 ms or less   | -                                       |  |
| Ripple factor tolerance<br>Leakage current<br>Inrush current<br>Power consumption<br>Rated output voltage<br>Output current   |  | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)<br>1 ms or less   | -                                       |  |
| Ripple factor tolerance<br>Leakage current<br>Inrush current<br>Power consumption<br>Rated output voltage<br>Output current<br>Insulation method  |  | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)<br>1 ms or less<br>90 W or less   | -                                       |  |
| Ripple factor tolerance<br>Leakage current<br>Inrush current<br>Power consumption<br>Rated output voltage<br>Output current<br>Insulation method  |  | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)<br>1 ms or less<br>90 W or less<br>1950 V AC, 1 minute  | -                                       |  |
| Ripple factor tolerance<br>Leakage current<br>Inrush current<br>Power consumption<br>Rated output voltage<br>Output current<br>Insulation method<br>Dielectric strength   | O.25mA or less     O.25mA or less     (at 100 V AC)     40 Ao-p or less (at 240 V AC)     (ambient temperature = 25°C not repeated)     1 ms or less     220 VA or less     24 V DC (23.9 to 26.1 V DC)     0 to 2.92 A     Transducer     2300 V AC, 1 minute     Between power input terminal and ground   | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)<br>1 ms or less<br>90 W or less<br>1950 V AC, 1 minute  | -                                       |  |
| Ripple factor tolerance<br>Leakage current<br>Inrush current<br>Power consumption<br>Rated output voltage<br>Output current<br>Insulation method<br>Dielectric strength<br>Insulation resistance                          | 0.25mA or less         20 Ao-p or less (at 100 V AC)         40 Ao-p or less (at 240 V AC)         (ambient temperature = 25°C not repeated)         1 ms or less         220 VA or less         24 V DC (23.9 to 26.1 V DC)         0 to 2.92 A         Transducer         2300 V AC, 1 minute         Between power input terminal and ground         10 MΩ or more with 500 V DC megger                           | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)<br>1 ms or less<br>90 W or less<br>1950 V AC, 1 minute<br>Between power input terminal and ground | -                                       |  |
| Ripple factor tolerance<br>Leakage current<br>Inrush current<br>Power consumption<br>Rated output voltage<br>Output current<br>Insulation method<br>Dielectric strength<br>Insulation resistance<br>No. of occupied slots | —         0.25mA or less         20 Ao-p or less (at 100 V AC)         40 Ao-p or less (at 240 V AC)         (ambient temperature = 25°C not repeated)         1 ms or less         220 VA or less         24 V DC (23.9 to 26.1 V DC)         0 to 2.92 A         Transducer         2300 V AC, 1 minute         Between power input terminal and ground         10 MΩ or more with 500 V DC megger         2 slots | 20 Ao-p or less (at 110 V DC)<br>(ambient temperature = 25°C not repeated)<br>1 ms or less<br>90 W or less<br>1950 V AC, 1 minute<br>Between power input terminal and ground | -                                       |  |

#### **CPU Module: NP1P**

#### Features

- · Ultra-high-speed processing Regarding the basic instructions, the CPU module carries out ultra-high-speed processing as below: SPH3000MG: 6 ns SPH3000/SPH3000MM: 9 ns SPH300: 20 ns SPH2000: 30 ns SPH200: 70 ns
- Multi CPU configuration (SPH200 excluded) Up to 8 CPUs can be configured. High-speed control is performed through load distribution.
- Redundancy (SPH300/SPH2000) 1:1 warm-standby feature and N:1 backup feature improves the system safety and reliability.
- (N:1 backup feature is supported only by SPH300.) • IEC 61131-3

Complete compliance with the IEC 61131-3 international standard language This enables results of programming to be comprehended worldwide.

#### Performance specifications

|                          |                          |           | SPH300   |  |  |  |                           | SPH300EX                  |  |
|--------------------------|--------------------------|-----------|--|--|--|--|---------------------------|---------------------------|--|
| Model                    |                          |           | NP1PS-32   | NP1PS-32R  | NP1PS-74R  | NP1PS-117R   | NP1PS-245R                | NP1PS-74D                 |  |
| Control s                | ystem                    |           | Stored program<br>Cyclic scanning system   | n (default task), periodi  | c task, event task   |  |                           |                           |  |
| I/O conne                | ection method            |           | Direct connection I/O (  | SX bus), remote I/O (D   | eviceNet, OPCN-1, and ot   | her remote I/O links)  |                           |                           |  |
| I/O contro               | ol system                |           | SX bus: Tact synchroni<br>Remote I/O link: Refres  | zation refresh.<br>sh by a remote master   | at 10-ms fixed intervals (n                                      | ot synchronized with scan)                                   |                           |                           |  |
| CPU                      |                          |           | 32-bit OS processor, 32  | 2-bit execution process  | sor  |  |                           |                           |  |
| Program                  | ming language            |           | IEC 61131-3 conformed<br>IL language (Instruction<br>SFC element (Sequent  | n List). ST language (S  | tructured Text), LD langua                                       | ge (Ladder Diagram) FBD                                      | anguage (Function Block [ | Diagram),                 |  |
| Instruction<br>execution |                          |           | 20 ns or more/instruction  | n  |  |  |                           |                           |  |
| speed                    | Applied in               | struction | 40 ns or more/instruction  | on   |  |  |                           |                           |  |
| No. of I/C               | ) points                 |           | 8,192 points   |  |  |  |                           |                           |  |
| User mer                 | mory                     |           | 97 Kwords  |  | 277 Kwords   | 491 Kwords   | 1,003 Kwords              | 277×2+6 Kwords            |  |
| Prog                     | gram memory              |           | 65,536 words   |  | 151,552 words  | 239,616 words  | 501,760 words             | 151,552×2 words           |  |
|                          |                          |           | 32,768 steps   |  | 75,776 steps   | 119,808 steps  | 250,880 steps             | 75,776×2 steps            |  |
| Data                     | a memory                 |           | 33,792 words   |  | 132,096 words  | 263,168 words  | 525,312 words             | 132,096 × 2 + 6,144 words |  |
| Available                | basic data type          | *1        | BOOL, INT, DINT, UIN   | Γ, UDINT, REAL, TIME   | , DATE, TOD, DT, STRING  | i, WORD, DWORD   |                           |                           |  |
| Number                   | of tasks                 | *2        | Default tasks (Cyclic scanning): 1     The tasks shown to the left are available to each of the basic       Periodic task : 4     Up to 4 in total |  |  |  |                           |                           |  |
| No. of PC                | OUs in program           |           | 2000 (including POUs i   | n the library)   |  |  |                           |                           |  |
|                          | User ROM card<br>(CF/SD) |           | -  | ⊖<br>CF CARD   | ⊖<br>CF CARD   | ⊖<br>CF CARD   | O<br>CF CARD              | O<br>CF CARD              |  |
|                          | USB                      | *3        | -  | 0  | 0  | 0  | 0                         | 0                         |  |
|                          | Ethernet                 | *4        | -  | -  | -  | -  | -                         | -                         |  |
| Diagnost                 | ic function              |           | Self-diagnosis (memor  | y check, ROM sum che   | eck), system configuration                                       | monitoring, module fault m                                   | onitoring                 |                           |  |
| Security                 | function                 |           | Set limits to download/  | upload of the projects,  | reference, and clear etc., I                                     | by the password.   |                           |                           |  |
| Calendar                 |                          |           | Up to 31 Dec. 2069 23<br>When multi-CPU system   |  |  |  |                           |                           |  |
| Battery b                | ackup                    | *6        | Backup range: Data m<br>Battery used: Lithium p<br>Backup time (at 25°C)<br>Replacement time (at 2   | rimary battery<br>NP1PS-32/32R: 5 ye<br>NP1PS-74R/117R: A<br>NP1PS-245R: Approx<br>NP1PS-74D: Approx.<br>25°C): within 5 minutes | ars<br>pprox. 1.3 years<br>x. 0.7 years Using<br>0.65 years make | g the optionally available la<br>s the backup time two to th | ree times longer.         |                           |  |
| Memory                   | backup by flash ı        | nemory    | Application programs, s  | system definitions, and  | I ZIP files can be saved in t                                    | the flash memory built in th                                 | e CPU.                    |                           |  |
| Memory<br>(optional)     |                          | ROM card  | Application programs, s  | system definitions, zip  | files, compressed projects                                       | and User's data can be sa                                    | ved in user ROM card (con | npact flash card).        |  |
| No. of oc                | cupied slots             |           | 1 slot   |  |  |  |                           | 2 slots                   |  |
| Internal c               | current consumpt         | ion       | 24 V DC, 200 mA or les   | SS   |  |  |                           |                           |  |
|                          |                          |           | Approx. 200 g  |  |  | Approx. 220 g  |                           | Approx. 410 g             |  |

This depends on each instruction.

\*2 \*3  $\bigcirc:$  Standard component  $\ \ -:$  Not equipped

Specifications of USB (The USB is to be used exclusively for programming support tools.)

Applicable standard of USB: USB1.1

USB connector: USB-B type (NP1PS-32R/75D/74R/117R/245R), USB-miniB type (NP1PM-48R/48E/256E/256H, NP1PU-048E/128E/256E, NP1PU2-048E/256E, NP1PU1-256NE).

- Compatible with USB and user ROM • The SPH300/SPH2000/SPH3000/SPH3000MM/ SPH3000MG of the USB and user ROM versions with separate formats are offered.
- Large-capacity battery (optionally available) • By adding the optional large-capacity battery to SPH300 (74K/117K/245K step), the memory backup time can be extended to a max. of 3.5 years (at 25°C).



| SPH2000   |  |                                 |                                | SPH200  |   |                                  |                  |
|---|--|---------------------------------|--------------------------------|---|---|----------------------------------|------------------|
| NP1PM-48R   | NP1PM-48E  | NP1PM-256E                      | NP1PM-256H                     | NP1PH-08  | NP1PH-16  | Model                            |                  |
| Stored program<br>Cyclic scanning system  | n (default task), periodio                       | c task, event task              | · ·                            |   | ·   | Control system                   |                  |
| Direct connection I/O (   | SX bus), remote I/O (D                           | eviceNet, OPCN-1, and of        | her remote I/O links)          |   |   | I/O connection method            |                  |
| SX bus: Tact synchroni<br>Remote I/O link: Refree   |  | at 10-ms fixed intervals (n     | ot synchronized with scan)     |   |   | I/O control system               |                  |
| 32-bit RISC processor   |  |                                 |                                | 16-bit OS processor, 1  | 6-bit execution processor                         | CPU                              |                  |
| IEC 61131-3 conformer<br>IL language (Instruction<br>SFC element (Sequent                               | h List), ST language (St                         | ructured Text), LD langua       | ge (Ladder Diagram) FBD lanç   | guage (Function Block Di  | agram),   | Programming language             |                  |
| 30 ns or more/instruction   | on   |                                 |                                | 70 ns or more/instructi   | on  | instruction exec                 | uction<br>aution |
| 40 ns or more/instruction   | on   |                                 |                                | 140 ns or more/instruc  | tion  | Applied instruction spee         | d                |
| 8,192 points  |  |                                 |                                |   |   | No. of I/O points                |                  |
| 193 Kwords  |  | 2,561 Kwords                    |                                | 29 Kwords   | 57 Kwords   | User memory                      |                  |
| 98,304 words  |  | 524,288 words                   |                                | 16,384 words  | 32,768 words                                      | Program memory                   |                  |
| 49,152 steps  |  | 262,144 steps                   |                                | 8,192 steps   | 16,384 steps                                      |                                  |                  |
| 99,328 words  |  | 2,098,176 words                 |                                | 13,312 words  | 25,600 words                                      | Data memory                      |                  |
| BOOL, INT, DINT, UIN  | Γ, UDINT, REAL, TIME,                            | DATE, TOD, DT, STRING           | a, WORD, DWORD                 |   |   | Available basic data type        | *1               |
| Default tasks (Cyclic so<br>Periodic task : 4<br>Event tasks : 4  | canning): 1 $\left. \right\}$ Up to 4 in total   |                                 |                                |   |   | Number of tasks                  | *2               |
| 2000 (including POUs i  | in the library)                                  |                                 |                                |   |   | No. of POUs in program           |                  |
| ⊖<br>CF CARD  | O<br>CF CARD                                     | O<br>CF CARD                    | O<br>CF CARD                   | ROM for SPH200  | ROM for SPH200                                    | User ROM card<br>(CF/SD)         | Interface        |
| 0   | 0  | 0                               | 0                              | -   | -   | USB *3                           |                  |
| -   | 0  | 0                               | O *5                           | -   | -   | Ethernet *4                      |                  |
| Self-diagnosis (memor   | y check, ROM sum che                             | ck), system configuration       | monitoring, module fault monit | oring   |   | Diagnostic function              |                  |
| Set limits to download/   | upload of the projects,                          | reference, and clear etc.,      | by the password.               |   |   | Security function                |                  |
| Up to 31 Dec. 2069 23<br>When multi-CPU syste   | 59:59 Precision: 27se<br>m is used, time is sync | c/month (when active) hronized. |                                | Up to 31 Dec. 2069 23<br>Precision: 27 seconds/   |   | Calendar                         |                  |
| <br>Backup range: Data m<br>Battery used: Lithium p<br>Backup time (at 25°C):<br>Replacement time (at 2 | rimary battery<br>5 years                        | nory, RAS area                  |                                | Backup range: Applica<br>system definition, ZIP<br>calendar IC memory, F<br>Battery used: Lithium p<br>Backup time (at 25°C):<br>Replacement time (at 2 | file, data memory,<br>AAS area<br>primary battery | Battery backup                   | *6               |
| Application programs, s   | system definitions, and                          | ZIP files can be saved in       | the flash memory built in the  | Application programs, files can be saved in th  | system definitions, and ZIP ne user ROM card.     | Memory backup by flash           | memory           |
| Application programs, s<br>in user ROM card (com  |  | iles, compressed projects       | and User's data can be saved   | Application programs, files can be saved.   | system definitions, and ZIP                       | Memory backup by user (optional) | ROM card         |
| 1 slot  |  |                                 |                                |   |   | No. of occupied slots            |                  |
| 24 V DC, 200 mA or les  | SS   |                                 |                                | 24 V DC, 85 mA or les   | S   | Internal current consum          | otion            |
| Approx. 220 g   |  |                                 |                                | Approx. 170 g   |   | Weight                           |                  |

The Ethernet interface is 10 Base-T/100 Base-TX. \*4

\*5 \*6 Ethernet interface is for equalization only during redundancy, so it is not available for general-purpose communications.

Backup time (25°C) when using the optionally available large-capacity battery: NP1PS-74R: Approx. 3.5 years NP1PS-117R: Approx. 3.5 years

NP1PS-245R: Approx. 2 years NP1PS-74D: Approx. 1.75 years



#### Performance specifications

|                          |                            | SPH3000                                     |  |   | SPH3000D                |                        |                       |                 |  |  |  |
|--------------------------|----------------------------|---|--|---|-------------------------|------------------------|-----------------------|-----------------|--|--|--|
| Model                    |                            | NP1PU-048E                                  | NP1PU-128E   | NP1PU-256E                              | NP1PU-048EZM            | NP1PU-096EZM           | NP1PU-0128EZM         | NP1PU-256EZM    |  |  |  |
| Control sys              | rstem                      | Stored program<br>Cyclic scanning sys       | Stored program<br>Cyclic scanning system (default task), periodic task, event task   |   |                         |                        |                       |                 |  |  |  |
| l/O connec               | ction method               | Direct connection I/                        | Direct connection I/O (SX bus), remote I/O (DeviceNet, OPCN-1, and other remote I/O links)   |   |                         |                        |                       |                 |  |  |  |
| I/O control system       |                            | SX bus: Tact synch<br>Remote I/O link: Re   |  | ster at 10-ms fixed inter               | vals (not synchronized  | d with scan)           |                       |                 |  |  |  |
| CPU                      |                            | 32-bit RISC process                         | sor  |   |                         |                        |                       |                 |  |  |  |
| Programm                 | ning language              |   |  | e (Structured Text), LD                 | language (Ladder Dia    | gram) FBD language (F  | unction Block Diagram | ),              |  |  |  |
| Instruction<br>execution |                            | 9 ns or more/instruc                        | ction  |   |                         |                        |                       |                 |  |  |  |
| speed                    | Applied instruction        | 8 ns or more/instruc                        | ction  |   |                         |                        |                       |                 |  |  |  |
| No. of I/O               | points                     | 8,192 points                                |  |   |                         |                        |                       |                 |  |  |  |
| SX bu                    |                            | 8,192 points                                |  |   |                         |                        |                       |                 |  |  |  |
|                          | bus0/E-SX bus1             | -   |  |   |                         | 1                      |                       |                 |  |  |  |
| Jser mem                 |                            | 353 Kwords                                  | 1,281 Kwords   | 2,561 Kwords                            | 545 Kwords              | 1,409 Kwords           | 1,473 Kwords          | 2,753 Kwords    |  |  |  |
| Progr                    | ram memory                 | 98,304 words                                | 262.144 words  | 524,288 words                           | 98,304 words            | 196,608 words          | 262,144 words         | 524,288 words   |  |  |  |
|                          |                            | 49,152 steps                                | 131,072 steps  | 262,144 steps                           | 49,152 steps            | 98,304 steps           | 131,072 steps         | 262,144 steps   |  |  |  |
| 5                        | SX bus                     | 98,304 words                                | 262,144 words  | 524,288 words                           | 98,304 words            | 196,608 words          | 262,144 words         | 524,288 words   |  |  |  |
|                          |                            | 49,152 steps                                | 131,072 steps  | 262,144 steps                           | 49,152 steps            | 98,304 steps           | 131,072 steps         | 242,144 steps   |  |  |  |
| E                        | E-SX bus0/E-SX bu          | s1 <u>-</u>                                 | ·  |   |                         |                        |                       |                 |  |  |  |
|                          |                            | -   | · · · · · · · · · · · · · · · · · · ·  |   |                         |                        |                       |                 |  |  |  |
|                          | memory                     | 263,168 words                               | 1,049,600 words  | 2,098,176 words                         | 459,776 words           | 1,246,208 words        | 1,246,208 words       | 2,294,784 words |  |  |  |
|                          | SX bus                     | 263,168 words                               | 1,049,600 words  | 2,098,176 words                         | 459,776 words           | 1,246,208 words        | 1,246,208 words       | 2,294,784 words |  |  |  |
| E                        | E-SX bus0/E-SX bu          | -   |  |   |                         |                        |                       |                 |  |  |  |
| vailable t               | basic data type            |   | JINT, UDINT, REAL, TI  | ME, DATE, TOD, DT, S                    | STRING, WORD, DWO       | ORD                    |                       |                 |  |  |  |
| Number of                | f tasks                    | Default tasks (Cyclic<br>Periodic task : 4  |  |   |                         |                        |                       |                 |  |  |  |
| No. of POI               | Us in program              | 2000 (including PO                          | Us in the library)   |   |                         |                        |                       |                 |  |  |  |
|                          | User ROM card<br>(CF/SD)   | O<br>SD memory card                         |  |   |                         |                        |                       |                 |  |  |  |
| L. L                     | JSB *                      | 2 0   |  |   |                         |                        |                       |                 |  |  |  |
| E                        | Ethernet *                 | 3 0   |  |   |                         |                        |                       |                 |  |  |  |
| Diagnostic               | c function                 | Self-diagnosis (mer                         | mory check, ROM sum  | check), system config                   | uration monitoring, mo  | dule fault monitoring  |                       |                 |  |  |  |
| Security fu              | unction                    | Set limits to downlo                        | ad/upload of the proje   | cts, reference, and clea                | ar etc., by the passwor | d.                     |                       |                 |  |  |  |
| Calendar                 |                            |   | 23:59:59 Precision: 2<br>vstem is used, time is s  | 27sec/month (when acti<br>synchronized. | ive)                    |                        |                       |                 |  |  |  |
| Battery backup           |                            | Battery used: Lithiu<br>Backup time (at 25° | Backup range: Data memory, calendar IC memory, RAS area<br>Battery used: Lithium primary battery<br>Backup time (at 25°C): 5 years<br>Replacement time (at 25°C): within 5 minutes |   |                         |                        |                       |                 |  |  |  |
| Memory b                 | ackup by flash men         | nory Application program                    | ns, system definitions,  | and ZIP files can be sa                 | ved in the flash memo   | ory built in the CPU.  |                       |                 |  |  |  |
| Memory ba<br>card (optio | ackup by user ROM<br>onal) | Application program                         | ns, system definitions,  | zip files, compressed p                 | projects and User's dat | a can be saved in user | ROM card (compact fla | ish card).      |  |  |  |
| No. of occ               | upied slots                | 1 slot                                      |  |   |                         |                        |                       |                 |  |  |  |
| nternal cu               | urrent consumption         | 24 V DC, 200 mA o                           | r less   |   |                         |                        |                       |                 |  |  |  |
|                          |                            |   |  |   |                         |                        |                       |                 |  |  |  |

\*1 This depends on each instruction.

\*2 Specifications of USB (The USB is to be used exclusively for programming support tools.)

Applicable standard of USB (The USB is to be used exclusively for programming support tools.) Applicable standard of USB: USB1.1 USB connector: USB-B type (NP1PS-32R/74D/74R/117R/245R), USB-miniB type (NP1PM-48R/48E/256E/256H, NP1PU-048EZM/096EZM/128EZM/256EZM, NP1PU2-048E/256E, NP1PU1-256NE).
 \*3 The Ethernet interface is 10 Base-T/100 Base-TX (SPH3000, SPH3000PN, SPH3000PN, SPH3000MM, SPH3000MG)

#### Performance specifications

|   |                                       | SPH3000MM   |  |  |  |  |  |
|---|---------------------------------------|---|--|--|--|--|--|
| Vodel   |                                       | NP1PU2-048E   | NP1PU2-256E  |  |  |  |  |
| Model<br>Control system                       |                                       |   |  |  |  |  |  |
|   |                                       | Stored program<br>Cyclic scanning system (default task), periodic task, event task  |  |  |  |  |  |
| I/O connection                                | on method                             | Direct connection I/O (SX bus), remote I/O (T-links, Device   | eNet, PROFIBUS, and other remote I/O links)  |  |  |  |  |
| I/O control sys                               | ystem                                 | SX bus: SX bus tact synchronization refresh.<br>E-SX bus: E-SX bus tact synchronization refresh.<br>Remote I/O link: Refresh by a remote master at 10-ms fixed intervals (not synchronized with scan) |  |  |  |  |  |
| CPU   |                                       | 32-bit RISC processor × 3   |  |  |  |  |  |
| Programming                                   |                                       | SFC element (Sequential Function Chart)   | xt), LD language (Ladder Diagram) FBD language (Function Block Diagram),   |  |  |  |  |
| Instruction Sequence<br>execution instruction |                                       | 9 ns or more/instruction  |  |  |  |  |  |
| speed   | Applied instruction                   | 8 ns or more/instruction  |  |  |  |  |  |
| No. of I/O poir                               |                                       | 139,264 points  |  |  |  |  |  |
| SX bus  |                                       | 8,192 points  |  |  |  |  |  |
| E-SX bus                                      | us0/E-SX bus1                         | 65,536/65,536 points  |  |  |  |  |  |
| User memory                                   | У                                     | 1234.5 Kwords   | 5650.5 Kwords  |  |  |  |  |
| Program                                       | n memory                              | 196,608 words   | 1,048,576 words  |  |  |  |  |
|   |                                       | 98,304 steps  | 524,288 steps  |  |  |  |  |
| E-S.  | SX bus0/E-SX bus1                     | 98,304/98,304 words   | 524,288/524,288 words  |  |  |  |  |
|   |                                       | 49,152/49,152 steps   | 262,144/262,144 steps  |  |  |  |  |
| Data mer                                      | emory                                 | 1,067,520 words   | 4,737,536 words  |  |  |  |  |
| Con   | mmon multi                            | 132,608 words   | 132,608 words  |  |  |  |  |
| E-S.  | SX bus0/E-SX bus1                     | 467,456/467,456 words   | 2,302,464/2,302,464 words  |  |  |  |  |
| Available basi                                |                                       | BOOL, INT, DINT, UINT, UDINT, REAL, TIME, DATE, TOD   | D, DT, STRING, WORD, DWORD   |  |  |  |  |
| Number of tas                                 | asks *2                               | E-SX bus0/E-SX bus1<br>Default tasks (Cyclic scanning): 1<br>Periodic task : 4<br>Event tasks : 4 Up to 4 in total  |  |  |  |  |  |
| No. of POUs i                                 | in program                            | 2000 (including POUs in the library)  |  |  |  |  |  |
| Interface Use                                 | 1 0                                   | SD memory card  |  |  |  |  |  |
| USE   |                                       | miniB connector x 1 port (for programming tool connection   | 1)   |  |  |  |  |
| Ethe  | nernet                                | 10BASE-T/100BASE-TX (auto-negotiation, AUTO MDI/ME  |  |  |  |  |  |
| Diagnostic fur                                | Inction                               | Self-diagnosis (memory check, ROM sum check), system  |  |  |  |  |  |
| Security funct                                |                                       | Set limits to downloading/uploading of the projects, referen  |  |  |  |  |  |
| Calendar                                      |                                       | Up to 31 Dec. 2069 23:59:59 Precision: 27sec/month (wh<br>When multi-CPU system is used, time is synchronized.  |  |  |  |  |  |
| Battery backup                                |                                       |   | Backup range: Data memory, calendar IC memory, RAS area<br>Battery used: Lithium primary battery<br>Backup time (at 25°C): 5 years |  |  |  |  |
| Memory back                                   | kup by flash memory                   | Application programs, system definitions, and ZIP files car   | n be saved in the flash memory built in the CPU.   |  |  |  |  |
|   | kup by user ROM                       | Application programs, system definitions, zip files, compre   |  |  |  |  |  |
| No. of occupie                                | · · · · · · · · · · · · · · · · · · · | 2 slots   |  |  |  |  |  |
|   | ent consumption                       | 24 V DC 360 mA  |  |  |  |  |  |
| Neight  | - p not                               | Approx. 420 g   |  |  |  |  |  |
|   |                                       |   |  |  |  |  |  |

\*1 This depends on each instruction.
 \*2 SPH3000MM contains one SX bus and two E-SX buses. The number of tasks available for each of these buses is shown in the table.
 \*3 Applicable standard of USB: USB1.1

#### Performance specifications

|                             |                         | SPH3000MG   | SPH5000H   | BACnet MS/TP CPU*   |
|-----------------------------|-------------------------|---|--|---|
| Model                       |                         | NP1PU1-256NE  | NP1PU1-512H  | NP1PUBM-048C  |
| Control syste               | tem                     | Stored program<br>Cyclic scanning system (default task), periodic task  | a, event task  |   |
| I/O connection              | ion method              | Direct connection I/O (SX bus), remote I/O (T-link, I   | Direct connection I/O (SX bus), remote I/O (T-links)<br>DeviceNet, PROFIBUS, and other remote I/O links  |   |
| I/O control s               | system                  | SX bus: SX bus tact synchronization refresh.<br>E-SX bus: E-SX bus tact synchronization refresh.<br>Remote I/O link: Refresh by a remote master at 10-                                | SX bus: Tact synchronization refresh.<br>Remote I/O link: Refresh by a remote master at 10<br>ms fixed intervals (not synchronized with scan)                    |   |
| CPU                         |                         | 32-bit RISC processor × 2   |  | 32-bit RISC processor   |
| Programmin                  | ng language             | IEC 61131-3 conformed<br>IL language (Instruction List), ST language (Structu<br>language (Function Block Diagram),<br>SFC element (Sequential Function Chart)                        | red Text), LD language (Ladder Diagram) FBD  |   |
| Instruction<br>execution    | Sequence instruction    | 6 ns or more/instruction  |  | 9 ns or more/instruction  |
| speed                       | Applied<br>instruction  | 5 ns or more/instruction  | 1  | 8 ns or more/instruction  |
| No. of I/O po               | oints                   | 73,727 points   | 65,536 points  | 8,192 points  |
| SX bus                      | 3                       | 8,192 points  | -  | 8,192 points  |
| E-SX bus0/E-SX bus1         |                         | 65,536 points   |  | -   |
| User memor                  | ry                      |   |  |   |
| Program                     | m memory                | 524,288 words   | 1,048,576 words  | 98,304 words  |
|                             |                         | 262,144 steps   | 524,288 steps  | 49,152 steps  |
| SX                          | X bus                   | -   | 98,304 words   |   |
|                             |                         | Note) There are no tasks synchronized with the SX bus.  | 49,152 steps   |   |
| E-9                         | SX bus0/E-SX bus1       | 524,288 words   | 1,048,576 words  | -   |
|                             |                         | 262,144 steps   | 524,288 steps  | -   |
| Data me                     | nemory                  |   |  |   |
| Mu                          | ulti                    | 132,096 words -   |  | 263,168 words   |
| E-S                         | SX bus0/E-SX bus1       | 2,302,464 words   | 2,302,464 words  | -   |
|                             | asic data type *1       | BOOL, INT, DINT, UINT, UDINT, REAL, TIME, DAT   |  |   |
| Number of ta                | asks *2                 | E-SX bus<br>Default tasks<br>(Cyclic scanning): 1<br>Periodic task : 4 ↓ Up to 4<br>Event tasks : 4 ↓ in total  | SX bus<br>Default tasks<br>(Cyclic scanning): 1<br>Periodic task : 4   |   |
| No. of POUs                 | s in program            | 2000 (including POUs in the library)  |  | 2000 (including POUs in the library)  |
| Interface Us                | ser ROM card            | SD memory card  |  |   |
| US                          | SB *3                   | miniB connector x 1 port (for programming tool con  | nection)   |   |
| Eth                         | hernet                  | 10BASE-T/100BASE-TX (auto-negotiation, AUTO<br>MDI/MDI-X compatible)  | 10BASE-T, 100BASE-TX, 1000BASE-T   | -   |
| Diagnostic fu               | unction                 | Self-diagnosis (memory check, ROM sum check), s monitoring  | system configuration monitoring, module fault  |   |
| Security fund               | ction                   | Set limits to downloading/uploading of the projects,  | reference, and clear etc., with a password.  |   |
| Calendar                    |                         | Up to 31 Dec. 2069 23:59:59 Precision: 27sec/mor<br>When multi-CPU system is used, time is synchroniz   |  |   |
| Battery backup              |                         | Backup range: Data memory, calendar IC memory,<br>RAS area<br>Battery used: Lithium primary battery<br>Backup time (at 25°C): 5 years<br>Replacement time (at 25°C): within 5 minutes | Backup range: Calendar IC memory *4<br>Battery used: Lithium primary battery<br>Backup period (at 25°C): 5 years<br>Replacement time: within 5 minutes (at 25°C) | Backup range: Data memory, calendar IC memory,<br>RAS area<br>Battery used: Lithium primary battery<br>Backup time (at 25°C): 5 years<br>Replacement time (at 25°C): within 5 minutes |
| Memory bac                  | ckup by flash memory    | Application programs, system definitions, and ZIP f CPU.  | iles can be saved in the flash memory built in the   |   |
| Memory bac<br>card (optiona | ckup by user ROM<br>al) | Application programs, system definitions, ZIP files,  | compressed projects, and user data can be saved.   |   |
| No. of occup                |                         | 2 slots   |  | 1 slot  |
| Internal curre              | rent consumption        | 24 V DC 650 mA  | 24 V DC 600 mA   | 24 V DC, 380 mA or less   |
|                             |                         | Approx. 450 g   | Approx. 600 g  | Approx. 220 g   |

\*Only for Japan's doemestic market

\*1 This depends on each instruction.
 \*2 One SX bus and two E-SX buses. The number of tasks available for each of these buses is shown in the table.

Note) There are no tasks synchronized with the SX bus.

\*3 Applicable standard of USB: USB1.1

\*4 Retain memory and RAS information \*5 This indicates the current value used to charge the module's built-in capacitor when turned on. During steady-state operation, it is 200 mA or less.



| Model         MP1PA1-089E         MP1PA1-28E         MP1PA1-256E         MP1PA1-512E           Control system         Cyclic scanning system (default task), periodic task, event task         Second task periodic task p  |  | SPH5000M  |  |                                       |                                |             |  |  |  |
|---|--|---|--|---------------------------------------|--------------------------------|-------------|--|--|--|
| <th colspace="" of="" seco<="" second="" td="" the=""><td>Model</td><td>NP1PA1-096E</td><td>NP1PA1-128E</td><td>NP1PA1-256E</td><td>NP1PA1-512E</td></th>   | <td>Model</td> <td>NP1PA1-096E</td> <td>NP1PA1-128E</td> <td>NP1PA1-256E</td> <td>NP1PA1-512E</td> | Model   | NP1PA1-096E                              | NP1PA1-128E                           | NP1PA1-256E                    | NP1PA1-512E |  |  |  |
| bDC centrol system       EX bus: SX bus tact synchronization aftereb or refresh or profesh by a CPU module at 10-ms fixed intervals (mot synchronized with scan)         Tack synchronization bus specification       Ether the SX bus or F-SX bus is specified as the synchronization bus for the task.         OPU       32-bit RISC processor, dual core         Programming language       IEC 61131-3 conformed (I. language (Instruction Lisk), STC element (Gegental Function Chart)         Instruction       4 no or more/instruction, ADD UDINT 4 no or more/instruction         No. of I/O points       73.728 points         SX bus: SX bas (Steppen Steppen Ste  | Control system   |   |  |                                       |                                |             |  |  |  |
| IS A to bis icit synchronization refres by a refore by a CPU module at I-OP Module | I/O connection method  | Direct connection I/O (SX bus/ E-SX b   | us), remote I/O (T-links, DeviceNet, PR  | OFIBUS, and other remote I/O links)   |                                |             |  |  |  |
| PipedFunction2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-2-   | I/O control system   | E-SX bus: E-SX bus tact synchronizat  | ion refresh or refresh by a CPU module   | at 10-ms fixed intervals              |                                |             |  |  |  |
| Programming larguage       EC 6113-3 contermed<br>the larguage (function Chart)       EC 6113-3 contermed<br>the larguage (function Chart)         Instruction<br>security page       Sequence<br>sequential Function Chart)       4 ns or more/instruction<br>applied<br>E-SX bus       4 ns or more/instruction<br>applied<br>E-SX bus       4 ns or more/instruction<br>security page       57.28 points         Imative<br>Status       51.28 points       51.28 points       51.28 points         Imative<br>E-SX bus       65.536 points       512 Ksteps       512 Ksteps         Imative<br>E-SX bus       65.536 points       512 Ksteps       512 Ksteps         Imative<br>E-SX bus       65.536 points       512 Ksteps       512 Ksteps         Imative<br>Program memory (shared by<br>two application control units)       96 Ksteps       128 Ksteps       256 Ksteps       512 Ksteps         Imative<br>Maxibile back dat type       2       800L, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRINO, WORD, DWORD       Max. 6,144 kW         Available back dat type       200 (including POLS in the Ibrary)       US       So memory card       US         Imater Comparison       200 (including POLS in the Ibrary)       So memory card, SDH oemory card       US       US       US         Imater Comparison       200 (including POLS in the Ibrary)       Security function (150 POS arming) in periodical size is periodical size is periodical size is periodical size is periodical siz   |  | Either the SX bus or E-SX bus is spec   | ified as the synchronization bus for the | task.                                 |                                |             |  |  |  |
| Instruction       LL anguage (Instruction Char)         Instruction       4 ns or more/instruction         Applied<br>instruction       4 ns or more/instruction         Applied<br>instruction       LD WORD 0.9 ns or more/instruction         Applied<br>instruction       7.7.28 points         Sequent 81 Purpose       7.7.28 points         Sequent 82 Purpose       8.182 points         E-SX bus       8.182 points         E-SX bus       65.586 points         Data memory       *1         Max. 1,840 kW       Max. 1,840 kW         Variable basis data type       '2         Data memory       *1         Move of POUs in program       2000 (including POUs in the library)         Vinter 42       SD memory card         User ROM card       SD memory card         User ROM card       SD memory card         User ROM card       SD memory card, SDHC memory card         User V servit Sus       Self-diagnosis (memory cards, SDHC memory card         User V servit Sus       Sulf-diagnosis (memory cards, SDHC memory card         Security function       Self-diagnosis (memory cards, SDHC memory card         User Sub security function       Self-diagnosis (memory cards, SDHC memory card         User Sub security (SUMC memory cards)       User SUM secur  | CPU  | 32-bit RISC processor, dual core  |  |                                       |                                |             |  |  |  |
| execution performance         Instruction         Applied<br>instruction         Instruction           Applied<br>instruction         LD WORD 0.9 ns or more/instruction, ADD UDINT 4 ns or more/instruction           Not of Do prints         73.728 points           State memory         73.728 points           E-SUX         55.36 points           Image: State memory         512 Ksteps           Image: State memory         96 Ksteps           Image: State memory         10 Max. 1,840 KW           Image: State memory         90 Cl, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRING, WORD, DWORD           Not of POUse memory         90 Cl, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRING, WORD, DWORD           Not of POUse memory         90 Cl, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRING, WORD, DWORD           Not of POUse memory         90 Cl, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRING, WORD, DWORD           Not of POUse memory         90 Cl, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRING, WORD, DWORD           Not of POUse memory         90 Cl, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRING, WORD, DWORD           Not of POUse memory         90 Cl, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRING, WORD, DWORD           Not of POUse memory         90 Cl, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRING, WORD, DWORD           Not of POUse memory   | Programming language   | IL language (Instruction List), ST langu  | uage (Structured Text), LD language (La  | adder Diagram), FBD language (Functic | on Block Diagram), SFC element |             |  |  |  |
| Instruction         T 3,728 points           State         State <th rowsp<="" td=""><td></td><td>4 ns or more/instruction</td><td></td><td></td><td></td></th>   | <td></td> <td>4 ns or more/instruction</td> <td></td> <td></td> <td></td>                          |   | 4 ns or more/instruction                 |                                       |                                |             |  |  |  |
| SX bus       8,192 points         E-SX bus       65,536 points         User       memory         Program memory (shared by<br>two application control units)       96 Ksteps       128 Ksteps       256 Ksteps       512 Ksteps         Data memory       1       Max. 1,840 kW       Max. 3,184 kW       Max. 6,144 kW         Available basic data type       *2       BOCI, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRING, WORD, DWORD       Image: Control units)         Number of tasks       *3       Default tasks (Cyclic scanning): 1<br>Periodic tasks : 4 ) lo tol 4<br>Event tasks : 4 ) lo tol 4       Image: Control units)       Image: Control units)         No. of POUs in program       2000 (including POUs in the library)       Image: Control units)       Image: Control units)         Interface       User ROM card       SD memory card, SDHC memory card       Image: Control units)       Image: Control units)         Security function       Set limits to downloading/uploading of the projects, reference, and clear etc., with a password.       Image: Control units)       Image: Control units)         Security function       Set limits to downloading/uploading of the projects, reference, and clear etc., with a password.       Image: Control units)       Image: Control units)         Battery backup by flash memory       Sa Backy passe: Jass 59 Freision: Zase/month (when active)       Image: Sa Backy passe: Sa Backy passe:  |  | LD WORD 0.9 ns or more/instruction,   | ADD UDINT 4 ns or more/instruction       |                                       |                                |             |  |  |  |
| E-SX bus       65,596 points         User memory       65,596 points         Program memory (shared by<br>two application control units)       96 Kstps       128 Ksteps       256 Ksteps       512 Ksteps         Data memory       10       Max. 1,840 kW       Max. 3,184 kW       Max. 6,144 kW         Available basic data type       '2       BOOL, INT, UINT, UDINT, DENT, TURE, DATE, TOD, DT, STRING, WORD, DWORD       Max. 6,144 kW         Available basic data type       '2       BOOL, INT, UINT, DINT, UDINT, DELT, DATE, TOD, DT, STRING, WORD, DWORD       Max. 6,144 kW         Number of tasks       'a       'Default tasks (Cyclic scanning): 1<br>Periodic tasks: 4 ') in total       Periodic tasks: 4 ') up to 4<br>Event tasks: 4 ') in total       Stresset         No. of POUs in program       2000 (including POUs in the library)       Stresset       Stresset       Stresset         No. of POUs the render       2000 (including POUs in the library)       Stresset       Stresset       Stresset         No. of POUs the render       2000 (including POUs in the library)       Stresset       Stresset       Stresset         No. of POUs the render       2000 (including POUs in the library)       Stresset       Stresset       Stresset         No. of POUs the render       2000 (including POUs and the render, stresset       Stresset       Stresset       Stresset   | No. of I/O points  | 73,728 points   |  |                                       |                                |             |  |  |  |
| User memory       Forgram memory (shared by 96 Ksteps       128 Ksteps       256 Ksteps       512 Ksteps         Data memory       1       Max. 1,840 KW       Max. 3,184 KW       Max. 6,144 kW         Available basic data type       2       BOOL, INT, UINT, DINT, D   | SX bus   | 8,192 points  |  |                                       |                                |             |  |  |  |
| Program memory (shared by op/lication control units)       96 Ksteps       128 Ksteps       256 Ksteps       512 Ksteps         Data memory       *1       Max. 1,840 kW       Max. 3,184 kW       Max. 6,144 kW         Available basic data type       2       BOCL, INT, UINT, DINT, UDINT, REAL, TIME, DATE, TOD, DT, STRING, WORD, DWORD       Wax. 6,144 kW         Number of tasks       *3       Default tasks (Cyclic scanning): 1<br>Periodic tasks: 4 ) Up to 4<br>Event tasks: 4 ) Up to 4       Event tasks: 4 ) Up to 4         Interface       User ROM card       SD memory card       SD memory card         USB       *4       minB connector x 1 port (for programming tool connection)       Interface         User ROM card       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring         Security function       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring       Self-diagnosis (memory backup by flash memory         Satary user: Lithium primary battery<br>Replacement time (at 25°C): System is used, time is synchronized.       Seves application programs, system definitions, and zip files in flash memory.         Satery backup by glash memory<br>and nonvolatile RAM       Application programs, system definitions, zip files, compressed projects and User's data can be saved.         No. of occupied slots  | E-SX bus   | 65,536 points   |  |                                       |                                |             |  |  |  |
| Image         Image <t< td=""><td>User memory</td><td></td><td colspan="7"></td></t<>   | User memory  |   |  |                                       |                                |             |  |  |  |
| Available basic data type       *2       BOOL, INT, UINT, DINT, REAL, TIME, DATE, TOD, DT, STRING, WORD         Number of tasks       *3       Default tasks (Cyclic scanning): 1<br>Periodic tasks: 4       Up to 4<br>Event tasks: 4: 1 Up to 4         No. of POUs in program       2000 (including POUs in the library)       2000 (including POUs in the library)         Interface       User ROM card       SD memory card, SDHC memory card         USB       *4       miniB connector x 1 port (for programming tool connection)         Ethernet       100BASE-TX/1000BASE-T         Diagnostic function       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring         Security function       Self imits to downloading/uploading of the projects, reference, and clear etc., with a password.         Calendar       Up to 31 Dec. 2069 23:59:59 Precision: 27sec/month (when active)<br>When multi-CPU system is used, time is synchronized.         Battery backup       *5       Backup range: Calendar IC memory<br>Battery used: Lithium primary battery<br>Replacement time (at 25°C): within 5 minutes<br>Backup time (at 25°C): System definitions, and zip files in flash memory.         Memory backup by flash memory<br>and nonvolatile RAM       Saves application programs, system definitions, zip files, compressed projects and User's data can be saved.         No. of occupied slots       1 slot         Internal current consumption       ta slot         Internal current consumpti  |  | y 96 Ksteps   | 128 Ksteps                               | 256 Ksteps                            | 512 Ksteps                     |             |  |  |  |
| Number of tasks       *3       Default tasks (Cyclic scanning): 1         Periodic tasks : 4       ) Up to 4         Event tasks : 4       ) Up to 4         Event tasks : 4       ) Up to 4         Interface       User ROM card       SD memory card, SDHC memory card         USB       *4       miniB connector x 1 port (for programming tool connection)         Ethernet       100BASE-TX/100BASE-T         Diagnostic function       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring         Security function       Set limits to downloading/uploading of the projects, reference, and clear etc., with a password.         Calendar       Up to 31 Dec. 2069 23:59:59 Precision: 27sec/month (when active)         When multi-CPU system is used, time is synchronized.       Backup range: Calendar IC memory Backup targe: Calendar IC memory, RAS, and logging and trace settings in nonvolatile R   | Data memory *1   | Max. 1,840 kW   |  | Max. 3,184 kW                         | Max. 6,144 kW                  |             |  |  |  |
| Periodic tasks: 4 Up to 4 Event tasks : 4 Up to 5 D memory card, SDHC memory card         Diagnostic function       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring         Security function       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring         Security function       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring         Security function       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring         Security function       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring         Battery back up task memory       Set limits to downloading/uploading of the projects, reference, and clear etc., with a password.         Calendar       Up to 31 Dec. 2069 23:50:59 Precision: 27sec/month (when active) When multi-CPU system is used, time is synchronized.         Battery back up task memory       Backup range: Calendar IC memory Battery used. Limit m   | Available basic data type *2   | BOOL, INT, UINT, DINT, UDINT, REAL  | ., TIME, DATE, TOD, DT, STRING, WC       | RD, DWORD                             |                                |             |  |  |  |
| Interface         User ROM card         SD memory card, SDHC memory card           USB         *4         miniB connector x 1 port (for programming tool connection)           Ethernet         100BASE-TX/1000BASE-T           Diagnostic function         Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring           Security function         Set limits to downloading/uploading of the projects, reference, and clear etc., with a password.           Calendar         Up to 31 Dec. 2069 23:59:59 Precision: 27sec/month (when active)<br>When multi-CPU system is used, time is synchronized.           Battery backup         *5         Backup range: Calendar IC memory<br>Replacement time (at 25°C): within 5 minutes<br>Backup time (at 25°C): system definitions, and zip files in flash memory.<br>Stores retained memory, RAS, and logging and trace settings in nonvolatile RAM.           Memory backup by user ROM<br>card (optional)         Application programs, system definitions, zip files, compressed projects and User's data can be saved.           No. of occupied slots         1 slot           Internal current consumption         1 slot   | Number of tasks *3   | Periodic tasks : 4 ) Up to 4  |  |                                       |                                |             |  |  |  |
| USB       *4       miniB connector x 1 port (for programming tool connection)         Ethernet       100BASE-TX/1000BASE-T         Diagnostic Function       Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring         Security function       Set limits to downloading/uploading of the projects, reference, and clear etc., with a password.         Calendar       Up to 31 Dec. 2069 23:59:59 Precision: 27sec/month (when active)         When multi-CPU system is used, time is synchronized.       Battery backup zote: Calendar IC memory         Battery backup by tash       Battery calendar IC memory         Battery backup by flash memory       Sates publication programs, system definitions, and zip files in flash memory.         Stores retained memory, RAS, and logging and trace settings in nonvolatile RAM.       Saves application programs, system definitions, zip files, compressed projects and User's data can be saved.         No. of occupied slots       1 slot         Internal currut consumption *6       24 V DC, 700 mA or less  | No. of POUs in program   | 2000 (including POUs in the library)  |  |                                       |                                |             |  |  |  |
| Ethernet         100BASE-TX/1000BASE-T           Diagnostic function         Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring           Security function         Set limits to downloading/uploading of the projects, reference, and clear etc., with a password.           Calendar         Up to 31 Dec. 2069 23:59:59 Precision: 27sec/month (when active)<br>When multi-CPU system is used, time is synchronized.           Battery backup         *5         Backup range: Calendar IC memory<br>Battery used: Lithium primary battery<br>Replacement time (at 25°C): within 5 minutes<br>Backup time (at 25°C): or system         Saves application programs, system definitions, and zip files in flash memory.           Memory backup by glash memory<br>and nonvolatile RAM         Saves application programs, system definitions, zip files, compressed projects and User's data can be saved.           No. of occupied slots         1 slot           Internal current consumption *6         24 V DC, 700 mA or less  | Interface User ROM card  | SD memory card, SDHC memory card  | 1  |                                       |                                |             |  |  |  |
| Diagnostic function         Self-diagnosis (memory check, ROM sum check), system configuration monitoring, module fault monitoring           Security function         Set limits to downloading/uploading of the projects, reference, and clear etc., with a password.           Calendar         Up to 31 Dec. 2069 23:59:59 Precision: 27sec/month (when active)<br>When multi-CPU system is used, time is synchronized.           Battery backup         *5         Backup range: Calendar IC memory<br>Battery used: Lithium primary battery<br>Replacement time (at 25°C): within 5 minutes<br>Backup time (at 25°C): Syears           Memory backup by flash memory<br>and nonvolatile RAM         Saves application programs, system definitions, and zip files in flash memory.<br>Stores retained memory, RAS, and logging and trace settings in nonvolatile RAM.           Memory backup by user ROM<br>card (optional)         Application programs, system definitions, zip files, compressed projects and User's data can be saved.           No. of occupied slots         1 slot           Internal current consumption *6         24 V DC, 700 mA or less  | USB *4   | miniB connector x 1 port (for programm  | ning tool connection)                    |                                       |                                |             |  |  |  |
| Security function       Set limits to downloading/uploading of the projects, reference, and clear etc., with a password.         Calendar       Up to 31 Dec. 2069 23:59:59 Precision: 27sec/month (when active)<br>When multi-CPU system is used, time is synchronized.         Battery backup       *5       Backup range: Calendar IC memory<br>Battery used: Lithium primary battery<br>Replacement time (at 25°C): within 5 minutes<br>Backup time (at 25°C): within 5 minutes<br>Backup time (at 25°C): 5 years         Memory backup by flash memory<br>and nonvolatile RAM       Saves application programs, system definitions, and zip files in flash memory.<br>Stores retained memory, RAS, and logging and trace settings in nonvolatile RAM.         Memory backup by user ROM<br>card (optional)       Application programs, system definitions, zip files, compressed projects and User's data can be saved.         No. of occupied slots       1 slot         Internal current consumption *6       24 V DC, 700 mA or less   | Ethernet   | 100BASE-TX/1000BASE-T   |  |                                       |                                |             |  |  |  |
| Calendar       Up to 31 Dec. 2069 23:59:59 Precision: 27sec/month (when active)         When multi-CPU system is used, time is synchronized.         Battery backup       *5         Backup range: Calendar IC memory<br>Battery used: Lithium primary battery<br>Replacement time (at 25°C): within 5 minutes<br>Backup time (at 25°C): within 5 minutes<br>Backup time (at 25°C): 5 years         Memory backup by flash memory<br>and nonvolatile RAM       Saves application programs, system definitions, and zip files in flash memory.<br>Stores retained memory, RAS, and logging and trace settings in nonvolatile RAM.         Memory backup by user ROM<br>card (optional)       Application programs, system definitions, zip files, compressed projects and User's data can be saved.         No. of occupied slots       1 slot         Internal current consumption *6       24 V DC, 700 mA or less   | Diagnostic function  | Self-diagnosis (memory check, ROM s   | sum check), system configuration monit   | oring, module fault monitoring        |                                |             |  |  |  |
| Men multi-CPU system is used, time is synchronized.         Battery backup       Sackup range: Calendar IC memory<br>Battery used: Lithium primary battery<br>Replacement time (at 25°C): within 5 minutes<br>Backup time (at 25°C): within 5 minutes<br>Backup time (at 25°C): system         Memory backup by flash memory<br>and nonvolatile RAM       Saves application programs, system definitions, and zip files in flash memory.<br>Stores retained memory, RAS, and logging and trace settings in nonvolatile RAM.         Memory backup by user ROM<br>card (optional)       Application programs, system definitions, zip files, compressed projects and User's data can be saved.         No. of occupied slots       1 slot         Internal current consumption *6       24 V DC, 700 mA or less  | Security function  | Set limits to downloading/uploading of  | the projects, reference, and clear etc., | with a password.                      |                                |             |  |  |  |
| Battery used: Lithium primary battery<br>Replacement time (at 25°C): Syithin 5 minutes<br>Backup time (at 25°C): Syears         Memory backup by flash memory<br>and nonvolatile RAM       Saves application programs, system definitions, and zip files in flash memory.<br>Stores retained memory, RAS, and logging and trace settings in nonvolatile RAM.         Memory backup by user ROM<br>card (optional)       Application programs, system definitions, zip files, compressed projects and User's data can be saved.         No. of occupied slots       1 slot         Internal current consumption *6       24 V DC, 700 mA or less   | Calendar   |   |  |                                       |                                |             |  |  |  |
| and nonvolatile RÁM       Stores retained memory, RAS, and logging and trace settings in nonvolatile RAM.         Memory backup by user ROM card (optional)       Application programs, system definitions, zip files, compressed projects and User's data can be saved.         No. of occupied slots       1 slot         Internal current consumption *6       24 V DC, 700 mA or less   | Battery backup *5  | Battery used: Lithium primary battery<br>Replacement time (at 25°C): within 5 r | ninutes                                  |                                       |                                |             |  |  |  |
| card (optional)     Internal current consumption *6       24 V DC, 700 mA or less   |  |   |  |                                       |                                |             |  |  |  |
| Internal current consumption *6 24 V DC, 700 mA or less   |  | Application programs, system definitio  | ns, zip files, compressed projects and l | Jser's data can be saved.             |                                |             |  |  |  |
|   | No. of occupied slots  | 1 slot  |  |                                       |                                |             |  |  |  |
| Weight Approx. 420 g  | Internal current consumption *6  | 24 V DC, 700 mA or less   |  |                                       |                                |             |  |  |  |
|   | Weight   | Approx. 420 g   |  |                                       |                                |             |  |  |  |

\*1 This is the total of the shared and private areas used by two application control units.

\*1 This is the total of the shared and private areas used by two application control units.
\*2 This depends on each instruction.
\*3 The periodic task must be an integer multiple of the bus tact specified for synchronization. If it is not, an error will occur and the task will not run.
\*4 Applicable standard of USB: USB2.0
\*5 Stored data, such as retained memory and RAS information, is automatically backed up to the CPU's built-in nonvolatile memory when the SPH5000M is powered off. This means that there is no need for battery backup for those memories. However, if calendar memory backup is required, please purchase an optional battery (NP8P-BT).
\*6 The SPH5000M must be installed in an EP bus-compatible slot on an EP bus-compatible baseboard.

#### Performance specifications

|                                 |  | SPH5000EC   |                                    |                                     |   |  |  |  |
|---------------------------------|--|---|------------------------------------|-------------------------------------|---|--|--|--|
| Vodel                           |  | NP1PA1C-096E  | NP1PA1C-128E                       | NP1PA1C-256E                        | NP1PA1C-512E                              |  |  |  |
| Control system                  | I                                      | Stored program<br>Cyclic scanning system (default task), periodic task, event task  |                                    |                                     |   |  |  |  |
| /O connection                   | method                                 | Direct connection I/O (SX bus / Ether   | CAT), remote I/O (T-links, Device  | Net, PROFIBUS, and other remote     | e I/O links)                              |  |  |  |
| /O control syst                 | iem                                    | EtherCAT: EtherCAT tact synchroniza   | tion refresh                       |                                     |   |  |  |  |
|                                 |  | SX bus: Refresh by a CPU module at fixed intervals  |                                    |                                     |   |  |  |  |
|                                 |  | Remote I/O link: Refresh by a remote  | master at fixed intervals (not syr | nchronized with scan)               |   |  |  |  |
| Fask synchroni                  | zation bus                             | Only EtherCAT can be selected   |                                    |                                     |   |  |  |  |
| CPU                             |  | 32-bit RISC processor, dual core  |                                    |                                     |   |  |  |  |
| Programming la                  | anguage                                | IEC 61131-3 conformed<br>IL language (Instruction List), ST lang<br>(Sequential Function Chart)   | uage (Structured Text), LD langu   | age (Ladder Diagram), FBD langua    | age (Function Block Diagram), SFC element |  |  |  |
| nstruction<br>execution         | Sequence instruction                   | 4 ns or more/instruction  |                                    |                                     |   |  |  |  |
| speed                           | Applied instruction                    | LD WORD 0.9 ns or more/instruction,   | ADD UDINT 4 ns or more/instru      | ction                               |   |  |  |  |
| No. of I/O point                | ts                                     | 73,728 points   |                                    |                                     |   |  |  |  |
| SX bus                          |  | 8,192 points  |                                    |                                     |   |  |  |  |
| EtherCAT                        |  | 65,536 points   |                                    |                                     |   |  |  |  |
| Jser memory                     |  |   |                                    |                                     |   |  |  |  |
|                                 | nemory (shared by ation control units) | 96Kstep   | 128Kstep                           | 256Kstep                            | 512Kstep                                  |  |  |  |
| Data mem                        | ory *1                                 | Max. 1,840 kW   |                                    | Max. 3,184 kW                       | Max. 6,144 kW                             |  |  |  |
| vailable basic                  | data type *2                           | BOOL, INT, UINT, DINT, UDINT, REA   | L, TIME, DATE, TOD, DT, STRIN      | IG, WORD, DWORD                     | I.  |  |  |  |
| Number of task                  | (s *3                                  | Default tasks (Cyclic scanning): 1<br>Periodic tasks : 4 \ Up to 4<br>Event tasks : 4 ∫ in total  |                                    |                                     |   |  |  |  |
| lo. of POUs in                  | program                                | 2000 (including POUs in the library)  |                                    |                                     |   |  |  |  |
| nterface User                   | ROM card                               | SD memory card, SDHC memory card  | d                                  |                                     |   |  |  |  |
| USB                             | *4                                     | miniB connector x 1 port (for program   | ming tool connection)              |                                     |   |  |  |  |
| Ethe                            | rnet                                   | 100BASE-TX/1000BASE-T   |                                    |                                     |   |  |  |  |
| Diagnostic fund                 | ction                                  | Self-diagnosis (memory check, ROM   | sum check), system configuratio    | n monitoring, module fault monitori | ng  |  |  |  |
| Security function               | on                                     | Set limits to downloading/uploading o   | f the projects, reference, and cle | ar etc., with a password.           |   |  |  |  |
| Calendar                        |  | Up to 31 Dec. 2069 23:59:59 Precisio<br>When multi-CPU system is used, time   |                                    |                                     |   |  |  |  |
| Battery backup *5               |  | Backup range: Calendar IC memory<br>Battery used: Lithium primary battery<br>Replacement time (at 25°C): within 5<br>Backup time (at 25°C): 5 years | minutes                            |                                     |   |  |  |  |
| Memory backu<br>memory and no   | p by flash<br>pnvolatile RAM           | Application programs, system definition Retain memory, RAS, and logging and   |                                    |                                     |   |  |  |  |
| Memory backu<br>card (optional) | p by user ROM                          | Application programs, system definition   | ons, zip files, compressed projec  | ts and User's data can be saved.    |   |  |  |  |
| No. of occupied                 | d slots *6                             | 1 slot  |                                    |                                     |   |  |  |  |
| nternal current                 | t consumption                          | 24 V DC, 700 mA or less   |                                    |                                     |   |  |  |  |
|                                 |  | Approx. 420 g   |                                    |                                     |   |  |  |  |

\*1 This is the total of the shared and private areas used by two application control units.

\*2 This depends on each instruction.

\*3 The periodic task must be an integer multiple of the bus tact specified for synchronization. If it is not, an error will occur and the task will not run.

\*4 Applicable standard of USB: USB2.0

\*5 Stored data, such as retained memory and RAS information, is automatically backed up to the CPU's built-in nonvolatile memory when the SPH5000EC is powered off. This means that there is no need for battery backup for those memories. However, if calendar memory backup is required, please purchase an optional battery (NP8P-BT). \*6 The SPH5000EC must be installed in an EP bus-compatible slot on an EP bus-compatible baseboard.

Note: Currently, SPH5000EC is compatible with the programming support tool Expert (D300win). We are also planning to support the programming support tool Standard in the future.

#### Performance specifications (user memory detail)

|        |  |                   | SPH300       |            |               |               |                | SPH300EX                     |
|--------|--|-------------------|--------------|------------|---------------|---------------|----------------|------------------------------|
| Nodel  |  |                   | NP1PS-32     | NP1PS-32R  | NP1PS-74R     | NP1PS-117R    | NP1PS-245R     | NP1PS-74D                    |
| lser n | Program memory Program memory Data memory I/O memory Non-retain memory Retain memory | 97 Kwords         |              | 277 Kwords | 491 Kwords    | 1,003 Kwords  | 277×2+6 Kwords |                              |
|        | Program memory   |                   | 65,536 words |            | 151,552 words | 239,616 words | 501,760 words  | 151,552×2 words              |
|        |  |                   | 32,768 steps |            | 75,776 steps  | 119,808 steps | 250,880 steps  | 75,776 × 2 steps             |
|        |  |                   | 33,792 words |            | 132,096 words | 263,168 words | 525,312 words  | 132,096 × 2 + 6,144<br>words |
|        | 1 MA   | I/O memory        | 512 words    |            |               |               |                | 512×2 words                  |
|        |  | Non-retain memory | 8,192 words  |            | 32,768 words  | 131,072 words | 262,144 words  | 32,768 × 2 words             |
|        | 1 MA   |                   | 4,096 words  |            | 16,384 words  | 32,768 words  | 130,048 words  | 16,384 × 2 words             |
|        | 1 MA   | User FB memory    | 4,096 words  |            | 16,384 words  | 32,768 words  | 66,560 words   | 16,384 × 2 words             |
|        | 1 MA   | System FB memory  | 16,384 words |            | 65,536 words  |               |                | 65,536 × 2 words             |
|        | 1 MA   | Edge detection    | 1,024 points |            | 4,096 points  |               |                | 4,096 x 2 points             |
|        |  | Counter           | 256 points   |            | 1,024 points  |               |                | 1,024 x 2 points             |
|        | 1 MA   | Integrating timer | 128 points   |            | 512 points    |               |                | 512 x 2 points               |
|        |  | Timer             | 512 points   |            | 2,048 points  |               |                | 2,048 x 2 points             |
|        |  | Others            | 8,192 words  |            | 32,768 words  |               |                | 32,768 × 2 words             |
|        |  | System memory     | 512 words    |            |               |               |                | 512 × 2 words                |
|        | A DEAL   | Common memory     | -            |            |               |               |                | 6,144 words                  |

|        |        |                |                   | SPH2000      |                |                 |                 | SPH200       |              |            |  |
|--------|--------|----------------|-------------------|--------------|----------------|-----------------|-----------------|--------------|--------------|------------|--|
| Model  |        |                |                   | NP1PM-48R    | NP1PM-48E      | NP1PM-256E      | NP1PM-256H      | NP1PH-08     | NP1PH-16     |            |  |
| User m | nemory | <i>,</i>       |                   | 193 Kwords   |                | 2,561 Kwords    |                 | 29 Kwords    | 57 Kwords    |            |  |
|        | Progra | am men         | nory              | 98,304 words |                | 524,288 words   |                 | 16,384 words | 32,768 words |            |  |
|        |        |                |                   | 49,152 steps |                | 262,144 steps   |                 | 8,192 steps  | 16,384 steps |            |  |
|        | Data n | nemory         |                   | 99,328 words |                | 2,098,176 words |                 | 13,312 words | 25,600 words |            |  |
|        |        | I/O me         | emory             | 512 words    |                |                 |                 |              |              |            |  |
|        |        | Non-re         | etain memory      | 65,536 words | 65,536 words   |                 | 1,703,936 words |              | 8,192 words  |            |  |
|        | 1      | Retain memory  |                   | 8,192 words  |                | 237,568 words   | 237,568 words   |              | 4,096 words  |            |  |
|        |        | User FB memory |                   | 8,192 words  | s 73,728 words |                 | 2,048 words     | 4,096 words  |              |            |  |
|        |        | Syster         | n FB memory       | 16,384 words | 16,384 words   |                 | 81,920 words    |              | 8,192 words  |            |  |
|        |        |                | Edge detection    | 1,024 points | 1,024 points   |                 |                 | 256 points   | 512 points   |            |  |
|        |        |                | Counter           | 256 points   |                | 1,280 words     |                 | 64 points    | 128 points   |            |  |
|        |        |                | Integrating timer | 128 points   |                | 640 words       |                 | 32 points    | 64 points    |            |  |
|        |        |                |                   | Timer        | 512 points     |                 | 2,560 words     |              | 128 points   | 256 points |  |
|        |        |                | Others            | 8,192 words  |                | 40,960 words    |                 | 2,048 words  | 4,096 words  |            |  |
|        |        | System memory  |                   | 512 words    |                |                 |                 |              |              |            |  |
|        |        | Comm           | non memory        | -            |                |                 |                 |              |              |            |  |

|          |            |            |                   | SPH3000                    |                 |                 |
|----------|------------|------------|-------------------|----------------------------|-----------------|-----------------|
| Model    |            |            |                   | NP1PU-048E                 | NP1PU-128E      | NP1PU-256E      |
| Jser men | nory       |            |                   | 353 Kwords                 | 1,281 Kwords    | 2,561 Kwords    |
| Pro      | ogram memo | ory        |                   | 98,304 words 262,144 words |                 | 524,288 words   |
|          |            |            |                   | 49,152 steps               | 131,072 steps   | 262,144 steps   |
| Dat      | ta memory  |            |                   | 263,168 words              | 1,049,600 words | 2,098,176 words |
|          | SX bu      | S          |                   | 263,168 words              | 1,049,600 words | 2,098,176 words |
|          |            | I/O memo   | ry                | 512 words                  |                 |                 |
|          |            | Non-retair | n memory          | 98,304 words               | 786,432 words   | 1,703,936 words |
|          |            | Retain me  | mory              | 40,960 words               | 122,880 words   | 237,568 words   |
|          |            | User FB n  | nemory            | 40,960 words               | 57,344 words    | 73,728 words    |
|          |            | System FI  | 3 memory          | 81,920 words               | 31,920 words    |                 |
|          |            |            | Edge detection    | 5,120 points               |                 |                 |
|          |            |            | Counter           | 1,280 points               |                 |                 |
|          |            |            | Integrating timer | 640 points                 |                 |                 |
|          |            |            | Timer             | 2,560 points               |                 |                 |
|          |            |            | Others            | 40,960 words               |                 |                 |
|          |            | System m   | emory             | 512 words                  |                 |                 |

Note: Area sizes of the non-retain memory, the retain memory, the user FB memory and the system FB memory can be changed.

#### Performance specifications (user memory detail)

|          |        |            |                                       | SPH3000D      |                 |                  |                 |
|----------|--------|------------|---------------------------------------|---------------|-----------------|------------------|-----------------|
|          |        |            |                                       | NP1PU-048EZM  | NP1PU-096EZM    | NP1PU-256EZM     | NP1PU-256EZM    |
| nemory   |        |            |                                       | 545 k words   | 1,409 k words   | 1,473 k words    | 2,753 k words   |
| Program  | n memo | ry         |                                       | 98,304 words  | 196,608 words   | 262,144 words    | 524,288 words   |
|          |        |            |                                       | 49,152 steps  | 98,304 steps    | 131,072 steps    | 262,144 steps   |
| Data me  | emory  |            |                                       | 459,776 words | 1,246208 words  | 1,246,208 words  | 2,294,784 words |
|          | SX bi  | IS         |                                       | 459,776 words | 1,246208 words  | 1,246,208 words  | 2,294,784 words |
|          |        | I/O memo   | 0/                                    | 512 words     | 1,210200 110100 | 1,210,200 110100 | 2,201,701110100 |
|          |        |            |                                       |               | 700, 100        | 700, 400         | 4 700 000       |
|          |        | Non-retair |                                       | 98,304 words  | 786,432 words   | 786,432 words    | 1,703,936 words |
|          |        | Retain me  | •                                     | 40,960 words  | 122,880 words   | 122,880 words    | 237,568 words   |
|          |        | User FB n  | nemory                                | 172,032 words | 188,416 words   | 188,416 words    | 204,800 words   |
|          |        | System Fl  | 3 memory                              | 147,456 words |                 |                  |                 |
|          |        |            | Edge detection                        | 10,240 points |                 |                  |                 |
|          |        |            | Counter                               | 6,144 points  |                 |                  |                 |
|          |        |            | Integrating timer                     | 1,024 points  |                 |                  |                 |
|          |        |            |                                       |               |                 |                  |                 |
|          |        |            | Timer                                 | 6,144 points  |                 |                  |                 |
|          |        |            | Others                                | 45,056 words  |                 |                  |                 |
|          |        | System m   | emory                                 | 512 words     |                 |                  |                 |
|          |        |            |                                       |               |                 |                  |                 |
|          |        |            |                                       | 3000MM        |                 |                  |                 |
| I        |        |            |                                       | NP1PU2-048E   |                 | NP1PU2-256E      |                 |
| memory   |        |            |                                       | 1234.5 Kwords |                 | 5650.5 Kwords    |                 |
|          | n memo | ry         |                                       | 196,608 words |                 | 1,048,576 words  |                 |
| J. J. J. |        |            |                                       | 98,304 steps  |                 | 524,288 steps    |                 |
|          | SX b   | 16         |                                       | -             |                 | 027,200 31603    |                 |
|          | 57.0   | 45         |                                       | -             |                 |                  |                 |
|          |        |            |                                       | -             |                 |                  |                 |
|          | E-SX   | bus 0      |                                       | 98,304 words  |                 | 524,288 words    |                 |
|          |        |            |                                       | 49,152 steps  |                 | 262,144 steps    |                 |
|          | E-SX   | bus 1      |                                       | 98,304 words  |                 | 524,288 words    |                 |
|          |        |            |                                       | 49,152 steps  |                 | 262,144 steps    |                 |
| Data me  | emory  |            |                                       |               |                 | , ,              |                 |
|          | SX bi  | 10         |                                       |               |                 |                  |                 |
|          | SX DI  |            |                                       | 510           |                 |                  |                 |
|          |        | I/O memo   |                                       | 512 words     |                 |                  |                 |
|          |        | Non-retair | memory                                | 65,536 words  |                 |                  |                 |
|          |        | Retain me  | mory                                  | 65,536 words  |                 |                  |                 |
|          |        | User FB m  | nemory                                | -             |                 |                  |                 |
|          |        | System FE  | 3 memory                              | -             |                 |                  |                 |
|          |        |            | Edge detection                        | -             |                 |                  |                 |
|          |        |            |                                       |               |                 |                  |                 |
|          |        |            | Counter                               | -             |                 |                  |                 |
|          |        |            | Integrating timer                     | -             |                 |                  |                 |
|          |        |            | Timer                                 | -             |                 |                  |                 |
|          |        |            | Others                                | -             |                 |                  |                 |
|          |        | System m   | emory                                 | 512 words     |                 |                  |                 |
|          |        | Common     | nemory                                | 512 words     |                 |                  |                 |
|          | E-SX   | bus 0      |                                       |               |                 |                  |                 |
|          |        | I/O memor  | CV                                    | 4,096 words   |                 |                  |                 |
|          |        |            |                                       |               |                 | 1 700 000        |                 |
|          |        | Non-retair |                                       | 98,304 words  |                 | 1,703,936 words  |                 |
|          |        | Retain me  |                                       | 40,960 words  |                 | 237,568 words    |                 |
|          |        | User FB n  | nemory                                | 172,032 words |                 | 204,800 words    |                 |
|          |        | System FE  | 3 memory                              | 147,456 words |                 |                  |                 |
|          |        |            | Edge detection                        | 10,240 points |                 |                  |                 |
|          |        |            | Counter                               | 6,144 points  |                 |                  |                 |
|          |        |            | Integrating timer                     | 1,024 points  |                 |                  |                 |
|          |        |            |                                       |               |                 |                  |                 |
|          |        |            | Timer                                 | 6,144 points  |                 |                  |                 |
|          |        |            | Others                                | 45,056 words  |                 |                  |                 |
|          |        | System m   | emory                                 | 4,608 words   |                 |                  |                 |
|          | E-SX   | bus 1      |                                       |               |                 |                  |                 |
|          |        | I/O memo   | ry                                    | 4,096 words   |                 |                  |                 |
|          |        | Non-retair |                                       | 98,304 words  |                 | 1,703,936 words  |                 |
|          |        | Retain me  | -                                     | 40,960 words  |                 | 237,568 words    |                 |
|          |        |            |                                       |               |                 |                  |                 |
|          |        | User FB n  |                                       | 172,032 words |                 | 204,800 words    |                 |
|          |        | System FE  | 3 memory                              | 147,456 words |                 |                  |                 |
|          |        |            | Edge detection                        | 10,240 points |                 |                  |                 |
|          |        |            | Counter                               | 6,144 points  |                 |                  |                 |
|          |        |            | Integrating timer                     | 1,024 points  |                 |                  |                 |
|          |        |            |                                       |               |                 |                  |                 |
|          |        |            | Timer                                 | 6,144 points  |                 |                  |                 |
|          |        |            | Others                                | 45,056 words  |                 |                  |                 |
|          |        |            | · · · · · · · · · · · · · · · · · · · |               |                 |                  |                 |

Note: Area sizes of the non-retain memory, the retain memory, the user FB memory and the system FB memory can be changed.

#### Performance specifications (user memory detail)

|          |         |                   | SPH3000MG       | SPH5000H                              | BACnetCPU *3 |
|----------|---------|-------------------|-----------------|---------------------------------------|--------------|
| el       |         |                   | NP1PU1-256NE    | NP1PU1-512H                           | NP1PUBM-048C |
| er memor | ry      |                   |                 |                                       | ·            |
| Progra   | ram mem | lory              | 524,288 words   | 1,048,576 words                       | 98,304 word  |
|          |         |                   | 262,144 steps   | 524,288 steps                         | 49,152 steps |
| Data     | memory  |                   |                 |                                       |              |
|          | I/O     | memory (SX bus)   | 512 words       | -                                     | 512 words    |
|          | I/O .   | memory (E-SX bus) | 4,096 words     | · · · · · · · · · · · · · · · · · · · | -            |
|          | Nor.    | n-retain memory   | 1,703,936 words |                                       | 98,304 words |
|          | Ret     | tain memory       | 237,568 words   | 262,144 words                         | 40,960 words |
|          | Use     | er FB memory      | 204,800 words   | 65,536 words                          | 40,960 words |
|          | Sys     | stem FB memory    | 147,456 words   | 65,536 words                          | 81,920 words |
|          |         | Edge detection    | 10,240 points   | 4,096 points                          | 5,120 points |
|          |         | Counter           | 6,144 points    | 1,024 points                          | 1,280 points |
|          |         | Integrating timer | 1,024 points    | 512 points                            | 640 points   |
|          |         | Timer             | 6,144 points    | 2,048 points                          | 2,560 points |
|          |         | Others            | 45,056 words    | 32,768 words                          | 40,960 words |
|          | Sys     | stem memory       |                 | · · · · · · · · · · · · · · · · · · · | 512 words    |
|          |         | SX bus            | 512 words       | 512 words                             | 512 words    |
|          |         | E-SX bus          | 4,608 words     | 33,280 words                          | -            |
|          |         | Built-in FL-net   | -               | 512 words                             | -            |

|           |          |                          | SPH5000M           |                |                 |                                       |  |  |
|-----------|----------|--------------------------|--------------------|----------------|-----------------|---------------------------------------|--|--|
| del       | iel      |                          |                    | NP1PA1-128E    | NP1PA1-256E     | NP1PA1-512E                           |  |  |
| er memory | r memory |                          |                    |                |                 | ·                                     |  |  |
| Program r | nemo     | ry                       | 196,608 words      | 262,144 words  | 524,288 words   | 1,048,576 words                       |  |  |
|           |          |                          | 98,304 steps       | 131,072 steps  | 262,144 steps   | 524,288 steps                         |  |  |
| Data mem  | nory     |                          |                    | ·              | ·               | · · ·                                 |  |  |
|           | I/O m    | iemory (SX bus)          | 512 words          |                |                 |                                       |  |  |
|           | I/O m    | iemory (E-SX bus)        | 4,096 words        |                |                 |                                       |  |  |
|           | Non-ı    | retain memory            | *1 1,310,720 words |                | 2,621,440 words | 5,242,880 words                       |  |  |
|           | Retai    | n memory                 | *1 573,440 words   |                | 638,976 words   | 1,048,576 words                       |  |  |
|           | User     | FB memory                | *2 212,992×2 words |                | 294,912×2 words | 376,832×2 words                       |  |  |
|           | Syste    | em FB memory             | *2 147,456×2 words |                |                 | · · · · · · · · · · · · · · · · · · · |  |  |
|           |          | Edge detection           | 10,240×2 points    |                |                 |                                       |  |  |
|           |          | Counter                  | 6,144×2 points     | 6,144×2 points |                 |                                       |  |  |
|           |          | Integrating timer        | 1,024×2 points     |                |                 |                                       |  |  |
|           |          | Timer                    | 6,144×2 points     |                |                 |                                       |  |  |
|           |          | Others                   | 45,056×2 words     |                |                 |                                       |  |  |
|           | Syste    | em memory                |                    |                |                 |                                       |  |  |
|           |          | SX bus                   | 512 words          |                |                 |                                       |  |  |
|           |          | APL0/1+E-SX bus          | 1,024 words        |                |                 |                                       |  |  |
|           |          | E-SX bus integrated type | 16,384 words       |                |                 |                                       |  |  |

|            |                     |              | SPH5000EC       |               |                 |                 |
|------------|---------------------|--------------|-----------------|---------------|-----------------|-----------------|
| əl         |                     | NP1PA1C-096E | NP1PA1C-128E    | NP1PA1C-256E  | NP1PA1C-512E    |                 |
| memory     |                     |              |                 |               | ·               |                 |
| Program me | emory               |              | 196,608 words   | 262,144 words | 524,288 words   | 1,048,576 words |
|            |                     |              | 98,304 steps    | 131,072 steps | 262,144 steps   | 524,288 steps   |
| Data memor | ry                  |              |                 |               | · · ·           |                 |
| 1/0        | O memory (SX bus)   |              | 512 words       |               |                 |                 |
| 1/0        | O memory (E-SX bus) |              | 4,096 words     |               |                 |                 |
| N          | Ion-retain memory   | *1           | 1,310,720 words |               | 2,621,440 words | 5,242,880 words |
| R          | Retain memory       | *1           | 573,440 words   |               | 638,976 words   | 1,048,576 words |
| U          | Jser FB memory      | *2           | 212,992×2 words |               | 294,912×2 words | 376,832×2 words |
| S          | System FB memory    | *2           | 147,456×2 words |               |                 |                 |
|            | Edge detection      |              | 10,240×2 points |               |                 |                 |
|            | Counter             |              | 6,144×2 points  |               |                 |                 |
|            | Integrating timer   |              | 1,024×2 points  |               |                 |                 |
|            | Timer               |              | 6,144×2 points  |               |                 |                 |
|            | Others              |              | 45,056×2 words  |               |                 |                 |
| S          | System memory       |              |                 |               |                 |                 |
|            | SX bus              |              | 512 words       |               |                 |                 |
|            | APL0/1              |              | 1,024 words     |               |                 |                 |
|            | EtherCAT            |              | 512 words       |               |                 |                 |

\*1 This indicates the total value including user FB memory(non-retain memory, retain memory)and system FB memory (non-retain memory, retain memory). \*2 This indicates the default values in the Expert loader for the user FB memory and system FB memory.

\*3 Only for Japan's doemestic market

## SPH5000EC, SPH3000D Motion System

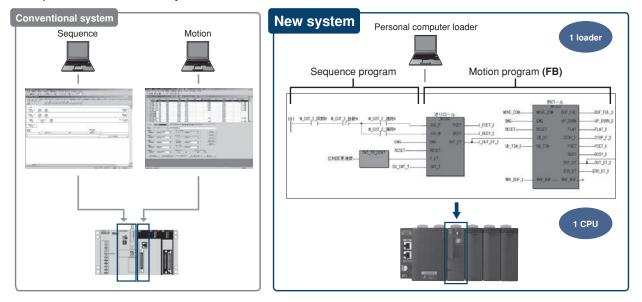
#### Features

- The EtherCAT and SX bus realize wiring-saving motion systems.
- SPH5000EC: Large-scale (high-speed, high-accuracy) motion system
- · 32-axis control / 1 ms (single-CPU system: 1 CPU)
- 96-axis control / 1 ms (multi-CPU system: 3 CPUs)
- SX bus, EtherCAT compatible
- Max. number of connected axes: 64 (EtherCAT 1-system)
- Maximum number of slaves: 238
- SPH3000D: Small- and medium-scale (economical) motion system

- · 32-axis control / 2 ms
- SX bus compatible
- Max. number of connected axes: 32
- Makes it possible to build scalable motion systems according to system scale and application
- Comes with 10 built-in motion-specific FBs, making it ideal for motion control applications.
- Makes it possible to build highly functional, high-performance motion systems with minimal configuration.
  - Achieves sequence control and motion control on a single CPU

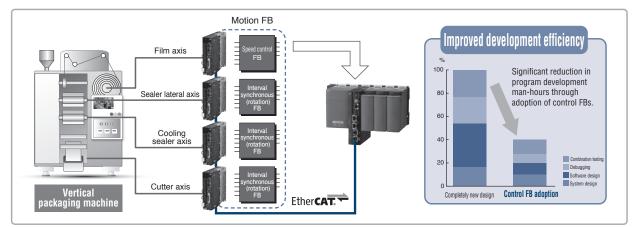
#### **Two in One** Sequence control and motion control are realized with only one CPU.

- · Expensive special motion modules are unnecessary. You can save money to a large extent.
- Supporting both sequence and motion control by one programming tool (SX-Programmer Expert (D300win)) substantially improves the work efficiency.



#### Smart Various motion programs (FBs) are provided.

- · Various function software programs (FBs) are provided.
- · You can combine FBs to realize motion programs for large systems in a short time.
- You can freely set functions necessary for your machine for each axis. There are no limits on how to combine motion functions (such as positioning, interpolation, and synchronous operation).
- · You can reuse the FB, so that the program development efficiency, debugging efficiency and reliability are substantially improved.



## SPH5000H Highly reliable duplex system

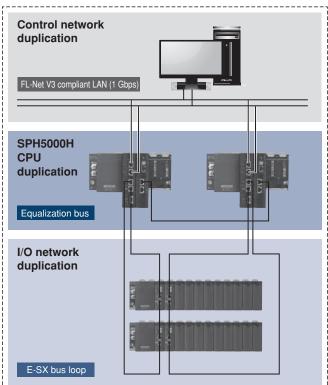
#### Features

#### **High Reliability**

- Redundant CPU High-performance operation utilizing 1 Gbps equalization bus. High-reliability by ECC memory.
- Duplexd network
   High-speed communication utilizing
   1 Gbps FL-Net protocol.
   High performance communication by integrated network function in CPU module.
- Duplexd I/O network with loop function High-speed I/O refresh by E-SX bus.
   Robust I/O network utilizing loop support.

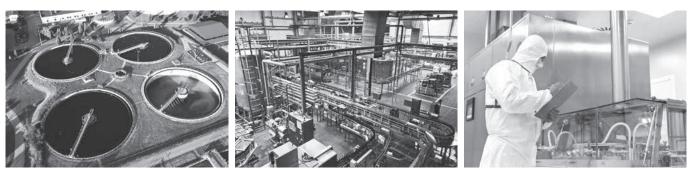
#### System configuration example

MICREX-SX SPH5000H Configuration



#### Deployment Example

Suitable for systems that require 24/7 operation with no stoppages.



Water treatment system

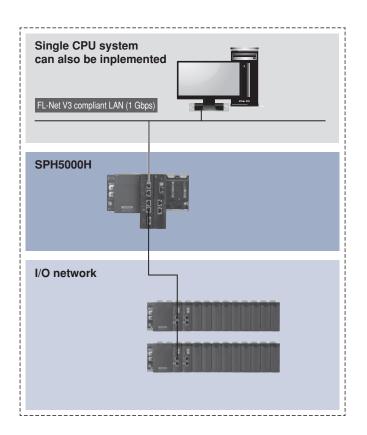
Foodplant

# Developability and Applicability

- Large-scale I/O
- E-SX bus with up to 65,536 points.
- E-SX bus
  - Ideal for high-speed processing, distributed deployment, high-speed synchronous systems. Enables mounting of processor link and I/O master on a baseboard with duplex E-SX bus interface module.

#### Highly maintainability

· Battery-less datarementention with retain memory, RAS.



## SPH2000 redundant system

#### Features

- Mass equalization data
- Up to 320 Kwords of data can be equalized.
- · High-speed transmission through dedicated equalization bus 100 Mbps dedicated equalization bus transmits the equalization data. Also, as a connection cable, a commercially available LAN cable (shielded category 5, cross connect cable) is used.
- Module exchangeable during running CPU A failed CPU module can be exchanged without stopping the system by using a hot pluggable base board.

#### System configuration example

Relevant model: NP1PM-256H

- · Redundant multi-CPU system enabled Up to 4 multi-CPUs can be used for redundancy in multi-CPU (distributed processing) systems.
- · Easy equalization setting
- Equalization area can be set up on a per-FB instance basis in addition to on a per-variable basis.
- · System configuration with standard modules enabled Standard modules allow you to construct systems such as power supplies, base boards and I/O modules.

| System configur  | ation exan   | nple         |          |        | Comparing SPH   | redundancy perfo                     | ormance                           |
|--|--------------|--------------|----------|--------|---|--------------------------------------|-----------------------------------|
|  |              |              |          |        |   | SPH2000                              | SPH300                            |
|  |              |              |          |        |   | NP1PM-256H                           | NP1PS-DD                          |
|  |              |              |          |        | Max. equalization   | 320 Kwords                           | 8 Kwords                          |
|  | Dana I       |              |          |        | capacity  |                                      |                                   |
|  |              |              |          |        | Equalization  | 20 ms/8 Kwords                       | 200 ms/8 Kwords                   |
| 1  |              |              |          |        | performance   | 250 ms/320 Kwords                    |                                   |
|  |              |              |          |        |   |                                      |                                   |
| Annual  |              |              |          |        | Equalization bus  | Ethernet (for only)                  | SX bus                            |
|  |              |              | 1.1      |        |   | 100 Mbps                             |                                   |
|  |              |              |          |        | Equalization timing   | Setting task (multiple)              | Default                           |
| <cpu redundancy<="" th=""><th>on another</th><th>base board&gt;</th><th><u> </u></th><th></th><th><cpu redundan<="" th=""><th>cy on the same b</th><th>ase board&gt;</th></cpu></th></cpu> | on another   | base board>  | <u> </u> |        | <cpu redundan<="" th=""><th>cy on the same b</th><th>ase board&gt;</th></cpu> | cy on the same b                     | ase board>                        |
| 01/1   |              |              |          | I      | Power su  | pply CPU module<br>ndancy redundancy |                                   |
| SX bus   | / Active sy  | vetom        |          |        |   |                                      |                                   |
|  | Active s     | ystern       |          | SX bus | /   |                                      |                                   |
|  |              |              |          | I<br>I |   |                                      |                                   |
|  |              |              |          |        |   |                                      |                                   |
| Bat Plug   |              | l            |          |        | Hot Place   |                                      | Declara custom                    |
|  |              |              |          |        | Active sys  | tem                                  | <ul> <li>Backup system</li> </ul> |
| SX bus   | Favolization | CPU module   |          |        | SX bus  | Equalization bus                     |                                   |
|  | Equalization | redundancy   |          |        | Power su  |                                      |                                   |
| ľ  |              | localitatioy |          |        | module redu   | ppiy<br>ndancy                       |                                   |
|  |              | J            |          |        |   |                                      |                                   |
| Free Contraction of State  |              |              |          |        |   | ``````                               |                                   |
|  |              |              |          |        |   | 4                                    |                                   |
|  | -            |              |          |        | 0   |                                      |                                   |
|  |              |              |          | 1      |   |                                      |                                   |
| •  |              |              |          |        | P <sub>p</sub> , <sub>maxes</sub>   |                                      |                                   |
| Hot Plug   | Ļ            |              |          |        |   |                                      |                                   |
|  | Backup s     | system       |          |        |   |                                      |                                   |
| SX bus   |              | -            |          |        | SX bus  |                                      |                                   |
|  |              |              |          |        |   |                                      |                                   |
| i-i-i  |              | HDB          |          |        |   | RIN<br>C                             |                                   |
|  | to have      |              |          |        |   |                                      |                                   |

<Operation overview>

CPU module redundancy

SPH2000 supports "1:1 redundancy" which allows you to equalize the data and continue operation without stopping the system. Data equalization rate is up to 320 Kwords/250 ms (equalization bus transmission rate: 100 Mbps) using dedicated "equalization bus." • Power supply module redundancy

When two power supply modules are mounted on the same base board, the power supply modules run in parallel, and each module supplies 50% of the electric power.

When an error occurs in one of the power supply modules, the normally running power supply module supplies 100% of the electric power.

## SX-Net system of SPH3000MG

SX-Net is a controller level network based on gigabit Ethernet. It allows high-speed large-capacity communications.

#### Features

- Large scale The network enables 126 nodes to be connected per system.
- · High speed

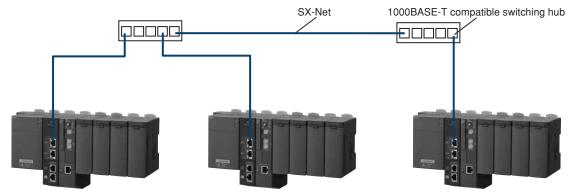
The settable shortest network scan interval is 0.5 ms (0.5 ms steps, up to 30 ms).

 Large capacity The network allows 128 Kwords (2,048 blocks in total in the unit of 64 words) as common memory space per system.

#### SX-Net specifications

| Item               |                          | Specifications   |
|--------------------|--------------------------|--|
| No. of conne       | ectable modules          | 126 units  |
| Station numl       | per setting range        | 1 to 126   |
| Scan interva       | 1                        | 0.5 ms to 30 ms (0.5 ms steps)<br>(This depends on the number of connected modules,<br>distance, total data quantity, and the number of hubs.) |
| Common             | 1-slot transmission size | 512 W  |
| memory<br>function | 1-slot transmission time | 30 us  |
| Turiction          | Maximum number of slots  | 256 slots  |
|                    | Data area size           | 128 KW (64 * 2048 blocks)  |
|                    | Area definition          | 64 W fixed-block selection method  |
|                    | Unit of data guarantee   | Unit of station occupation   |
|                    | Area update timing       | At the time of each scan (Batch transfer of area data)   |
| Message            | Туре                     | Unicast message (1 to 1)   |
| function           |                          | Broadcast message (1 to N)   |
|                    | Size                     | 1024 bytes   |

#### System configuration example



## **BACnet Monitoring System\***

#### Features

- The BACnet communication protocol complies with ANSI/ ASHRAE Standard 135-2012.
- It runs as a BACnet MS/TP master. Device profiles support B-ASC functionality.

#### BACnet MS/TP communication protocol

| Item                  |             | Description  |  |  |
|-----------------------|-------------|--|--|--|
| Port                  |             | Serial port 1 (general-purpose communication mode)   |  |  |
| Standard              |             | EIA-485 (RS-485)   |  |  |
| Transmission speed    |             | 9600bps, 19200bps, 38400bps (default), 76800bps, 115200bps   |  |  |
| Transmission distance |             | 1,200 m (Transmission speed: 76,800 bps or less)<br>1,000 m (Transmission speed: 115,200 bps)<br>Note: Please use ANSI/ASHRE recommended cables.   |  |  |
| Communication met     | hod         | 3-wire half-duplex system  |  |  |
| Synchronization met   | thod        | Start-stop synchronous transmission  |  |  |
| Protocol              |             | BACnet MS/TP master  |  |  |
| Number of connectit   | ole modules | Max. of 32 units per segment (80 units when using a repeater)  |  |  |
| Terminating resistor  |             | 120 Ω  |  |  |
| Address               |             | 0 to 127 (MS/TP master)  |  |  |
| Cable specifications  |             | Shielded twisted pair cable AWG 12 to 24<br>[ANSI/ASHRE recommendation]<br>AWG 18 or thicker cables<br>Capacitance between cables : 100 pF/m or less<br>Capacitance between cable and shield: 200 pF/m or less |  |  |
| Transmission          | Data length | 8 bits   |  |  |
| format                | Parity      | Non parity   |  |  |
|                       | Stop bits   | 1 bit  |  |  |
| Insulation method     |             | Photocoupler insulation  |  |  |

\* The following BACnet objects are supported. The maximum number of objects that can be registered is 300

| Object name         | Object Type | Description                  |
|---------------------|-------------|------------------------------|
|                     |             |                              |
| Analog Input        | 0           | Analog input                 |
| Analog Output       | 1           | Analog output                |
| Analog Value        | 2           | Analog input/output          |
| Binary Input        | 3           | Binary input                 |
| Binary Output       | 4           | Binary output                |
| Binary Value        | 5           | Binary input/output          |
| Device              | 8           | Device information           |
| Multi-state Input   | 13          | Multi-state input            |
| Multi-state Output  | 14          | Multi-state output           |
| Multi-state Value   | 19          | Multi-state I/O              |
| Nortification Class | 15          | Event notification recipient |
|                     |             | management                   |
| Accumulator         | 23          | Integrated value             |

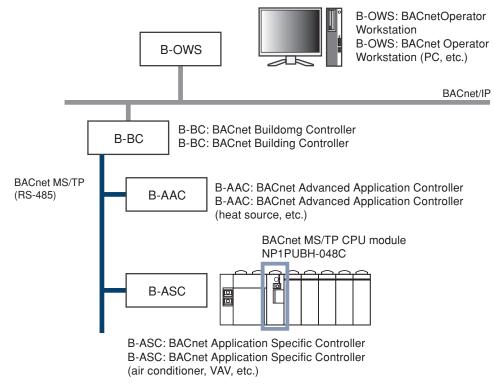
\* The property data of each object will be retained even during a power failure.

\* It is compatible with the SX-Programmer Expert (D300win) programming

support tool.
\* The dedicated Excel file and BACnet configuration tool makes it easy to configure settings

configure settings. (The Excel file and configuration tool can be downloaded for free from our website.)

#### System configuration

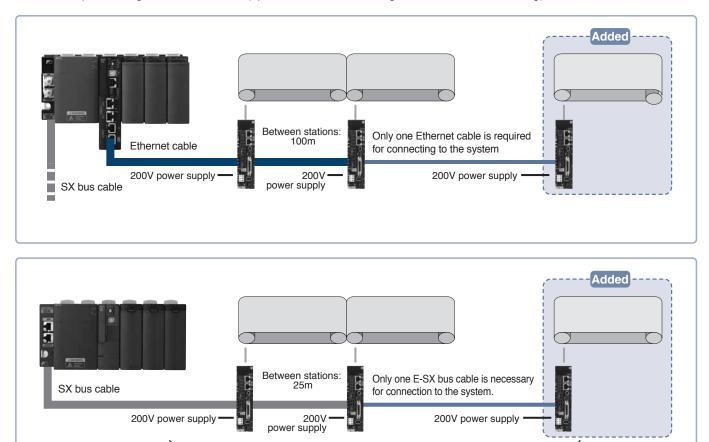


\*This product is for Japan's doemestic market.

#### Simple Ultra high-speed serial bus system (EtherCAT: 100Mbps, SX bus: 25Mbps) is adopted.

Minimum command communication cycle for EtherCAT: 0.5 ms; SX bus: 1 ms

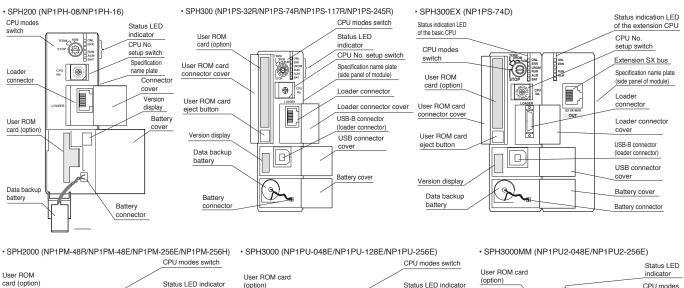
- The servo amplifier directly connected to the EtherCAT and SX bus helps establish a wiring-saving system. Cumbersome I/O wiring work and faults caused by wiring are substantially reduced.
- You can operate the servo using the servo loader from the PC connected to the CPU module. (Not necessary to change PC connections)
- · Addition of a servo axis to the system is quick with the Ethernet cable and SX bus cable. (Modular connector)
- · You can perform high level data control (operation status monitoring and fault status monitoring) from the loader.

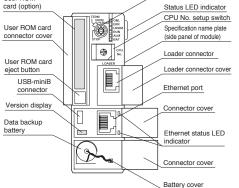


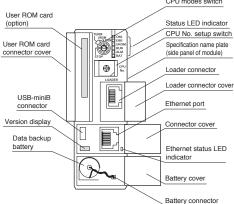
#### Overall length 25m (standard)

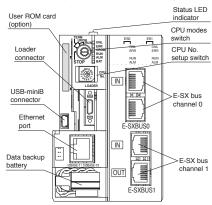
\* With SX bus electric repeaters, max. 100m. With SX bus optical converters, max. 25.6km.

#### Appearance

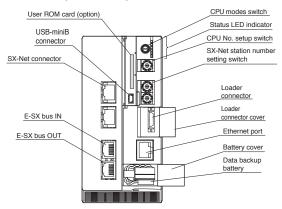


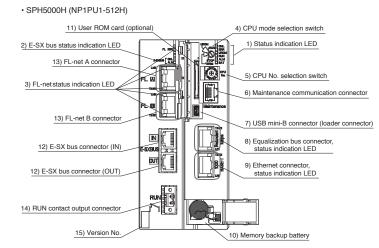




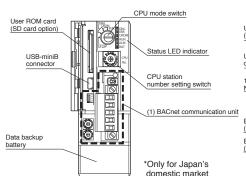


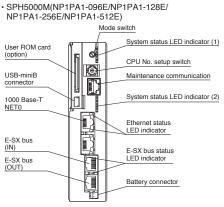
#### • SPH3000MG (NP1PU1-256NE)

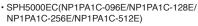


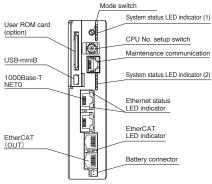


#### • BACnet MS/TP(NP1PUBM-048C)\*









## Base Board: NP1B -----

| Name                                       |                     | Model     | Max. no. of modules             | Internal current consumption | Weight        | Remarks                                 |
|--|---------------------|-----------|---------------------------------|------------------------------|---------------|---|
| Standard base board                        | Base board 3 slots  | NP1BS-03  | 2 (Not include a power supply)  | 35 mA or less                | Approx. 250 g | SX bus 3 slots, processor bus 2 slots   |
|  | Base board 6 slots  | NP1BS-06  | 5 (Not include a power supply)  | 45 mA or less                | Approx. 420 g | SX bus 6 slots, processor bus 4 slots   |
|  | Base board 8 slots  | NP1BS-08  | 6 (Not include a power supply)  | 50 mA or less                | Approx. 540 g | SX bus 8 slots, processor bus 3 slots   |
|  | Base board 11 slots | NP1BS-11  | 9 (Not include a power supply)  | 60 mA or less                | Approx. 720 g | SX bus 11 slots, processor bus 3 slots  |
|  | Base board 13 slots | NP1BS-13  | 11 (Not include a power supply) | 70 mA or less                | Approx. 840 g | SX bus 13 slots, processor bus 3 slots  |
| High-performance base board                | Base board 13 slots | NP1BP-13  | 11 (Not include a power supply) | 70 mA or less                | Approx. 840 g | SX bus 13 slots, processor bus 10 slots |
| Standard base board with                   | Base board 8 slots  | NP1BS-08S | 6 (Not include a power supply)  | 60 mA or less                | Approx. 550 g | SX bus 8 slots, processor bus 3 slots   |
| station number setting switch              | Base board 11 slots | NP1BS-11S | 9 (Not include a power supply)  | 70 mA or less                | Approx. 730 g | SX bus 11 slots, processor bus 3 slots  |
|  | Base board 13 slots | NP1BS-13S | 11 (Not include a power supply) | 80 mA or less                | Approx. 850 g | SX bus 13 slots, processor bus 3 slots  |
| High-performance base board                | Base board 13 slots | NP1BP-13S | 11 (Not include a power supply) | 80 mA or less                | Approx. 850 g | SX bus 13 slots, processor bus 10 slots |
| with station number setting switch         | 1 <u></u>           | ' I       | l I                             | l1                           | l i           |   |
| Standard hot plug base board               | Base board 8 slots  | NP1BS-08D | 6 (Not include a power supply)  | 70 mA or less                | Approx. 550 g | SX bus 8 slots, processor bus 3 slots   |
| with station number setting switch         | Base board 11 slots | NP1BS-11D | 9 (Not include a power supply)  | 80 mA or less                | Approx. 730 g | SX bus 11 slots, processor bus 3 slots  |
|  | Base board 13 slots | NP1BS-13D | 11 (Not include a power supply) | 80 mA or less                | Approx. 850 g | SX bus 13 slots, processor bus 3 slots  |
| Station number setting switch incorporated | Base board 13 slots | NP1BP-13D | 11 (Not include a power supply) | 80 mA or less                | Approx. 850 g | SX bus 13 slots, processor bus 10 slots |
| high-performance hot plug base board       | ·                   | ·         | l I                             | l1                           | l             |   |
| EP bus-compatible base                     | Base board 6 slots  | NP1BE-06  | 4 (Not include a power supply)  | 31 mA or less                | Approx. 490 g | SX bus 6 slots, processor bus 3 slots   |
| (EP bus 3 slots)                           | Base board 8 slots  | NP1BE-08  | 6 (Not include a power supply)  | 31 mA or less                | Approx. 630 g | SX bus 8 slots, processor bus 3 slots   |
|  | Base board 11 slots | NP1BE-11  | 9 (Not include a power supply)  | 31 mA or less                | Approx. 850 g | SX bus 11 slots, processor bus 3 slots  |
|  | Base board 13 slots | NP1BE-13  | 11 (Not include a power supply) | 31 mA or less                | Approx. 980 g | SX bus 13 slots, processor bus 3 slots  |
|  | Base board 13 slots | NP1BX-13  | 11 (Not include a power supply) | 31 mA or less                | Approx. 980 g | SX bus 13 slots, processor bus 10 slots |

Note: It allows operators to build a single-CPU or multi-CPU SPH5000M configuration by mounting one to three SPH5000Ms to an EP (enhanced processor) bus-compatible baseboard. Furthermore, it ensures compatibility with standard, high-performance baseboards (models: NP1BS-\_\_\_/NP1BP-\_\_\_\_).

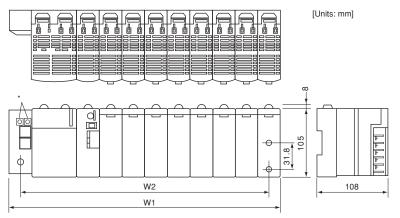
Mount a power supply module, plus not less than one module, onto the base board. Make sure to always mount the power supply module at the left side of the base board. A high-performance base board is used when configuring the system, such as one with multi-CPUs and redundancy, and it uses a processor bus heavily. Modules which use the processor bus are as follows:

· CPU module · FL-net module

· P-link/PE-link module · LE-net related module

Single-slot power supplies (model: NP1S-91/NP1S-81) cannot be used with EP bus-compatible baseboards.

#### Dimension



| No. of slots | W1     | W2     |
|--------------|--------|--------|
| 3            | 133 mm | 115 mm |
| 6            | 238 mm | 220 mm |
| 8            | 308 mm | 290 mm |
| 11           | 413 mm | 395 mm |
| 13           | 483 mm | 465 mm |

Note: When the connector is mounted, the depth is a max. of 195.3mm.

The bracket is already mounted on the base board.

\* Station number setting switch Incorporated in base board with the station number setting switch

## **Programmable Controllers** MICREX-SX series **E-SX bus Product**

#### **E-SX bus Product**





Analog input unit

Digital input unit





High-speed counter



Integrated type interface module



Auxiliary power supply unit

#### Digital input/output unit

It is a separate mounting type I/O unit that can be directly connected to the E-SX bus.

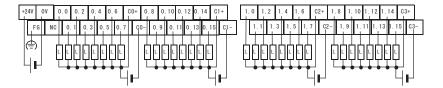
#### · Digital input unit

| Item                         | Specifications   |
|------------------------------|--|
| Model                        | NU2X3206-W   |
| Input method                 | Sink/source in common use 32-point (8-point common x 4 circuits)   |
| Input voltage                | Rating: 24 V DC, max. acceptable: 30 V DC, Acceptable ripple rate: 5% or less  |
| Power supply method          | E-SX bus cable (24 V DC)   |
| Rated current                | 7 mA (at 24 V DC)  |
| Standard operation           | OFF→ON: 15-30 V  |
| range                        | ON→OFF: 0-5 V  |
| Input delay time             | OFF to ON: 25 $\mu$ s or less (hard filter time) + (soft filter time)<br>ON to OFF: 75 $\mu$ s or less (hard filter time) + (soft filter time) |
| Insulation method            | Photocoupler insulation  |
| External connections         | Detachable M3 screw terminal block   |
| Internal current consumption | Operating: 260 mA or less, Bypassing: 93 mA  |
| Dimension<br>(W×H×D) [mm]    | 240 x 65 x 60 (except DIN rail mounting protrusions)   |
| Weight                       | Approx. 430 g  |

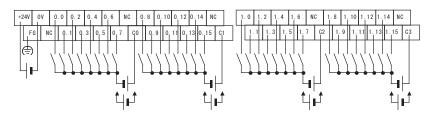
#### Digital output unit

| Item                         | Specifications  |  |
|------------------------------|---|--|
| Model                        | NU2Y32T09P6   |  |
| Output method                | Transistor sink 32 points (8-point common x 4 circuits)   |  |
| Output voltage               | Rating: 24 V DC, Allowable: 10.8 V to 30 V DC   |  |
| Power supply method          | E-SX bus cable (24 V DC)  |  |
| Max. load current            | 0.6 A/ point 4 A/ common  |  |
| Output delay time            | OFF to ON: 10 $\mu$ s or less   |  |
|                              | ON to OFF: 200 $\mu$ s or less  |  |
| Output protection            | Overload protection: built-in fuse (common unit 4 fuses)<br>Surge suppression: Varistor (total 32 points) |  |
| Insulation method            | Photocoupler insulation   |  |
| External connections         | Detachable M3 screw terminal block  |  |
| Internal current consumption | Operating: 300 mA or less, Bypassing: 93 mA   |  |
| Dimension                    | 240 x 65 x 60 (except DIN rail mounting protrusions)  |  |
| (W×H×D) [mm]                 |   |  |
| Weight                       | Approx. 410 g   |  |

#### · Example external connection of digital input

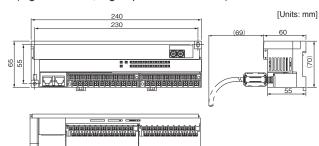


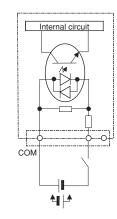
#### · Example external connection of digital output



(02)

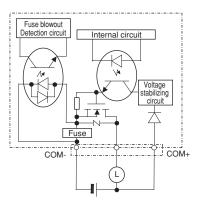
· Outline dimensional drawing (digital I/O unit, high-speed counter unit)





· Internal circuit diagram of digital input

#### · Internal circuit diagram of digital output



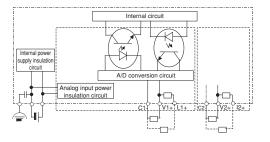
#### Analog input/output unit

It is a separate mounting type analog unit that can be directly connected to the E-SX bus.

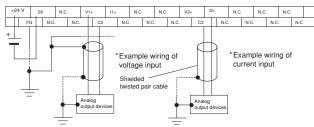
#### · Analog input unit

|                                    | unnt   |                          |              |            |
|------------------------------------|--|--------------------------|--------------|------------|
| Item                               | Specifications   |                          |              |            |
| Model                              | NU2AXH2-MR   |                          |              |            |
| Input format                       | Multi-range 2 channels   |                          |              |            |
| Power supply method                | E-SX bus cable   | E-SX bus cable (24 V DC) |              |            |
| Signal range                       | 0 to 10V   | -5 to +5V                | -20 to +20mA | 0 to 20mA  |
|                                    | 0 to 5V  | -10 to +10V              |              | 4 to 20mA  |
|                                    | 1 to 5V  |                          |              |            |
| Digital converted value (INT type) | 0 to 20000   | -20000 to +2000          | 0            | 0 to 20000 |
| Resolution                         | 15 bits  | 15 bits                  |              |            |
| Measurement accuracy               | $\pm 0.1\%$ of F.S.R. (Ta = 23°C $\pm 5$ °C), setting moving average for 8 data or more  |                          |              |            |
| Converting speed                   | 25 μs/2 channels   |                          |              |            |
| Insulation method                  | Between analog input terminal and FG: Photocoupler and transformer insulated<br>Between analog input terminal and channel: Transformer insulated |                          |              |            |
| External connections               | Detachable M3 screw terminal block   |                          |              |            |
| Internal current consumption       | Operating: 300 mA or less, Bypassing: 93 mA  |                          |              |            |
| Dimension<br>(W×H×D) [mm]          | 165 x 65 x 60 (except DIN rail mounting protrusions)   |                          |              |            |
| Weight                             | Approx. 360 g  |                          |              |            |

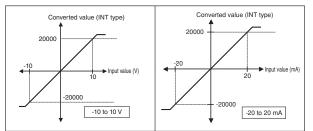
#### · Internal circuit diagram of analog input



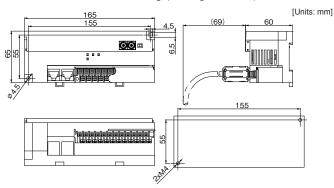
· Example external connection of analog input



· Analog input unit characteristic diagram



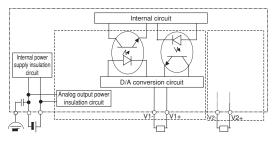
· Outline dimensional drawing (analog I/O units)



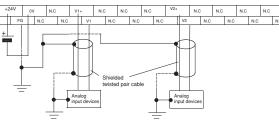
#### · Analog output unit

| Item                               | Specifications   |   |        |               |       |
|------------------------------------|--|---|--------|---------------|-------|
| Model                              | NU2AYH2V-MR  |   |        |               |       |
| Output format                      | Voltage multi-range 2 channels   |   |        |               |       |
| Power supply method                | E-SX bus cab   | le (24 V DC)  |        |               |       |
| Signal range                       | -10 to +10 V   | -10 to +10 V -5 to +5 V 0 to 10 V 0 to 5 V 1 to 5 V |        |               |       |
| Digital converted value (INT type) | -20000 to +20000 0 to 20000  |   |        |               |       |
| Max. resolution                    | 0.5 mV   | 0.25 mV   | 0.5 mV | 0.25mV        | 0.2mV |
| Measurement accuracy               | ±0.1% of F.S.R. (Ta = 23°C±5°C)  |   |        |               |       |
| Converting speed                   | 25 μs/2 channels   |   |        |               |       |
| Insulation method                  | Between analog output terminal and FG: Photocoupler and<br>transformer insulated |   |        |               |       |
|                                    | Between analog output terminal and channel: Transformer insulate                 |   |        | mer insulated |       |
| External connections               | Detachable M3 screw terminal block   |   |        |               |       |
| Internal current consumption       | Operating: 300 mA or less, Bypassing: 93 mA                                      |   |        |               |       |
| Dimension<br>(W×H×D) [mm]          | 165 x 65 x 60 (except DIN rail mounting protrusions)                             |   |        |               |       |
| Weight                             | Approx. 350 g  |   |        |               |       |

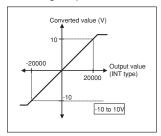
#### · Internal circuit diagram of analog output



· Example external connection of analog input



· Analog output unit characteristic diagram



## Programmable Controllers MICREX-SX series E-SX bus Product

#### High-speed counter unit

It is a separate mounting type high-speed counter that can be directly connected to the E-SX bus.

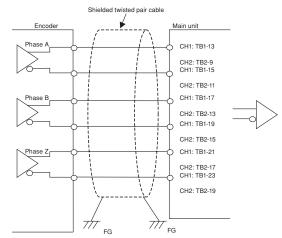
| Item                         | Specifications   |                |                |                |
|------------------------------|--|----------------|----------------|----------------|
| Model                        | NU2F-HC2   |                |                |                |
| Input format                 | 90-degree phase difference, 2-phase signal, 2-channel  |                |                |                |
| Power supply method          | E-SX bus cable (24 V DC)   |                |                |                |
| Signal type                  | Differential input   | Open collector | Open collector | Open collector |
| Rated voltage                | 5 V DC   | 5 V DC         | 12 V DC        | 24 V DC        |
| Response frequency           | 1MHz 250KHz  |                |                |                |
| Max. input frequency         | 4 Mbps   | ops 1 Mbps     |                |                |
| Counting range               | Signed 32-bit binary (-2147483648 to +2147483647)  |                |                |                |
| Counting operation mode      | Linear/ring operation, gate operation, preset operation latch operation, Z phase detection operation |                |                |                |
| Insulation method            | Photocoupler insulation  |                |                |                |
| External connections         | Detachable M3 screw terminal block   |                |                |                |
| Internal current consumption | Operating: 250 mA or less, Bypassing: 93mA or less   |                |                |                |
| Dimension<br>(W×H×D) [mm]    | 240 x 65 x 60 (except DIN rail mounting protrusions)   |                |                |                |
| Weight                       | Approx. 500 g  |                |                |                |

#### Integrated type interface module

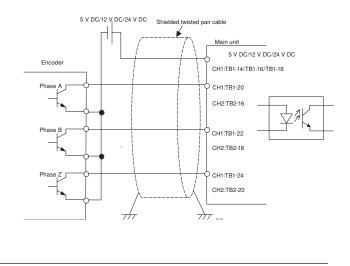
It can be mounted on the conventional SPH base board so that the SX bus connection device which is controlled by this module can be used as a module on the E-SX bus.

| Item                                 | Specifications  |  |  |
|--------------------------------------|---|--|--|
| Model                                | NP1L-RU1  | NP1L-RU1H  |  |
| Application                          | Connects modules connected to SX bus to E-SX bus  | Connects modules connected to SX bus to E-SX bus, and makes E-SX bus lines redundant |  |
| Connected CPU                        | SPH3000MG series,<br>SPH3000MM series   | SPH5000H series  |  |
| Number of connectible modules        | Max. 8 modules/E-SX bus<br>system   | Max. 32 modules/configuration  |  |
| Number of I/Os                       | 4096 words  | 4096 words   |  |
| E-SX bus connection<br>configuration | Bus connection  | Bus connection,<br>loop connection   |  |
| Base plate                           | Standard base<br>NP1B<br>Base with station number<br>setting function<br>NP1B S<br>* Use of base with live wire<br>removal function not possible. | Base with station number<br>setting function<br>NP1B                                 |  |
| USB port                             | For program support tool connection   |  |  |
| Internal current consumption         | 24V DC 140mA or less  | 24V DC 140mA or less   |  |
| Weight                               | Approx. 220 g   | Approx. 220 g  |  |

Differential input section wiring



Open collector input section wiring

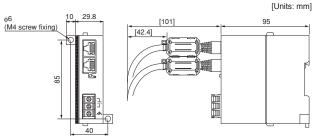


#### Auxiliary power supply unit

It is a separate mounting auxiliary unit to supply 24 V DC to the E-SX bus cable and to connect 5 or more units which are compatible with the E-SX bus to the E-SX bus connector of the CPU module.

| Item                            | Specifications  |  |  |
|---------------------------------|---|--|--|
| Model                           | NU2V-PA1  |  |  |
| No. of connectable              | Max. of 10 units on the E-SX bus (Max. of 8 m between main units) |  |  |
| modules                         | This one unit for 5 E-SX bus devices as a guide                   |  |  |
| Rated input voltage             | 24 V DC (external power supply is used)*1                         |  |  |
| Voltage tolerance               | 22.8 V DC to 27 V DC  |  |  |
| Overcurrent detection           | When an overcurrent is detected, the 24 V DC supply is stopped.   |  |  |
|                                 | To restart the power supply, press the reset switch.              |  |  |
| Internal current consumption    | No load: 70 mA or less, 10 units connected: 1 A or less           |  |  |
| Dimensions (W x H x D)<br>in mm | 50 × 95 × 95  |  |  |
| Weight                          | Approx. 150 g   |  |  |

Outline drawing of auxiliary power unit



\*1 Use a switching power supply (UL-specified product) of 24 V DC and 1.1 A for an external power supply.

### Digital Input Module: NP1X

#### Performance specifications

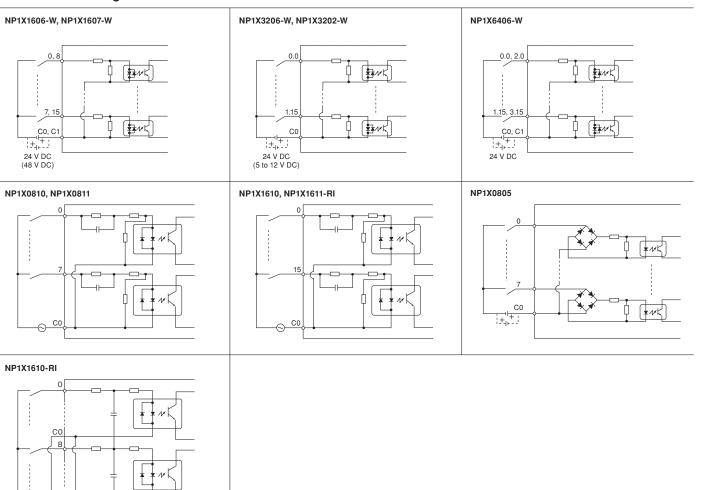
| Model       | Input       | No. of input | Rated voltage   | Rated     | Standard ope  | ration range | Input delay  | r time        | Insulation    | Status     | No. of points/ | External    | Internal current | Weight        |
|-------------|-------------|--------------|-----------------|-----------|---------------|--------------|--------------|---------------|---------------|------------|----------------|-------------|------------------|---------------|
|             | format      | points       |                 | current   | OFF→ON        | OFF→ON       | OFF→ON       | OFF→ON        | method        | indication | common         | connections | (24 V DC)        |               |
| NP1X0805 *  | DC input,   | 8 points     | 110 V DC        | 5 mA      | 80 to 140 V   | 0 to 22 V    | 1 to 1 ms, 3 | 3 to 3 ms     | Photocoupler  | LED        | 8 points x 1   | Terminal    | 35 mA or less    | Approx. 300 g |
| NP1X1606-W  | sink/source | 16 points    | 24 V DC         | 7 mA      | 15 to 30 V    | 0 to 5 V     | 3 to 10 ms,  | 10 to 10 ms   | insulation ON | indication | 8 points x 2   | block       | 35 mA or less    | Approx. 150 g |
| NP1X1607-W  |             |              | 48 V DC         | 5 mA      | 34 to 60 V    | 0 to 10 V    | 30 to 30 ms, | 100 to 100 ms | to OFF        |            |                |             | 35 mA or less    | Approx. 150 g |
| NP1X3206-W  |             | 32 points    | 24 V DC         | 4 mA      | 15 to 30 V    | 0 to 5 V     | Variable by  | /             |               |            | 32 points x 1  | Connector   | 50 mA or less    | Approx. 130 g |
| NP1X3202-W  |             |              | 5 to 12 V DC    | 3 to 9 mA | 3.5 to 13.2 V | 0 to 1 V     | parameter    | setting       |               |            |                |             | 50 mA or less    | Approx. 130 g |
| NP1X6406-W  |             | 64 points    | 24 V DC         | 4 mA      | 15 to 30 V    | 0 to 5 V     |              |               |               |            | 32 points x 2  |             | 85 mA or less    | Approx. 180 g |
| NP1X0810    | AC input    | 8 points     | 100 to 120 V AC | 10 mA     | 80 to 132 V   | 0 to 20 V    | Approx.      | Approx.       |               |            | 8 points x 1   | Terminal    | 35 mA or less    | Approx. 130 g |
| NP1X1610    |             | 16 points    |                 |           |               |              | 10 ms        | 10 ms         |               |            | 16 points x 1  | block       | 40 mA or less    | Approx. 170 g |
| NP1X0811    |             | 8 points     | 200 to 240 V AC |           | 160 to 264 V  | 0 to 40 V    |              |               |               |            | 8 points x 1   |             | 35 mA or less    | Approx. 130 g |
| NP1X1610-RI |             | 16 points    | 100 to 120 V AC | 7 mA      | 80 to 132 V   | 0 to 20 V    |              | Approx. 30 ms |               |            | 16 points x 1  |             | 40 mA or less    | Approx. 170 g |
| NP1X1611-RI |             |              | 200 to 240 V AC |           | 160 to 264 V  | 0 to 40 V    |              |               |               |            |                |             |                  | Approx. 180 g |

\* NP1X0805 occupies two slots of the base board.

#### Internal circuit diagram

CO

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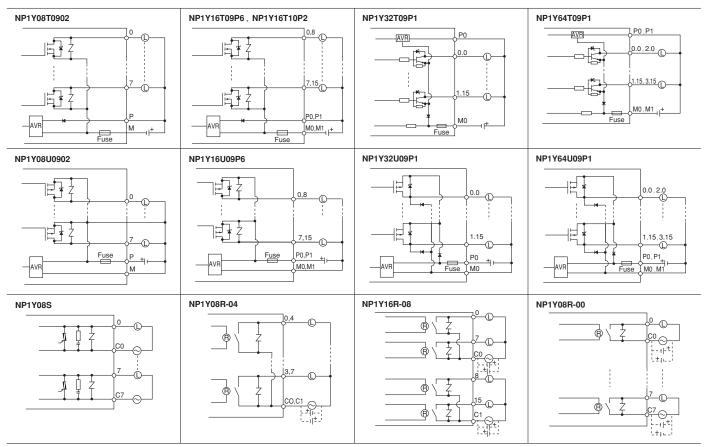


### Digital Output Module: NP1Y

#### Performance specifications

| Model       | Output       | No. of        | Rated                 | Max. loa   | d current  | Output dela   | y time        | Insulation          | Status     | No. of points/                 | Surge                       | External       | Internal current      | Weight        |
|-------------|--------------|---------------|-----------------------|--|--|---------------|---------------|---------------------|------------|--------------------------------|-----------------------------|----------------|-----------------------|---------------|
|             | format       | output points | voltage               | 1 point  | Common   | OFF→ON        | ON→OFF        | method              | indication | common                         | protection                  | connections    | consumption (24 V DC) |               |
| NP1Y08T0902 | Transistor   | 8 points      | 12 to                 | 2.4 A  | 8 A  | 1 ms or less  | 1 ms or less  | Photocoupler        | LED        | 8 points x 1                   | Varistor                    | Terminal block | 20 mA or less         | Approx. 150 g |
| NP1Y16T09P6 | output sink  | 16 points     | 24 V DC               | 0.6 A  | 4 A  |               |               | insulation          | indication | 8 points x 2                   |                             |                | 42 mA or less         | Approx. 160 g |
| NP1Y16T10P2 | type         |               | 48 V DC               | 0.2 A  | 1.6 A  |               |               |                     |            |                                |                             |                | 42 mA or less         | Approx. 160 g |
| NP1Y32T09P1 |              | 32 points     | 12 to                 | 0.12A  | 3.2 A  |               |               |                     |            | 32 points x 1                  | Zener diode                 | Connector      | 45 mA or less         | Approx. 130 g |
| NP1Y64T09P1 |              | 64 points     | 24 V DC               |  |  |               |               |                     |            | 32 points x 2                  |                             |                | 90 mA or less         | Approx. 180 g |
| NP1Y08U0902 | Transistor   | 8 points      | ]                     | 2.4 A  | 8 A  |               |               |                     |            | 8 points x 1                   | Varistor                    | Terminal block | 20 mA or less         | Approx. 150 g |
| NP1Y16U09P6 |              | 16 points     | ]                     | 0.6 A  | 4 A  |               |               |                     |            | 8 points x 2                   |                             |                | 43 mA or less         | Approx. 160 g |
| NP1Y32U09P1 | type         | 32 points     | ]                     | 0.12 A   | 3.2 A  |               |               |                     |            | 32 points x 1                  | Diode                       | Connector      | 45 mA or less         | Approx. 140 g |
| NP1Y64U09P1 | ]            | 64 points     | ]                     |  |  |               |               |                     |            | 32 points x 2                  |                             |                | 90 mA or less         | Approx. 180 g |
| NP1Y08S     | SSR output   | 8 points      | 100 to<br>240 V AC    | 2.2 A  | 2.2 A  | 10 ms or less | 10 ms or less |                     |            | All points are<br>independent. | CR absorber<br>and varistor | Terminal block | 80 mA or less         | Approx. 200 g |
| NP1Y08R-04  | Relay output | 8 points      | 110 V DC/<br>240 V AC | 30 V DC/<br>264 V AC:<br>2.2 A<br>110 V DC:<br>0.2 A | 30 V DC/<br>264 V AC:<br>4 A<br>110 V DC:<br>0.8 A | Approx. 10 ms | Approx. 10 ms | Relay<br>insulation |            | 4 points x 2                   | Varistor                    |                | 80 mA or less         | Approx. 150 g |
| NP1Y16R-08  |              | 16 points     |                       |  | 30 V DC/<br>264 V AC:<br>8 A<br>110 V DC:<br>1.6 A |               |               |                     |            | 8 points x 2                   |                             |                | 176 mA or less        | Approx. 190 g |
| NP1Y08R-00  |              | 8 points      |                       |  | -  |               |               |                     |            | All points are<br>independent. |                             |                | 100 mA or less        | Approx. 170 g |

#### Internal circuit diagram

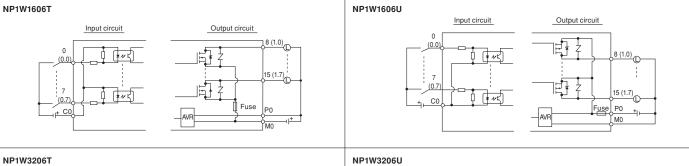


### Digital I/O Module: NP1W

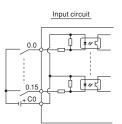
### Performance specifications

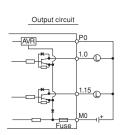
| Model     | Input            |                 |         |         |                | Output        |                  |         |              |              |                | Common       |            |                |                       |               |
|-----------|------------------|-----------------|---------|---------|----------------|---------------|------------------|---------|--------------|--------------|----------------|--------------|------------|----------------|-----------------------|---------------|
|           | Input format     | No. of          | Rated   | Rated   | No. of points/ | Output        | No. of           | Rated   | Max. load    | l current    | No. of points/ | Insulation   | Status     | External       | Internal current      | Weight        |
|           |                  | input<br>points | voltage | current | common         | format        | output<br>points | voltage | 1 point      | Common       | common         | method       | indication | connections    | consumption (24 V DC) |               |
| NP1W1606T | DC input,        | 8 point         | 24 V DC | 7 mA    | 8 points x 1   | Transistor    | 8 point          | 12 to   | 0.6 A/point  | 4 A/common   | 8 points x 1   | Photocoupler | LED        | Terminal block | 35 mA or less         | Approx. 150 g |
| NP1W3206T | source           | 16 point        |         | 4 mA    | 16 points x 1  | output sink   | 16 point         | 24 V DC | 0.12 A/point | 1.6 A/common | 16 points x 1  | insulation   | indication | Connector      | 50 mA or less         | Approx. 140 g |
| NP1W1606U | DC input,        | 8 point         |         | 7 mA    | 8 points x 1   | Transistor    | 8 point          |         | 0.6A/point   | 4 A/common   | 8 points x 1   |              |            | Terminal block | 35 mA or less         | Approx. 150 g |
| NP1W3206U | sink             | 16 point        |         | 4 mA    | 16 points      | output source | 16 point         |         | 0.12 A/point | 1.6 A/common | 16 points x 1  |              |            | Connector      | 50 mA or less         | Approx. 140 g |
| NP1W6406T | DC bidirectional | 32 point        |         | 4 mA    | 32 points x 1  | Transistor    | 32 point         |         | 0.12 A/point | 3.2 A/common | 32 points x 1  |              |            | Connector      | 90 mA or less         | Approx. 180 g |
|           | input            |                 |         |         |                | output sink   |                  |         |              |              |                |              |            |                |                       |               |
| NP1W6406U | DC bidirectional | 32 point        |         | 4 mA    | 32 points x 1  | Transistor    | 32 point         |         | 0.12 A/point | 3.2 A/common | 32 points x 1  |              |            | Connector      | 90 mA or less         | Approx. 180 g |
|           | input            |                 |         |         |                | output source |                  |         |              |              |                |              |            |                |                       |               |

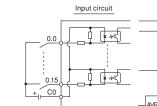
#### Internal circuit diagram



NP1W3206T







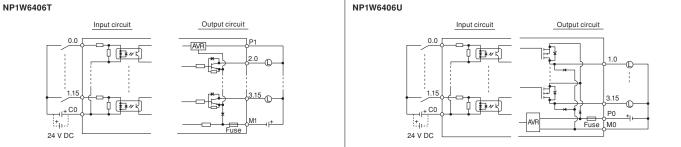
Output circuit 1 E

1.15

P0 +

Fuse M0

#### NP1W6406U



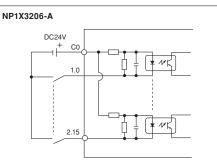
#### High-Speed Digital Input Module: NP1X3206-A

- · Digital input module with pulse catch input
- · Pulse catch input of min. 20  $\mu$ s or normal input
- Pulse counter input function of max. 20 kHz, 4 ch (2-phase)

#### Specifications

| Model      | Input       | No. of       | Rated   | Rated   | Standard ope | eration range | Input delay | time      | Insulation   | Status     | No. of points/ | External    | Internal current      | Weight        |
|------------|-------------|--------------|---------|---------|--------------|---------------|-------------|-----------|--------------|------------|----------------|-------------|-----------------------|---------------|
|            | format      | input points | voltage | current | OFF→ON       | ON→OFF        | OFF→ON      | ON→OFF    | method       | indication | common         | connections | consumption (24 V DC) |               |
| NP1X3206-A | 24V DC      | 32 points    | 24 V DC | 4 mA    | 15 to 30 V   | 0 to 5 V      | 0 to 100 m  | S         | Photocoupler | LED        | 32 points x 1  | Connector   | 50 mA or less         | Approx. 130 g |
|            | source type |              |         |         |              |               | Variable by | parameter | insulation   | indication |                |             |                       |               |
|            |             |              |         |         |              |               | setting     |           |              |            |                |             |                       |               |

#### Internal circuit diagram



#### Pulse Train Output Built-in Digital Output Module: NP1Y32T09P1-A

- · Module with transistor output and pulse train output built-in
- Pulse train output (20 kHz) can be selected up to max. 4 ch x 2 phases

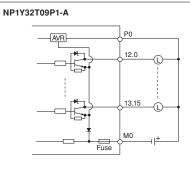
#### Specifications

| Ν | lodel        | Output     | No. of        | Rated   | Max. load | current | Output dela    | y time        | Insulation   | Status     | No. of points/ | Surge       | External    | Internal current      | Weight        |
|---|--------------|------------|---------------|---------|-----------|---------|----------------|---------------|--------------|------------|----------------|-------------|-------------|-----------------------|---------------|
|   |              | format     | output points | voltage | 1 point   | Common  | OFF→ON         | ON→OFF        | method       | indication | common         | protection  | connections | consumption (24 V DC) |               |
| Ν | P1Y32T09P1-A | Transistor | 32 point      | 12 to   | 0.12A     | 3.2 A   | Port 1 to 8: 2 | 20 µs or less | Photocoupler | LED        | 32 points x 1  | Zener diode | Connector   | 50 mA or less         | Approx. 200 g |
|   |              | output     |               | 24 V DC |           |         | Port 9 to 32:  | 1 ms or less  | insulation   | indication |                |             |             |                       |               |
|   |              | sink type  |               |         |           |         |                |               |              |            |                |             |             |                       |               |

#### Built-in pulse train output specifications

| Item                         | Specifications  |
|------------------------------|---|
| No. of pulse train           | 4 channels (max.) x 2 phases                          |
| output channels              | (Only with the pulse train output mode selected)      |
| Max. output frequency        | 20 kHz  |
| Pulse output mode            | (1) Forward pulse, reverse pulse                      |
|                              | (2) Pulse train + sign                                |
| Output pulse counting method | Built-in 16-bit up-down counter                       |
| Operation mode               | Start, stop, clear                                    |
|                              | Ring operation  |
|                              | Frequency/rotation direction/output form setting      |
| No. of general-purpose       | 32 points (min. 24 points in pulse train output mode) |
| output points                |   |

#### Internal circuit diagram



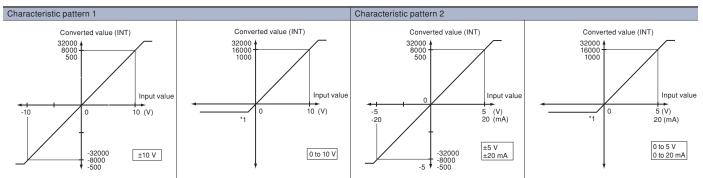
### Analog Input Module: NP1AX

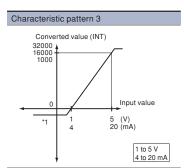
#### Performance specifications

| Model        | Input       | No. of   | Signal range     | Digital converted | Digital    | Tolerance                     | Converting | No. of occupied words | Insulation between | External    | Internal current      | Weight  |
|--------------|-------------|----------|------------------|-------------------|------------|-------------------------------|------------|-----------------------|--------------------|-------------|-----------------------|---------|
|              | format      | channels |                  | value             | resolution |                               | speed      | (input + output)      | channels           | connections | consumption (24 V DC) |         |
| NP1AX04-MR   | Multi-range | 4 ch     | -5 to +5 V DC    | -500 to +500      | 10 bits    | ±0.5% or less                 | 4 ms/      | 8 words +             | Non-insulation     | Terminal    | 120 mA or less        | Approx. |
|              | input       |          | 0 to 20 mA DC    | or                |            | (at 25°C)                     | 4 ch       | 2 words               |                    | block       |                       | 200 g   |
|              |             |          | 4 to 20 mA DC    | 0 to 1000         |            | ±1.0% or less                 |            |                       |                    |             |                       |         |
|              |             |          | -20 to +20 mA DC |                   |            | (at 0 to 55°C)                |            |                       |                    |             |                       |         |
| NP1AXH4-MR   |             |          | 0 to 5V DC       | -8000 to +8000    | 14 bits    | ±0.1% or less                 | 1 ms/      |                       |                    |             |                       |         |
|              |             |          | 0 to 10V DC      | or                |            | (at 25°C)                     | 4 ch       |                       |                    |             |                       |         |
|              |             |          | 1 to 5 V DC      | 0 to 16000        |            | ±1.0% or less                 |            |                       |                    |             |                       |         |
|              |             |          | -10 to +10 V DC  |                   |            | (at 0 to 55°C)                |            |                       |                    |             |                       |         |
| NP1AX08V-MR  |             | 8 ch     | 0 to 5V DC       | -500 to +500      | 10 bits    | ±0.5% or less                 | 5 ms/      | 16 words +            |                    |             |                       |         |
|              |             |          | 0 to 10V DC      | or                |            | (at 18 to 28°C)               | 8 ch       | 2 words               |                    |             |                       |         |
|              |             |          | 1 to 5 V DC      | 0 to 1000         |            | ±1.0% or less                 |            |                       |                    |             |                       |         |
|              |             |          | -5 to +5 V DC    |                   |            | (at 0 to 55°C)                |            |                       |                    |             |                       |         |
|              |             |          | -10 to +10 V DC  |                   |            |                               |            |                       |                    |             |                       |         |
| NP1AX08I-MR  |             |          | 0 to 20 mA DC    |                   |            |                               |            |                       |                    |             |                       |         |
|              |             |          | 4 to 20 mA DC    |                   |            |                               |            |                       |                    |             |                       |         |
|              |             |          | -20 to +20 mA DC |                   |            |                               |            |                       |                    |             |                       |         |
| NP1AXH8V-MR  |             |          | 0 to 5V DC       | 0 to 16000        | 14 bits    | ±0.1% or less (at 18 to 28°C) | 1.2 ms     | 8 words +             |                    |             | 200mA or less         | Approx. |
|              |             |          | 0 to 10V DC      |                   |            | ±0.2% or less (at 0 to 55°C)  | or less/   | 4 words               |                    |             |                       | 240 g   |
|              |             |          | 1 to 5 V DC      |                   |            | ±0.3% (at 0 to 55°C,          | 8 ch       |                       |                    |             |                       |         |
|              |             |          | -10 to +10 V DC  | -8000 to +8000    |            | 1 to 5 V range)               |            |                       |                    |             |                       |         |
| NP1AXH8I-MR  |             |          | 0 to 20 mA DC    | 0 to 16000        |            | ±0.1% or less (at 18 to 28°C) |            |                       |                    |             |                       |         |
|              |             |          | 4 to 20 mA DC    |                   |            | ±0.4% or less (at 0 to 55°C)  |            |                       |                    |             |                       |         |
|              |             |          | -20 to +20 mA DC |                   |            |                               |            |                       |                    |             |                       |         |
| NP1AXH8VG-MR |             |          | 0 to 5V DC       | -32000 to         | 16 bits    | ±0.05% or less                | 30 ms      |                       | Insulation         |             | 150mA or less         | Approx. |
|              |             |          | 0 to 10V DC      | +32000 or         |            | (at 18 to 28°C)               | or less/   |                       |                    |             |                       | 280 g   |
|              |             |          | 1 to 5 V DC      | 0 to 32000        |            | *1                            | 8 ch       |                       |                    |             |                       |         |
|              |             |          | -10 to +10 V DC  |                   |            |                               |            |                       |                    |             |                       |         |
| NP1AXH8IG-MR |             |          | 0 to 20 mA DC    |                   |            | ±0.239% or less               |            |                       |                    |             |                       |         |
|              |             |          | 4 to 20 mA DC    |                   |            | (at 10 to 55°C)               |            |                       |                    |             |                       |         |
|              |             |          | -20 to +20 mA DC |                   |            |                               |            |                       |                    |             |                       |         |

\*1 Take 40 minutes or more for warm-up (no need to warm-up for ±0.2%)

#### Characteristic diagram





\*1 For NP1AX04-MR and NP1AXH4-MR, the lower limit value (digital value) is "0".

#### Input value and converted value

| Input range  | Characte  | eristic patte | rn 1    | Characte  | eristic patte | rn 2    | Characte  | ristic patte | rn 3    |
|--------------|-----------|---------------|---------|-----------|---------------|---------|-----------|--------------|---------|
|              | Resolutio | on            |         | Resolutio | on            |         | Resolutio | on           |         |
|              | 10 bits   | 14 bits       | 16 bits | 10 bits   | 14 bits       | 16 bits | 10 bits   | 14 bits      | 16 bits |
| -5 to 5 V    |           |               |         | ±500      | ±8000         |         |           |              |         |
| 0 to 5 V     |           |               |         | 1000      | 16000         | 32000   |           |              |         |
| 1 to 5 V     |           |               |         |           |               |         | 1000      | 16000        | 32000   |
| 0 to 10 V    | 1000      | 16000         | 32000   |           |               |         |           |              |         |
| -10 to 10 V  | ±500      | ±8000         | ±32000  |           |               |         |           |              |         |
| 0 to 20 mA   |           |               |         | 1000      | 16000         | 32000   |           |              |         |
| 4 to 20 mA   |           |               |         |           |               |         | 1000      | 16000        | 32000   |
| -20 to 20 mA |           |               |         | ±500      | ±8000         | ±32000  |           |              |         |

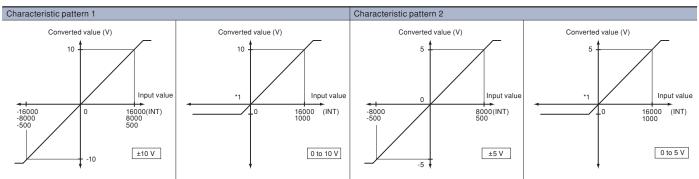
### Analog Output Module: NP1AY

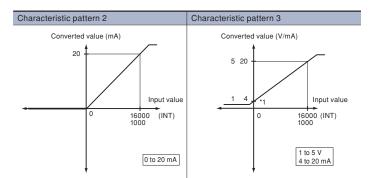
#### Performance specifications

| Model        | Output      | No. of   | Signal          | Digital          | Digital    | Tolerance                        | Converting | No. of occupied words | Insulation       | External       | Internal current      | Weight        |
|--------------|-------------|----------|-----------------|------------------|------------|----------------------------------|------------|-----------------------|------------------|----------------|-----------------------|---------------|
|              | format      | channels | range           | converted value  | resolution |                                  | speed      | (input + output)      | between channels | connections    | consumption (24 V DC) |               |
| NP1AY02-MR   | Multi-range | 2        | -5 to +5 V DC   | -500 to +500     | 10 bits    | ±0.5% or less (at 25°C)          | 2 ms/      | 2 words + 4 words     | Non-insulation   | Terminal block | 120 mA or less        | Approx. 200 g |
|              | output      |          | 0 to 20 mA DC   | or 0 to 1000     |            | ±1.0% or less                    | 2 ch       |                       |                  |                |                       |               |
|              |             |          | 4 to 20 mA DC   |                  |            | (at 0 to 55°C)                   |            |                       |                  |                |                       |               |
| NP1AYH2-MR   |             |          | 0 to 5 V DC     | -8000 to +8000   | 14 bits    | ±0.1% or less (at 25°C)          | 1 ms/      |                       |                  |                |                       |               |
|              |             |          | 0 to 10 V DC    | or 0 to 16000    |            | ±1.0% or less                    | 2 ch       |                       |                  |                |                       |               |
|              |             |          | 1 to 5 V DC     |                  |            | (at 0 to 55°C)                   |            |                       |                  |                |                       |               |
|              |             |          | -10 to +10 V DC |                  |            |                                  |            |                       |                  |                |                       |               |
| NP1AYH4V-MR  |             | 4        | 0 to 5V DC      | -8000 to +8000   |            | ±0.1% or less (at 18 to 28°C)    | 0.5 ms/    | 4 words + 4 words     |                  |                | 200 mA or less        | Approx. 240 g |
|              |             |          | 0 to 10 V DC    | or 0 to 16000    |            | ±0.2% or less (at 0 to 55°C)     | 4 ch       |                       |                  |                |                       |               |
|              |             |          | 1 to 5 V DC     |                  |            | ±0.3%                            |            |                       |                  |                |                       |               |
|              |             |          | -10 to +10 V DC |                  |            | (at 0 to 55°C, 1 to 5 V range)   |            |                       |                  |                |                       |               |
| NP1AYH4I-MR  |             |          | 0 to 20 mA DC   | 0 to 16000       | 15 bits    | ±0.1% or less (at 18 to 28°C)    | ]          |                       |                  |                |                       |               |
|              |             |          | 4 to 20 mA DC   |                  |            | ±0.4% or less (at 0 to 55°C)     |            |                       |                  |                |                       |               |
| NP1AYH4VG-MR |             |          | 0 to 5V DC      | -16000 to +16000 |            | ±0.1% or less (at 18 to 28°C) *1 | 0.6 ms/    |                       | Insulation       |                |                       | Approx. 300 g |
|              |             |          | 0 to 10V DC     | or 0 to 16000    |            | ±0.289% or less                  | 4 ch       |                       |                  |                |                       |               |
|              |             |          | 1 to 5 V DC     |                  |            | (at 0 to 55°C)                   |            |                       |                  |                |                       |               |
|              |             |          | -10 to +10 V DC |                  |            |                                  |            |                       |                  |                |                       |               |
| NP1AYH4IG-MR |             |          | 0 to 20 mA DC   | 0 to 16000       |            | ±0.1% or less (at 18 to 28°C) *1 | 1          |                       |                  |                | 250 mA or less        | 1             |
|              |             |          | 4 to 20 mA DC   |                  |            | ±0.289% or less (at 0 to 55°C)   |            |                       |                  |                |                       |               |
| NP1AYH8V-MR  |             | 8        | 0 to 5V DC      | -8000 to +8000   | 14 bits    | ±0.1% or less (at 18 to 28°C)    | 1 ms/      | 4 words + +8 words    | Non-insulation   | 1              | 240 mA or less        | Approx. 240 g |
|              |             |          | 0 to 10V DC     | or 0 to 16000    |            | ±0.2% or less (at 0 to 55°C)     | 8 ch       |                       |                  |                |                       |               |
|              |             |          | 1 to 5 V DC     |                  |            | ±0.3%                            |            |                       |                  |                |                       |               |
|              |             |          | -10 to +10 V DC |                  |            | (at 0 to 55°C, 1 to 5 V range)   |            |                       |                  |                |                       |               |
| NP1AYH8I-MR  | 1           |          | 0 to 20 mA DC   | 0 to 16000       |            | ±0.1% or less (at 18 to 28°C)    | 1          |                       |                  |                | 300 mA or less        | 1             |
|              |             |          | 4 to 20 mA DC   |                  |            | ±0.4% or less (at 0 to 55°C)     |            |                       |                  |                |                       |               |

\*1 Take 30 minutes or more for warm-up (no need to warm-up for ±0.2%)

#### Characteristic diagram





\*1 For NP1AY02-MR and NP1AYH2-MR, the lower limit value (digital value) is "0".

#### Output value and converted value

| Output range | Characte  | ristic patter | rn 1    | Characte  | ristic patter | n 2     | Characte  | ristic patter | m 3     |
|--------------|-----------|---------------|---------|-----------|---------------|---------|-----------|---------------|---------|
|              | Resolutio | n             |         | Resolutio | n             |         | Resolutio | n             |         |
|              | 10 bits   | 14 bits       | 15 bits | 10 bits   | 14 bits       | 15 bits | 10 bits   | 14 bits       | 15 bits |
| -5 to 5 V    |           |               |         | ±500      | ±8000         |         |           |               |         |
| 0 to 5 V     |           |               |         | 1000      | 16000         | 16000   |           |               |         |
| 1 to 5 V     |           |               |         |           |               |         | 1000      | 16000         | 16000   |
| 0 to 10 V    | 1000      | 16000         | 16000   |           |               |         |           |               |         |
| -10 to 10 V  | ±500      | ±8000         | ±16000  |           |               |         |           |               |         |
| 0 to 20 mA   |           |               |         | 1000      | 16000         | 16000   |           |               |         |
| 4 to 20 mA   |           |               |         |           |               |         | 1000      | 16000         | 16000   |

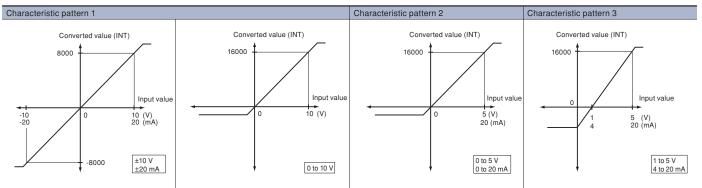
### Analog Input/Output Module: NP1AWH6-MR

#### Performance specifications

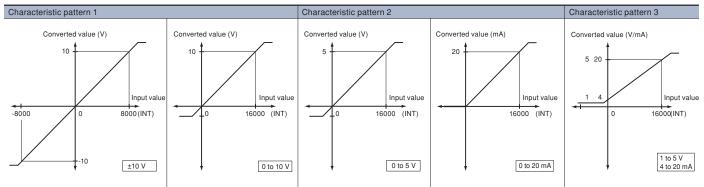
| Model      | I/O form    | No. of   | Signal range     | Digital converted | Digital    | Tolerance               | Converting | No. of occupied words | Insulation       | External       | Internal current      | Weight        |
|------------|-------------|----------|------------------|-------------------|------------|-------------------------|------------|-----------------------|------------------|----------------|-----------------------|---------------|
|            |             | channels |                  | value             | resolution |                         | speed      | (Input + output)      | between channels | connections    | consumption (24 V DC) |               |
| NP1AWH6-MR | Multi-range | 4        | Voltage input:   | -8000 to +8000 or | 14 bits    | ±0.1% or less           | 1 ms/      | 4 words + 4 words     | Non-insulation   | Terminal block | 200 mA or less        | Approx. 240 g |
|            | I/O         |          | 0 to 5 V DC      | 0 to 16000        |            | (at 18 to 28°C)         | 4 ch       |                       |                  |                |                       |               |
|            |             |          | 0 to 10 V DC     |                   |            | ±0.2% or less           |            |                       |                  |                |                       |               |
|            |             |          | 1 to 5 V DC      |                   |            | (at 0 to 55°C)          |            |                       |                  |                |                       |               |
|            |             |          | -10 to +10 V DC  |                   |            | ±0.3%                   |            |                       |                  |                |                       |               |
|            |             |          | Current input:   |                   |            | (0 to 55°C, 0 to 20 mA/ |            |                       |                  |                |                       |               |
|            |             |          | 0 to 20 mA DC    |                   |            | 4 to 20 mA ranges)      |            |                       |                  |                |                       |               |
|            |             |          | 4 to 20 mA DC    |                   |            |                         |            |                       |                  |                |                       |               |
|            |             |          | -20 to +20 mA DC |                   |            |                         |            |                       |                  |                |                       |               |
|            |             | 2        | Voltage output:  |                   |            |                         | 0.5 ms/    |                       |                  |                |                       |               |
|            |             |          | 0 to 5 V DC      |                   |            |                         | 2 ch       |                       |                  |                |                       |               |
|            |             |          | 0 to 10 V DC     |                   |            |                         |            |                       |                  |                |                       |               |
|            |             |          | 1 to 5 V DC      |                   |            |                         |            |                       |                  |                |                       |               |
|            |             |          | -10 to +10 V DC  |                   |            |                         |            |                       |                  |                |                       |               |
|            |             |          | Current output:  |                   |            |                         |            |                       |                  |                |                       |               |
|            |             |          | 0 to 20 mA DC    |                   |            |                         |            |                       |                  |                |                       |               |
|            |             |          | 4 to 20 mA DC    |                   |            |                         |            |                       |                  |                |                       |               |

#### Characteristic diagram

Analog input



### Analog output



#### Input/output value and converted value

· Analog input

| Input range  | Characteristic pattern 1 | Characteristic pattern 2 | Characteristic pattern 3 |
|--------------|--------------------------|--------------------------|--------------------------|
| 0 to 5 V     |                          | 16000                    |                          |
| 1 to 5 V     |                          |                          | 16000                    |
| 0 to 10 V    | 16000                    |                          |                          |
| -10 to 10 V  | ±8000                    |                          |                          |
| 0 to 20 mA   |                          | 16000                    |                          |
| 4 to 20 mA   |                          |                          | 16000                    |
| -20 to 20 mA | ±8000                    |                          |                          |

#### · Analog output

| Output range | Characteristic pattern 1 | Characteristic pattern 2 | Characteristic pattern 3 |
|--------------|--------------------------|--------------------------|--------------------------|
| 0 to 5 V     |                          | 16000                    |                          |
| 1 to 5 V     |                          |                          | 16000                    |
| 0 to 10 V    | 16000                    |                          |                          |
| -10 to 10 V  | ±8000                    |                          |                          |
| 0 to 20 mA   |                          | 16000                    |                          |
| 4 to 20 mA   |                          |                          | 16000                    |

#### **Resistance Thermometer Element Input Module: NP1AX**-PT

• IEC Standards conformed sensors (platinum resistance thermometer bulb) can be connected. (Batch setting is possible for all channels.)

• Error detection (resistance thermometer element wire breakage detection, resistance thermometer element shunt detection, etc.) is possible.

- · Temperature scale is selectable between Celsius and Fahrenheit.
- The NP1AXH6G-PT provides high accuracy and high resolution, thereby enabling fine-grained measurements.

#### Specifications

| Item                              | Specifications   |   |
|-----------------------------------|--|---|
| Model                             | NP1AXH4-PT   | NP1AXH6G-PT   |
| Measurement accuracy *2           | ±0.3% (ambient temperature 18 to 28°C <sup>*1</sup>        | ±0.05 to ±0.07% (ambient temperature 18 to 28°C)  |
|                                   | ±0.7% (ambient temperature 0 to 55°C)                      | ±0.239% (ambient temperature 0 to 55°C)   |
| Allowable input wiring resistance | 10 Ω or less   | 20 Ω or less  |
| Sampling interval                 | 500 ms/4 ch  | 45 ms/6 ch  |
| Input filtering time              | Hardware (time constant): 50 ms                            | Hardware (time constant): 30 ms   |
|                                   | Software filter: 1 s (variable from 1 to 100 s by program) | Software filter: 1 to 100 s, Moving average over: 4 times, 8 times, 16 times, 32 times. |
|                                   |  | (Configurable per 1s unit. Default value: Moving average over 32 times)                 |
| No. of input channels             | 4 ch (insulation between channels)                         | 6 ch (insulation between channels)  |
| No. of occupied I/O points        | Input: 8 words, output: 8 words                            | Input: 8 words, output: 4 words   |
| Internal current consumption      | 150 mA or less   | 150 mA or less  |
| External connections              | Detachable terminal block M3, 20 poles                     | Detachable terminal block M3, 20 poles  |
| Weight                            | Approx. 240 g  | Approx. 300 g   |

NP1AXH6G-PT

Туре

Platinum resistance thermometer element

\*1 In the range from 0.0 to 100.0°C, and from -20.0 to 80.0°C, full scale ±0.4% ±1 Digit (ambient temperature: 18 to 28°C), ±0.8% ±1 Digit (ambient temperature: 0 to 55°C). \*2 For more information, refer to the User's Manual: FEH208.

# Type of resistance thermometer element and resolutions NP1AXH4-PT

| Type of resistance              | Celsius (°C)    | Fahrenheit (°F)  | Resolution |
|---------------------------------|-----------------|------------------|------------|
| thermometer element Input range |                 | Input range      | of data    |
| PT                              | 0 to 200        | 32 to 392        | 1          |
|                                 | -20 to 80       | -4 to 176        |            |
|                                 | 0 to 100        | 32 to 212        |            |
|                                 | 0 to 400        | 32 to 752        |            |
|                                 | -200 to 200     | -328 to 392      |            |
|                                 | -200 to 600     | -328 to 1112     |            |
|                                 | 0.0 to 200.0    | 32.0 to 392.0    | 0.1        |
|                                 | -20.0 to 80.0   | -4.0 to 176.0    |            |
|                                 | 0.0 to 100.0    | 32.0 to 212.0    | 1          |
|                                 | 0.0 to 400.0    | 32.0 to 752.0    |            |
|                                 | -200.0 to 200.0 | -328.0 to 392.0  |            |
|                                 | -200.0 to 600.0 | -328.0 to 1112.0 |            |
| JPt                             | 0 to 200        | 32 to 392        | 1          |
|                                 | -20 to 80       | -4 to 176        |            |
|                                 | 0 to 100        | 32 to 212        |            |
|                                 | 0 to 400        | 32 to 752        |            |
|                                 | -200 to 200     | -328 to 392      |            |
|                                 | -200 to 500     | -328 to 932      |            |
|                                 | 0.0 to 200.0    | 32.0 to 392.0    | 0.1        |
|                                 | -20.0 to 80.0   | -4.0 to 176.0    |            |
|                                 | 0.0 to 100.0    | 32.0 to 212.0    |            |
|                                 | 0.0 to 400.0    | 32.0 to 752.0    |            |
|                                 | -200.0 to 200.0 | -328.0 to 392.0  | ]          |
|                                 | -200.0 to 500.0 | -328.0 to 932.0  |            |

| PT  | 0 to 200        | 32 to 392        | 1    |
|-----|-----------------|------------------|------|
|     | -20 to 80       | -4 to 176        |      |
|     | 0 to 100        | 32 to 212        |      |
|     | 0 to 400        | 32 to 752        |      |
|     | -200 to 200     | -328 to 392      |      |
|     | -200 to 600     | -                |      |
|     | -200 to 850     | -328 to 1562     |      |
|     | 0.0 to 200.0    | 32.0 to 392.0    | 0.1  |
|     | -20.0 to 80.0   | -4.0 to 176.0    |      |
|     | 0.0 to 100.0    | 32.0 to 212.0    |      |
|     | 0.0 to 400.0    | 32.0 to 752.0    |      |
|     | -200.0 to 200.0 | -                |      |
|     | -200.0 to 600.0 | -328.0 to 1112.0 |      |
|     | -200.0 to 850.0 | -328.0 to 1562.0 |      |
|     | -20.00 to 80.00 | -4.00 to 176.00  | 0.01 |
| JPt | 0 to 200        | 32 to 392        | 1    |
|     | -20 to 80       | -4 to 176        |      |
|     | 0 to 100        | 32 to 212        |      |
|     | 0 to 400        | —                |      |
|     | -200 to 200     | -328 to 392      |      |
|     | -200 to 500     | -328 to 932      |      |
|     | 0.0 to 200.0    | 32.0 to 392.0    | 0.1  |
|     | -20.0 to 80.0   | -4.0 to 176.0    |      |
|     | 0.0 to 100.0    | 32.0 to 212.0    |      |
|     | 0.0 to 400.0    | 32.0 to 752.0    |      |
|     | -200.0 to 200.0 | -328.0 to 392.0  |      |
|     | -200.0 to 500.0 | -328.0 to 932.0  |      |

Celsius (°C)

Input range

Resolution

of data

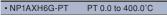
Fahrenheit (°F)

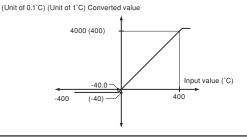
Input range

Note: The measuring range of temperature is  $\pm 5\%$  of the input range span.

#### Characteristic diagram

#### • NP1AXH4-PT PT 0.0 to 400.0 °C (Unit of 0.1 °C) (Unit of 1 °C) Converted value 4000 (400) -20 -20 (-20) 400 (°C)





### Thermo-Couple Input Module: NP1AXH□□-TC

 The following thermocouples that conform to IEC, ASTN and DIN Standards can be connected. (Batch setting is possible for all channels.)

JIS standards: R, K, J, S, B, E, T, N IEC standards: R, K, J, S, B, E, T, N

ASTM standards: W5Re, W26Re, PL II DIN standards: U, L

- Error detection (the detection of sensor wire breakage) is possible.
- Temperature scale is selectable between Celsius and Fahrenheit.
- The NP1AXH8G-TC provides high accuracy and high resolution, thereby enabling fine-grained measurements.

#### Specifications

| Item                               | Specifications  |   |  |
|------------------------------------|---|---|--|
| Model                              | NP1AXH4-TC  | NP1AXH8G-TC   |  |
| Measurement accuracy *3            | ±0.3% (ambient temperature 18 to 28°C) *1               | ±0.05% (ambient temperature 25°C) *2                      |  |
|                                    | ±0.7% (ambient temperature 0 to 55°C)                   |   |  |
| Cold contact compensation accuracy | ±1°C (ambient temperature 18 to 28°C)                   | ±1°C (ambient temperature 18 to 28°C)                     |  |
| Sampling interval                  | 500 ms/4 ch   | 60 ms/8 ch  |  |
| Input filtering time               | Hardware (time constant): 50 ms                         | Hardware (time constant): 30 ms                           |  |
|                                    | Digital filter: 1s (variable from 1 to 100s by program) | Digital filter: 1 s (variable from 1 to 100 s by program) |  |
| No. of input channels              | 4 ch (insulation between channels)                      | 8 ch (insulation between channels)                        |  |
| No. of occupied words              | Input: 8 words, output: 8 words                         | Input: 8 words, output: 4 words                           |  |
| Internal current consumption       | 150 mA or less  | 150 mA or less  |  |
| External connections               | Detachable terminal block M3, 20 poles                  | Detachable terminal block M3, 20 poles                    |  |
| Weight                             | Approx. 240 g   | Approx. 300 g   |  |

\*1 In the range from K (0.0 to 400.0°C, 0.0 to 500.0°C, and from 0.0 to 800.0°C), and T (0.0 to 400.0°C), full scale ±0.4% (ambient temperature: 18 to 28°C), ±0.8% (ambient temperature: 0 to 55°C). \*2 The measurement accuracy depends on the sensor, and measurement temperature.

\*3 For more information, refer to the User's Manual: FEH209.

### Thermo-couple types and resolutions

#### • NP1AXH4-TC

| Thermo-couple type | Celsius (°C) | Fahrenheit (°F) | Resolution |
|--------------------|--------------|-----------------|------------|
| menno-coupie type  | Input range  | Input range     | of data    |
| К                  | 0 to 1300    | 32 to 2372      | 1          |
|                    | 0 to 500     | 32 to 932       | ]          |
|                    | 0 to 800     | 32 to 1472      |            |
|                    | 0.0 to 400.0 | 32.0 to 752.0   | 0.1        |
|                    | 0.0 to 500.0 | 32.0 to 932.0   | ]          |
|                    | 0.0 to 800.0 | 32.0 to 1472.0  |            |
| В                  | 0 to 1800    | 32 to 3272      | 1          |
| R                  | 0 to 1700    | 32 to 3092      |            |
| S                  | 0 to 1700    | 32 to 3092      |            |
| E                  | 0 to 400     | 32 to 752       |            |
|                    | 0 to 700     | 32 to 1292      |            |
|                    | 0.0 to 700.0 | 32.0 to 1292.0  | 0.1        |
| J                  | 0 to 500     | 32 to 932       | 1          |
|                    | 0 to 800     | 32 to 1472      |            |
|                    | 0.0 to 400.0 | 32.0 to 752.0   | 0.1        |
|                    | 0.0 to 500.0 | 32.0 to 932.0   |            |
|                    | 0.0 to 800.0 | 32.0 to 1472.0  |            |
| Т                  | 0 to 400     | 32 to 752       | 1          |
|                    | 0.0 to 400.0 | 32.0 to 752.0   | 0.1        |
| Ν                  | 0 to 1300    | 32 to 2372      | 1          |
| U                  | 0 to 400     | 32 to 752       |            |
|                    | 0 to 600     | 32 to 1112      |            |
|                    | 0.0 to 600.0 | 32.0 to 1112.0  | 0.1        |
| L                  | 0 to 400     | 32 to 752       | 1          |
|                    | 0 to 900     | 32 to 1652      |            |
|                    | 0.0 to 400.0 | 32.0 to 752.0   | 0.1        |
|                    | 0.0 to 900.0 | 32.0 to 1652.0  | ]          |
| PL II              | 0 to 1200    | 32 to 2372      | 1          |
| W5Re, W26Re        | 0 to 2300    | 32 to 4172      | 1          |

Note: The measuring range of temperature is  $\pm 5\%$  of the input range span.

#### • NP1AXH8G-TC

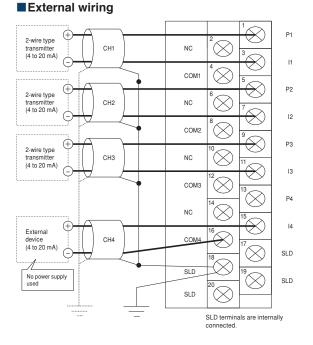
| Thermo-couple type | Celsius (°C)      | Fahrenheit (°F)  | Resolution |  |
|--------------------|-------------------|------------------|------------|--|
|                    | Input range       | Input range      | of data    |  |
| К                  | -200 to 1370      | -328 to 2498 1   |            |  |
|                    | -200 to 500       | -328 to 932      |            |  |
|                    | -100.0 to 1370.0  | -148.0 to 2498.0 | 0.1        |  |
|                    | -100.0 to 500.0   | -148.0 to 932.0  |            |  |
|                    | -100.0 to 230.0   | -148.0 to 446.0  |            |  |
|                    | 0.00 to 300.00    | —                | 0.05       |  |
| В                  | 0 to 1820         | 32 to 3308       | 1          |  |
| R                  | -50 to 1760       | -58 to 3200      |            |  |
| S                  | -50 to 1760       | -58 to 3200      |            |  |
| E                  | -250 to 1000      | -418 to 1832     |            |  |
|                    | -120.0 to 1000.0  | -184.0 to 1832.0 | 0.1        |  |
|                    | -120.00 to 160.00 | —                | 0.03       |  |
| J                  | -200 to 500       | -328 to 932      | 1          |  |
|                    | -200 to 800       | -328 to 1472     |            |  |
|                    | -200 to 1100      | -328 to 2012     |            |  |
|                    | -100.0 to 500.0   | -148.0 to 932.0  | 0.1        |  |
|                    | -100.0 to 800.0   | -148.0 to 1472.0 |            |  |
|                    | -100.0 to 1100.0  | -148.0 to 2012.0 |            |  |
|                    | -80.00 to 180.00  | —                | 0.04       |  |
| Т                  | -260 to 400       | -436 to 752      | 1          |  |
|                    | -150.0 to 200.0   | -238.0 to 392.0  | 0.1        |  |
| N                  | -200 to 1300      | -328 to 2372     | 1          |  |
| U                  | -150 to 550       | -238 to 1022     |            |  |
|                    | 0.0 to 550.0      | 32.0 to 1022.0   | 0.1        |  |
| L                  | -150 to 400       | -238 to 752      | 1          |  |
|                    | -150 to 850       | -238 to 1562     |            |  |
|                    | 0.0 to 400.0      | 32.0 to 752.0    | 0.1        |  |
|                    | 0.0 to 850.0      | 32.0 to 1562.0   |            |  |
| PL II              | 0 to 1300         | 32 to 2372       | 1          |  |
|                    | 0.0 to 1300.0     | 32.0 to 2372.0   | 0.1        |  |
| W5Re, W26Re        | 0 to 2300         | 32 to 4172       | 1          |  |

#### **Distributor Module: NP1AXH4DG-MR**

- Converts signals (4 to 20 mA) from two-wire transmitters, such as differential pressure flow meters, water gauges, and temperature communicators, into digital data.
- A transducer is unnecessary as the module is insulated with high pressure-resistance (1000 V AC) between channels.
- An external power supply is unnecessary as a power supply for two-wire transmitters is embedded in each channel.
- · Provides high precision and high resolution, thereby allowing detailed measurement.
- The square root extraction function allows you to input the data directly as like an industry value, to items such as the output from
  differential pressure flow meters and other devices that need to extract the square root.
- It can be also used as 4 channels of an insulation AI (amperage: 0 to 20 mA, 4 to 20 mA).
- A product compatible with the flow rate pulse input is also prepared (format: NP1F-Pl4).

#### Specifications

| Item                     | Specifications  |  |  |
|--------------------------|---|--|--|
| Model                    | NP1AXH4DG-MR  |  |  |
| No. of input points      | 4 points  |  |  |
| Analog input range       | 4 to 20 mA, 0 to 20 mA  |  |  |
| Input impedance          | 250 Ω   |  |  |
| Max. allowable voltage   | 30 mA   |  |  |
| Input filter             | Approx. 200 µs or less (Hardware: Primary delay time constant)                          |  |  |
| Resolution               | 16 bits   |  |  |
| Digital conversion value | 0 to 32000  |  |  |
| (INT model)              |   |  |  |
| Reference precision      | ±0.1% of F.S.R (Ta = 25°C)  |  |  |
| Temperature coefficient  | ±0.007%/°C  |  |  |
| Conversion cycle         | 30 ms/4 ch  |  |  |
| Warm up time *1          | 40 minutes or more  |  |  |
| Power supply for         | 1) Output voltage: 24 V DC ±15%   |  |  |
| transmission             | 2) Permissible current: 23 mA or less   |  |  |
| machine *2               | 3) Short-circuit limitation current: Approx. 25 mA                                      |  |  |
|                          | 4) Ripple noise: Approx. 250 mV (p-p) or less   |  |  |
|                          | 5) Suddenly change of the load: 4V (0-P) or less  |  |  |
|                          | (condition of the suddenly change of the load: 0 to 23 mA)                              |  |  |
| Response time *3         | Conversion cycle + tact cycle (ms)  |  |  |
| No. of occupied words    | Input: 8 words + output 4 words   |  |  |
| Insulation method        | Photo-coupler insulation or transformer insulation (Between I/O terminals and FG        |  |  |
|                          | Between analog input terminal and channel: Transformer insulated                        |  |  |
| Dielectric strength      | 1000 V AC, 1 minute, between I/O terminals and FG (short circuit current: 10 mA)        |  |  |
|                          | 1000 V AC, 1 minute, between analog input terminals and channels                        |  |  |
|                          | (short circuit current: 10 mA)  |  |  |
| Insulation resistance    | 10 $M\Omega$ or more with 500 V DC megger, between I/O terminals and FG                 |  |  |
|                          | 10 M $\Omega$ or more with 500 V DC megger, between analog input terminals and channels |  |  |
| Internal current         | 390 mA or less (When the transmission machine power supply used.)                       |  |  |
| consumption              | 170 mA or less (When the transmission machine power supply unused.)                     |  |  |
| Non-use output treatment | Basically, open   |  |  |
| Applicable cable         | Use the twisted pair wire with the shield. (Wiring length: 500 m or less)               |  |  |
| Weight                   | Approx. 290 g   |  |  |
| External connections     | Detachable screw terminal block (M3 x 20 poles)   |  |  |



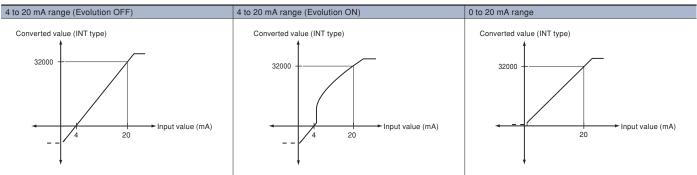
\*1 Reference precision = 0.22% (no need to warm-up when Ta = 25°C)
\*2 This can be reduced depending on the used number of transmission machine power supply. For more information, refer to the User's Manual: FEH432. An ambient temperature during short circuit should be 40°C or less.

(40 to 50°C: 10 minutes or less)

\*3 For a step response,

response time = 30 ms x average number of movements + 20 ms + input filter x 8 + tact cycle = 55.6 ms (no movement averaging, 5 ms tact cycle)

### Characteristic diagram



Note: The broken line represents the saturated area. Inputs below 0.8 mA may not be measured accurately.

### Duplex Analog Output Module: NP1AYH8VHR-MR

#### Features

- Duplication of analog output
  - $\cdot\,$  Analog output can be duplicated with the duplex switch control signal.
  - · Switching from the operation to the waiting can be performed by the application program or the front switch.
  - · The status of operation and waiting can be confirmed with the OUT LED on the front face of the module.
  - · The terminal block drop detection function is built in.
- Duplication of analog output by the instruction from the 2-system or 3-system of controller.

Operation instruction is available from controllers (max. of 3 systems) of different configurations to this module via the communication module.

| Operation mode                 | Overview  |
|--------------------------------|---|
| Single mode                    | Output data are provided by 1 unit of CPU and are D/A-converted.  |
| DUPLEX mode (CPU duplication)  | One of output data provided by 2 units of CPU is selected and D/A-converted.                                      |
| DUAL mode (CPU duplication)    | A mid value is selected from output data provided by 2 units of CPU and previous output value, and D/A-converted. |
| Triple mode (CPU triplication) | A mid value is selected from output data provided by 3 units of CPU, and D/A-converted.                           |

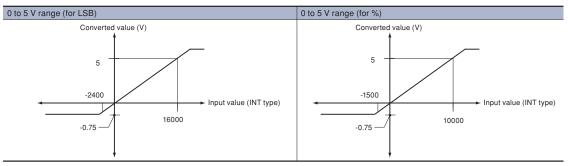
· High speed and high accuracy

High-speed conversion period of 3.2 ms/8 ch and high standard accuracy of ±0.25% enable a detailed control.

#### Specifications

| Model                        | NP1AYH8VHR-MR  |                 |              |               |
|------------------------------|--|-----------------|--------------|---------------|
| No. of output points         | 8 points   |                 |              |               |
| Analog output range          | 0 to 5 V   | 1 to 5 V        | 0 to 10 V    | -10 to +10 V  |
| Load impedance               | 500 Ω or more  |                 | 1 kΩ or more |               |
| Max. resolution              | 1.25 mV  |                 |              |               |
| Digital conversion           | 0 to 16000   |                 | 0 to 16000   | -8000 to 8000 |
| Total accuracy               | ±0.25% of F.S.R  |                 |              |               |
| Temperature coefficient      | ±0.007%/°C   |                 |              |               |
| Max. noise deviation         | ±0.6% of F.S.R   |                 |              |               |
| Conversion cycle             | 3.2 ms/8 points  |                 |              |               |
| Response time                | Conversion cycle + tact cycle (ms)   |                 |              |               |
| Load short protection        | Provided   |                 |              |               |
| No. of occupied words        | Input: 16 W + output: 34 W   |                 |              |               |
| Insulation method            | Between analog input terminal and FG: Photocoupler/transformer insulated                             |                 |              |               |
| Dielectric strength          | 500 V AC, 1 minute, between analog output terminals and FG (short-circuit current: 10 mA)            |                 |              |               |
| Insulation resistance        | 10 M $\Omega$ or more with the 500 V DC of DC megger between total analog output terminals and FG    |                 |              |               |
| Internal current consumption | 200 mA or less (at rated load)   |                 |              |               |
| Non use output treatment     | Basically, open  | Basically, open |              |               |
| Applicable cable             | Analog output cable<br>Use an AWG #22 to 18 shielded twisted pair line.                              |                 |              |               |
| Applicable cable             | Duplex switch signal cable (max. wire distance: 5m)<br>Use an AWG #22 to 18 shielded straight cable. |                 |              |               |
| Weight                       | Approx. 260 g  |                 |              |               |
| External connections         | Detachable screw terminal block (M3 x 20 poles)  |                 |              |               |
| Dimension                    | W35 x H105 x D111 mm (26 mm protrusion)  |                 |              |               |

#### Characteristic diagram



#### I/O Connection of Connector-Type Modules

The following types of modules are connected using connectors and recommended for the I/O connection use.

#### Connector type module list

| Item   | Model (ordering code)  | Specifications  |
|--|--|---|
| Digital input module   | NP1X3206-A   | 24 V DC, 32 points, 4 mA 0 ms to 100 ms variable, with 20 kHz x 4 ch. built-in pulse counter              |
|  | NP1X3206-W   | 24 V DC, 32 points, 4 mA 1 ms to 100 ms variable  |
|  | NP1X3202-W   | 5/12 V DC, 32 points, 3/9 mA, 1 to 100 ms variable  |
|  | NP1X6406-W   | 24 V DC, 64 points, 4 mA 1 ms to 100 ms variable  |
| Digital output module  | NP1Y32T09P1-A  | Tr. Sink, 24 V DC, 32 points, 0.12 A/point, 3.2 A/common, with 20 kHz x 4 ch. built-in pulse train output |
|  | NP1Y32T09P1  | Transistor sink, 12 to 24 V DC, 32 points, 0.12 A/point, 3.2 A/common                                     |
|  | NP1Y64T09P1  | Transistor sink, 12 to 24 V DC, 64 points, 0.12 A/point, 3.2 A/common                                     |
|  | NP1Y32U09P1  | Transistor source, 12 to 24 V DC, 32 points, 0.12 A/point, 3.2 A/common                                   |
|  | NP1Y64U09P1  | Transistor source, 12 to 24 V DC, 64 points, 0.12 A/point, 3.2 A/common                                   |
| Digital I/O mixed module   | NP1W3206T  | 24 V DC, 16-point source input, 12 to 24 V DC, Tr sink 16-point output                                    |
|  | NP1W3206U  | 24 V DC, 16-point sink input, 12 to 24 V DC, Tr source 16-point output                                    |
| NP1W6406   |  | 24 V DC, 32-point bidirectional input, 12 to 24 V DC, Tr sink 32-point output                             |
|  | NP1W6406U  | 24 V DC, 32-point bidirectional input, 12 to 24 V DC, Tr source 32-point output                           |
| High-speed counter module  | NP1F-HC2   | 500 kHz x 2 ch, 90-degree phase difference 2-phase signal, pulse + directional signal, others             |
| Multi-channel high-speed counter module  | NP1F-HC8   | 50kHz x 8 ch, 90-degree phase difference 2-phase signal, pulse + directional signal, others               |
| Pulse train output positioning control module  | NP1F-HP2   | Pulse train command 250 kHz x 2 ch.   |
| Two-axis pulse train multiple positioning control module: (open collector output)  | NP1F-MP2   | output pulse: 250 kHz, feedback pulse: 500 kHz  |
| Two-axis pulse train multiple positioning control module:  | se train multiple positioning control module: NP1F-HD2 output pulse: 5 MHz |   |
| (differential output)  | output) NP1F-HD2A output pulse: 5 MHz, feedback pulse: 5 MHz               |   |
| Two-axis analog multiple positioning control module  | NP1F-MA2   | feedback pulse: 500 kHz   |
| Four-axis pulse train multiple positioning control module: (differential output) NP1F-HD4 output pulse: 5 MHz, feedback pulse: 5 MHz |  | output pulse: 5 MHz, feedback pulse: 5 MHz  |

Note: Connector model implemented in the module is FCN-365P040-AU (plug) manufactured by Fujitsu Component Ltd.

#### Recommended connectors

| Types                        | Model (Fujitsu Component Ltd.) |  |  |
|------------------------------|--------------------------------|--|--|
|                              | Jack                           | Cover  |  |
| Soldered type*1              | FCN-361J040-AU                 | FCN-360C040-B (B type)                           |  |
| Crimp type                   | FCN-363J040 (Housing)          | FCN-360C040-D (D type: Wide mouthed type)        |  |
|                              | FCN-363J-AU (Contact)          | FCN-360C040-E (E type: Long screw type)          |  |
| Wire wrapping type           | FCN-362J040-AU                 | FCN-360C040-J2 (J2 type: Thinly, obliquely type) |  |
| Insulation displacement type | FCN-367J040-AU/FW              | The cover is not necessary.                      |  |

\*1 Fuji Electric solder type connector (NP8V-CN) is prepared (cover attached: FCN-360C040-B).

Note: For more details, refer to each manual.

#### Recommended relay terminal blocks (Fuji Electric Technica Co., Ltd.)

#### · Type/model/ordering code

ctor No. /Term

2 3 4 5 6

23

nal block No

7 8 9 10

30 31

I I 10 11

32 33 34 35 36 37

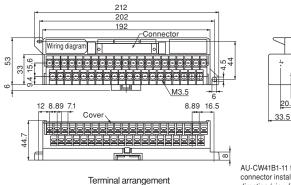
8 9 10 11 12 13 14 15 16 17

18 19 20

#### · Main unit

| Model        | Number of<br>terminal<br>block poles | Number of<br>connector<br>poles | Rating<br>(Connector)   | Performance   | Ordering code |
|--------------|--------------------------------------|---------------------------------|---|---|---------------|
| AU-CW41B1-11 | 41                                   | 40                              | Insulation voltage:<br>60 V (AC, DC)<br>Thermal current:<br>1 A (at 40°C) | $ \begin{array}{ll} \mbox{Insulation resistance:} \\ 100 \ M\Omega \ or \ more \\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $ | LP1W-41BA5    |

#### Outline dimensional drawing (AU-CW41B1-11 type)



AU-CW41B1-11 type connector installation direction (view from the engagement surface)

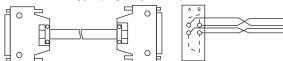
#### · Connection cable

| Applied terminal block type | No. of poles | Cable type            | Connection cable type | Ordering code |
|-----------------------------|--------------|-----------------------|-----------------------|---------------|
| AU-CW41B1-11                | 40           | Multi-conductor cable | AUX011-40 🗌           | LP911-40 🗌    |
|                             |              | Flat cable            | AUX021-40 🗌           | LP921-40 🗌    |

Note: "□" indicates the length of multi-core cables and flat cables. 1:1m (standard), 2:2m, 3:3m

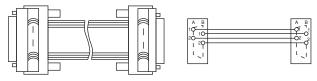
Cable wiring diagram
[Multi-core cable with connector]

#### AUX011-40 type (Fujitsu product)



[Flat cable with connector]

AUX021-40 type (Fujitsu product)



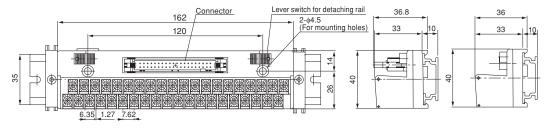
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### Recommended relay terminal blocks (Fuji Electric Technica Co., Ltd.)

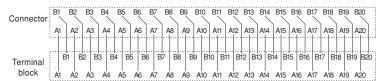
#### Specifications

| Model           | Number of terminal block poles | Connector              |                                  | Performance                                   |
|-----------------|--------------------------------|------------------------|----------------------------------|---|
| (ordering code) |                                | No. of poles           | Flame resistance: UL94V-0 rating |   |
| LP5W-40H1       | 40                             | 40                     | Insulation voltage:              | Insulation resistance: 100 M $\Omega$ or more |
|                 | M3 screw                       | Mounted connector:     | 125 V (AC, DC)                   | Voltage resistance: For 1 min. at 600 V       |
|                 | Supported by screws            | FCN-364P040-AU (plug)  | Rated thermal current: 1A        | Allowable ambient temperature: -10 to +50°C   |
|                 | Standard tightening torque:    | Fujitsu Component Ltd. |                                  | Flame resistance: UL94V-0                     |
|                 | 1.2N·m                         |                        |                                  |   |
|                 | Compliant cable: Up to         |                        |                                  |   |
|                 | 1.25mm <sup>2</sup>            |                        |                                  |   |

#### · Outline dimensional drawing



### • Wiring diagram



#### · Applicable connector

| Types                        | Model (Fujitsu Component Ltd.) |   |
|------------------------------|--------------------------------|---|
|                              | Jack                           | Cover                                     |
| Soldered type*1              | FCN-361J040-AU                 | FCN-360C040-B (B type)                    |
| Crimp type                   | FCN-363J040 (Housing)          | FCN-360C040-D (D type: Wide mouthed type) |
|                              | FCN-363J-AU (Contact)          | FCN-360C040-E (E type: Long screw type)   |
| Wire wrapping type           | FCN-362J040-AU                 |   |
| Insulation displacement type | FCN-367J040-AU/FW              | The cover is not necessary.               |

\*1 Fuji Electric solder type connector (NP8V-CN) is prepared (cover attached: FCN-360C040-B).

Note: For more details, refer to each manual.

### **Terminal Relay**

(Model by Fuji Electric FA Components & Systems Co., Ltd.)

#### Features

- · Min. width of 110 mm has been achieved. The external dimension is as compact as 110 mm (W) x 52 mm (D) x 37 mm (H).
- · Push-set terminal facilitates tightening screws. Push-set terminal is used in the terminal section, eliminating the screw tightening time and preventing screws from being lost.
- · LED operation indication facilitates I/O ON/OFF operation check. Operation indication LED is arranged in 1:1 correspondence with the relay. This makes the ON/OFF relay operation status clear at a glance.
- Two types of relays available for output and input. •
- With surge protection diode provided. .
- Terminal cover is installed as standard allowing device No. • indication.
- · With the built-in relay remover
- · Used for both DIN rail installation and rear-side screw mounting

#### Performance specifications

| Item          |  | Performance  |  |  |
|---------------|--|--|--|--|
| Operating     | duration                                 | 10 ms or less  |  |  |
| Recovery      | duration                                 | 10 ms or less  |  |  |
| Vibration     | Malfunction                              | 10 to 55 Hz, Duplex amplitude 1.0 mm                     |  |  |
| resistance    | Durability                               | 10 to 55 Hz, Duplex amplitude 1.0 mm                     |  |  |
|               |  | 3 times each in X, Y, and Z directions to total 18 times |  |  |
| Impact        | Malfunction                              | 100 m/s <sup>2</sup>                                     |  |  |
| resistance    | Durability                               | 200 m/s <sup>2</sup>                                     |  |  |
|               |  | 2 hours each in X, Y, and Z directions to total 6 hours  |  |  |
| Operating     | ambient temperature                      | -25 to +55°C (without condensation)                      |  |  |
| Relative h    | umidity                                  | 35 to 85%RH  |  |  |
| Terminal s    | crew size                                | МЗ   |  |  |
| External con  | nection tightening torque                | 0.5 to 0.7 N·m   |  |  |
| Mounting r    | nethod                                   | Rail mounting (screw mounting also possible)             |  |  |
| Applicable ro | und-type crimp-style terminal            | R1.25 to 3 (Max.6mm wide)                                |  |  |
| Connectio     | n wire                                   | Max. ø1.4  |  |  |
| LED indica    | ation color                              | Operating indication: Red, Power indication: Green       |  |  |
| Coil surge    | protection element                       | Diode  |  |  |
| Relay rem     | oval count                               | 50 times   |  |  |
| Insulation    | resistance (initial)                     | 100 MΩ or more (with 500 V DC megger)                    |  |  |
| Voltage       | Between contact coils                    | 2000 V AC, 1 minute                                      |  |  |
| resistance    | Between contacts with same polarity      | 1000 V AC, 1 minute                                      |  |  |
|               | Between contacts with different polarity | 2000 V AC, 1 minute                                      |  |  |
| Weight        |  | Approx. 200g   |  |  |

#### Rating

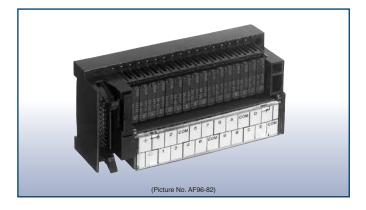
Opening section, connector side (for 1 point RB105)

|   | RS16 (output) resistor       |                    |                                |                   | RS16E (input) resistor       |                                |  |
|---|------------------------------|--------------------|--------------------------------|-------------------|------------------------------|--------------------------------|--|
| Load                                      | Resistance load              |                    | Inductive load                 |                   | Resistance load              | Inductive load                 |  |
| Item                                      | $(\cos\phi = 1, L/R = 0 ms)$ |                    | $(\cos\phi = 0.4, L/R = 7 ms)$ |                   | $(\cos\phi = 1, L/R = 0 ms)$ | $(\cos\phi = 0.4, L/R = 7 ms)$ |  |
| Rated load and rated voltage current      | 220 V AC 2 A                 | 24 V DC 2A         | 220 V AC 2 A                   | 24 V DC 2A        | 24 V DC 1A                   | 24 V DC 1A                     |  |
| Rated thermal current                     | 2A *1                        | 2A *1              |                                |                   |                              | 1A *2                          |  |
| Contact resistance                        | 30 mΩ or less                |                    |                                |                   | 30 mΩ or less                |                                |  |
| Min. application load application         | 0.1 V 0.1 mA                 |                    |                                |                   | 0.1 V 0.1 mA                 |                                |  |
| voltage current (P level reference value) |                              |                    |                                |                   |                              |                                |  |
| Electrical lifetime                       | 200 thousand times           |                    |                                |                   |                              |                                |  |
| Mechanical lifetime                       | 20 million times             | 300 thousand times | 100 thousand times             | 60 thousand times | -                            |                                |  |

\*1 While the used relay (RB105) is a product to use the rated thermal current 5 A, the rated thermal current of the main unit is 2 A because of the terminal relay unit structure. \*2 While the used relay (RB105) is a product to use the rated thermal current 5 A, the rated thermal current of the main unit is 1 A because of the terminal relay unit structure.

#### 0

| Operation coil I/O specifications (for 1 point RB105) Ambient temperature: 20°C |               |                 |                              |                              |                        |                   | mperature: 20°C |
|---|---------------|-----------------|------------------------------|------------------------------|------------------------|-------------------|-----------------|
| Rated voltage   | Rated current | Coil resistance | Pick-up voltage              | Return voltage               | Max. allowable voltage | Power consumption | [W]             |
|   | [mA]          | [Ω]±10%         |                              |                              |                        | Per 1 points      | Per 16 points   |
| 5 V DC  | 40            | 125             | 70% of rated voltage or less | 10% of rated voltage or more | 110% of rated voltage  | 0.2               | 3.2             |
| 24 V DC   | 8.3           | 2,880           | 70% of rated voltage or less | 10% of rated voltage or more | 110% of rated voltage  | 0.2               | 3.2             |



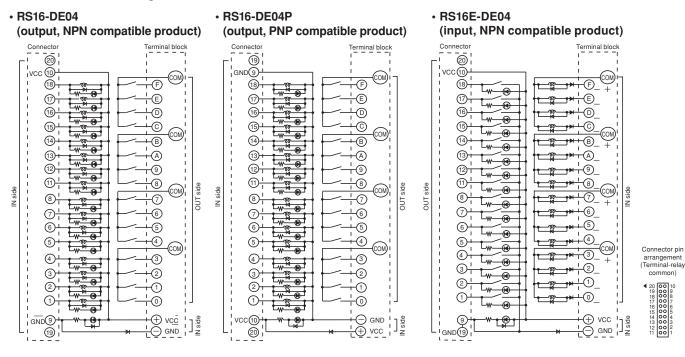
#### Type/model/ordering code

| Mod<br>(orde | el<br>ering code) | I/O<br>type | No. of points | Rated<br>voltage | Common line handling on connector side. |
|--------------|-------------------|-------------|---------------|------------------|---|
| · ·          | 6E-DE04           | Input       | 16 points     | 24 V DC          | NPN compatible (  common)               |
| RS1          | 6-DE04            | Output      | (1a x 16)     |                  | NPN compatible (  common)               |
| RS1          | 6-DE04P           | 1           |               |                  | PNP compatible ( ) common)              |

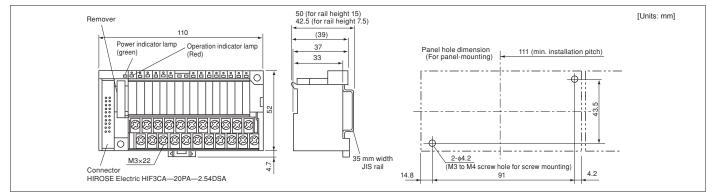
#### Terminal Relay Application Table

| 1 | Terminal relay  | RS16E-DE04 | RS16-DE04   | RS16-DE04P  |
|---|-----------------|------------|-------------|-------------|
|   | type            |            |             |             |
|   | SPH             | NP1X3206-W | NP1Y32T09P1 | NP1Y32U09P1 |
|   | I/O module type | NP1X6406-W | NP1Y64T09P1 | NP1Y64U09P1 |

#### Internal connection diagram



#### Outline dimensional drawing

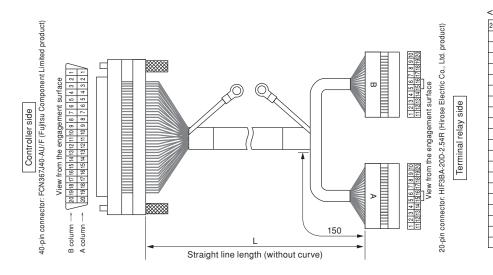


#### Terminal relay cable

#### Type/model/ordering code

| Туре                              | Cable length (L) | Model (ordering code) |
|-----------------------------------|------------------|-----------------------|
| Cable with connectors (1:2)       | 1,000 mm         | RS910M2-0104          |
| For MICREX-SX (for input, output) | 2,000 mm         | RS910M2-0204          |
|                                   | 3,000 mm         | RS910M2-0304          |

#### · Cable outline wiring diagram



#### <Wiring> 20 pin (A) 40 pin 20 pin (B) 40 pin A20 1 B20 2 A19 2 B19 3 3 A18 B18 I/O 4 A17 4 B17 5 5 B16 A16 signa A15 6 B15 6 7 B14 A14 8 B13 8 A13 A 1 9 A 2 9 Power supply (-) supply (+ 10 B 1 10 B 2 11 A12 11 B12 12 A11 12 B11 13 A10 13 B10 1/0 14 A 9 14 B 9 15 15 A 8 siana B 8 16 16 Β7 Α7 B 6 17 A 6 18 A 5 18 B 5 Power supply (-) Power supply (+) 19 Α3 19 A 4 Β4 20 Β3 20

#### Web Module: NP1L-WE1

#### Features

Through the Internet and intranet, this module realizes equipment supervision by Web browser, e-mail sending at failure occurrence, and remote control and remote maintenance (monitoring/program modification) with the programming support tool.

Versions which support English and Chinese are also available.

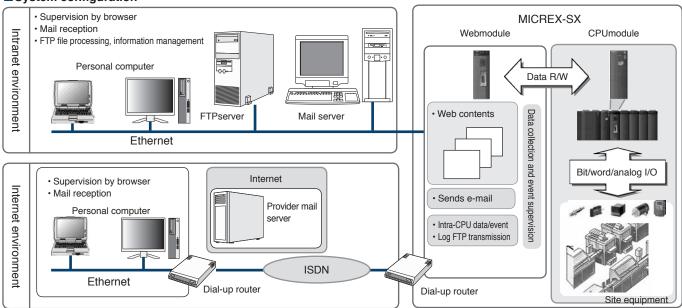


#### Functional specifications

| Item              | Specifications  |
|-------------------|---|
| Web server        | Controller data can be monitored and set using a browser (Internet Explorer)        |
| functions         | on a remote personal computer.  |
|                   | Mounts the tabular form data display and trend graph display functions as standard. |
|                   | Initial setup items for the Web modules are all set in the browser screen.          |
| E-main send       | Sends E-mail (contain the attached file) to the specified destination address at    |
| function          | occurrence of a set event (failure alarm notification, etc.).                       |
| FTP function      | Saves trend data and CPU data (binary file) in external FTP server at occurrence    |
|                   | of a set event.   |
|                   | Saved data can be processed to generate a daily/monthly report or trend graph.      |
| Security function | Limits users and setup operations by user name and password.                        |
| Remote loader     | Remote operation of SX support tool (D300win), such as monitoring of SPH            |
| function          | sequence, from a personal computer.   |
| PPP function      | Realizes the above functions through the modem (telephone and PHS circuit           |
|                   | connection service) and mobile arc (Dopa network) on the RS-232C interface.         |
| User contents     | Incorporates user-created contents in the Web module.                               |
| creation function |   |
| SNTP function     | Controller data can be calibrating the date data (calendar) of the CPU module.      |

#### • The following are recommended Ethernet devices: For industrial Ethernet devices, made by Phoenix Contact Co., Ltd. (Switching hub, repeater hub, category 5 cable, optical fiber cable etc.)

#### System configuration



#### Performance specifications

| Item                         | Specifications                                   |
|------------------------------|--|
| Ethernet interface           | 10BASE-T/100BASE-TX, RJ45 modular jack x 1       |
|                              | Auto negotiation                                 |
| RS-232C interface            | Max. 115.2 kbps, Dsub 9-pin (male) connector x 1 |
| (For PPP connection)         | Character format                                 |
|                              | Data length: 7/8 bits                            |
|                              | Parity: Even/odd/non settable                    |
|                              | Stop bits: 1/2 bits                              |
|                              | Hardware flow control: Provided                  |
| No. of units mounted         | Max. 4 units/configuration                       |
| Internal current consumption | 24 V DC, 140 mA or less                          |
| Weight                       | Approx. 140 g                                    |

#### Web Memory Module: NP1L-WS1

#### Features

The Ethernet communication module is equipped with a Web communication function and memory data collection function for the CPU module. A long-life, highly reliable system can be constructed,

compared with a personal computer OS and hard disk, etc. It can collect up to 400 Mbyte of memory data.

- Memory data collection and Ethernet communication with ٠ the host device can be achieved without creating any user programs.
- The data collected by this module can be saved into and restored from an SD card (type: NP8PSD-002, sold separately).

#### Functional specification

| Item                        | Specification   |
|-----------------------------|---|
| Web server function         | A Web browser can be used to set up the IP address,                               |
| (configurable with browser) | collection data memory, collection cycle, and others.                             |
| SX CPU memory data          | The memory data of the CPU module can be regularly collected into this module.    |
| collection function         | A Web browser can be used to set up the data collection area and cycle.           |
| Data transfer function      | The data collected by this module can be regularly                                |
| (FTP client) to host device | transferred to the host device.   |
| Backup function to          | It is triggered by the outage detection signal to save the data collected by this |
| SD card                     | module into the SD card, thereby enabling data preservation during outages.       |
|                             | Also, the switch operation enables you to save the data collected by this         |
|                             | module into the SD card.  |
| Remote loader function      | The programming assist tool can be remotely operated through Ethernet.            |
|                             | · Upload/download programs  |
|                             | · Monitor data in various formats   |
|                             | · Failure diagnosis, and others   |



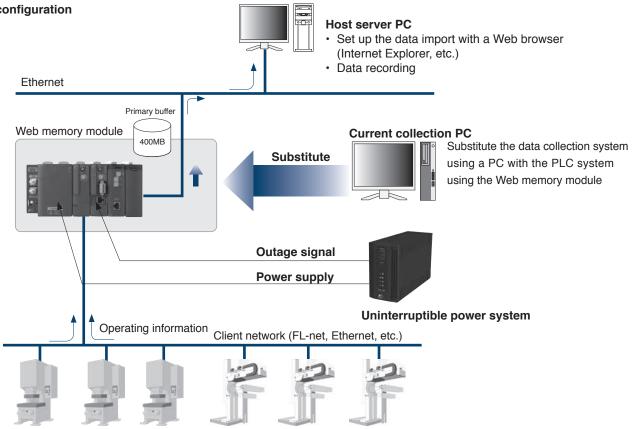
#### Performance specification

| Item                         | Specification  |
|------------------------------|--|
| Ethernet interface           | 10BASE-T/100BASE-TX  |
| Media control                | IEEE802.3u   |
| Interface switching method   | Automatic negotiation  |
| Connector                    | RJ45 modular jack type   |
| AUTO MDI/MDI-X               | Measures   |
| Transmission protocol        | TCP/IP, ICMP, ARP  |
| Internal current consumption | 24 V DC, below 80 mA (supplied from the power module via base board) |
| Weight                       | Approx. 140 g  |

#### Memory specification

| Item   | Specification     | Remarks                          |  |
|--|-------------------|----------------------------------|--|
| Internal memory capacity for data collection | 400 Mbyte (SDRAM) | 200 Mbyte x 2 areas              |  |
| SD card                                      | 2 Gbyte           | Type sold separately: NP8PSD-002 |  |





Collect operating information from the control equipment of plant lines, etc.

#### **Ethernet Interface Module: NP1L-ET1**

#### Features

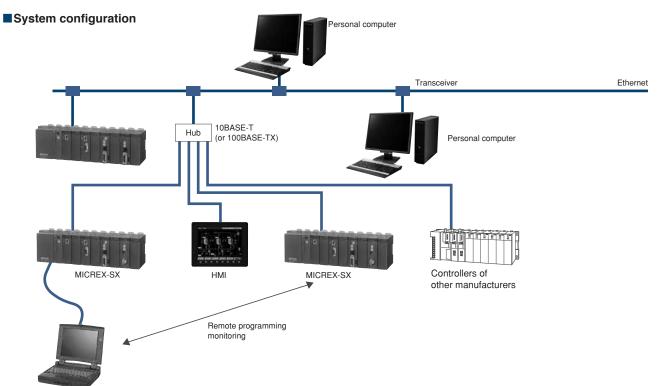
- Supports the 10BASE-T/100BASE-TX interface.
- Supports three different communication modes.
  - General purpose communication mode (TCP/IP or UDP/IP protocol communication)
  - Fixed buffer communication mode (Handshake communication between PC and specific node)
  - Loader command communication mode (MICREX-SX loader command function)



#### Performance specifications

| Item   |                                | Specifications  |  |
|--|--------------------------------|---|--|
| Model  |                                | NP1L-ET1  |  |
| Communication                                    | Application                    | General purpose communication   |  |
| function   | Communication mode             | Fixed buffer communication  |  |
|  | Loader command                 | Communications through Fuji Electric's original communication protocol. |  |
|  | Communication mode             |   |  |
| Interface  |                                | 10BASE-T/100BASE-TX   |  |
|  |                                | Automatic selection by the auto negotiation function                    |  |
| Media control                                    |                                | IEEE 802.3/IEEE 802.3u  |  |
| Transmission speed                               |                                | 10 Mbps/100 Mbps  |  |
| Transmission mediu                               | m                              | Twisted pair cable (UTP)  |  |
| Transmission protoc                              | ol                             | TCP/IP, UDP/IP  |  |
| Max. number of nodes                             | for simultaneous communication | 16 stations (ports)   |  |
| Max. number of transm                            | nit words                      | 1017 words  |  |
| Max. number of loader connections simultaneously |                                | 8 units   |  |
| No. of units mounted                             |                                | Max. 4 units/configuration  |  |
| Internal current consumption                     |                                | 24 V DC, 140 mA or less   |  |
| Weight   |                                | Approx. 140 g   |  |

• The following are recommended Ethernet devices: For industrial Ethernet devices, made by Phoenix Contact Co., Ltd. (Switching hub, repeater hub, category 5 cable, optical fiber cable etc.)



#### FL-net Ver. 3 (100 Mbps adaption) Module: NP1L-FL3

#### Features

- Up to 8 communication modules including P/PE-link can be installed on the base board equipped with CPU. (For SPH200, up to two modules)
- Data exchange between processors Cyclic data communication, message communication
- FL-net loader commands supported
- · SX system loader functions via network are supported.

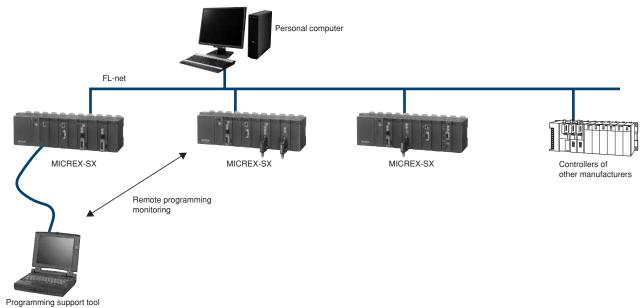


#### Performance specifications

| Item                              | Specifications   |  |
|-----------------------------------|--|--|
| Model                             | NP1L-FL3   |  |
| Transmission specifications       | 10BASE-T / 100BASE-TX  |  |
| No. of SX bus connectable modules | Max. 8 units/configuration (including P/PE-link)             |  |
| Max. number of system nodes       | 254 units (2 units / segment, including HUB)                 |  |
| Transmission line form            | Bus configuration (multi-drop)                               |  |
| Framing method                    | Ethernet II  |  |
| Access control                    | CSMA/CD  |  |
| Transmission system (code)        | Base band (Manchester coding)                                |  |
| Transmission speed                | 10 Mbps/100 Mbps   |  |
| Max. segment length               | 100 m: between node and HUB (Max. 200 m with repeater)       |  |
| Protocol                          | FA link protocol, UDP/IP, ICMP, ARP                          |  |
| IP address                        | Class C  |  |
| Data exchange method              | · Cyclic broadcast transmission method                       |  |
|                                   | · Data size: Max. 8.5 Kwords                                 |  |
|                                   | · Message transmission type                                  |  |
| · Data size: Max. 512 words       |  |  |
| Host interface                    | Common memory cyclic refresh method, block data read / write |  |
| Internal current consumption      | 24 V DC, 160 mA or less                                      |  |
| Weight                            | Approx. 220 g  |  |

• The following are recommended Ethernet devices: For industrial Ethernet devices, made by Phoenix Contact Co., Ltd. (Switching hub, repeater hub, category 5 cable, optical fiber cable etc.)

#### System configuration



#### LONWORKS Interface Module: NP1L-LW1

#### Features

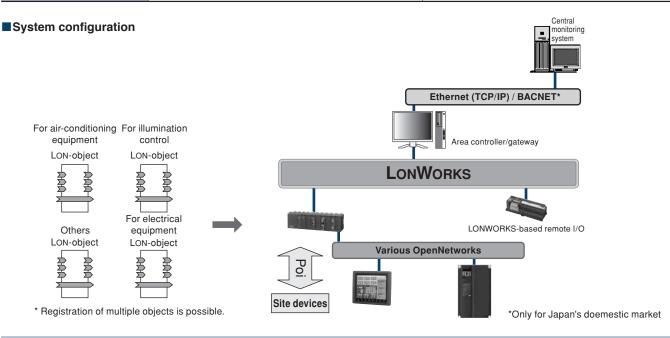
- Uses the communication extension FB compatible with the LONWORKS network, making it easier to transfer and receive MICREX-SX application data to/from other LONWORKS nodes.
- Max. number of NVs: 300, number of CPs: up to 200 intelligent nodes can be configured.
- Up to two units can be mounted in a single system (configuration).



NP1L-LW1 (Picture No. AF00-197A)

#### Specifications

| Item                              | Specifications                                   | Remarks  |  |
|-----------------------------------|--|--|--|
| Applicable standards              | LonTalk (EIA-709.1), LonMark                     |  |  |
| Transmission speed                | 78 kbps  |  |  |
| Transmission distance             | 2200 m (Bus connection)                          |  |  |
|                                   | 500 m (Free-topology connection)                 |  |  |
| No. of node connections           | 64 units   | No. of node connections in the same segment              |  |
| Transceiver                       | FTT-10A  |  |  |
| Control LSI                       | TMPN3120   | Application programs operate on SPH.                     |  |
| No. of SX bus connectable modules | Max. 2 units/configuration                       | Can be used through connection to two LONWORKS networks. |  |
| Max. number of NVs                | 300  | Depends on the definition.                               |  |
| Max. number of CPs                | 200  | Depends on the definition.                               |  |
| Total data size of NV+CP          | 8 Kwords + 128 words                             |  |  |
| I/O area size                     | 128 words  | Used for NV and CP.                                      |  |
| Memory area size                  | Any size x 4 blocks, a total of 8 Kwords or less | Used for NV and CP.                                      |  |
| No. of address entries            | 15 fixed   | No. of nodes for NVo variable binding                    |  |
| No. of domain table entries       | 2 fixed  |  |  |
| Internal current consumption      | 24 V DC, 140 mA or less                          |  |  |
| Weight                            | Approx. 200 g                                    |  |  |



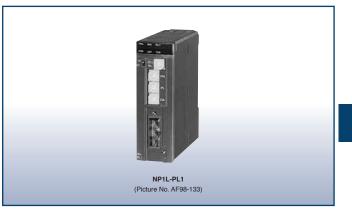
### LONWORKS Interface Module Support tool

- This support tool can be downloaded from our website at no charge.
- Usually communications through the LONWORKS network require the network variables to be defined with a dedicated tool which supports the LONWORKS network (programming with neuron C language).
- SLDEF makes it possible to define these variables with an ACCESS file without knowledge of the neuron C language.
- The information (SXD files) defined by SLDEF are downloaded from programming support tool Expert (D300win) to the LONWORKS module.
- · Since the node object definition specified by LonMark is offered as FB, LONWORKS control can be defined by PLC programming.

#### P-link Module : NP1L-PL1 PE-link Module : NP1L-PE1

#### Features

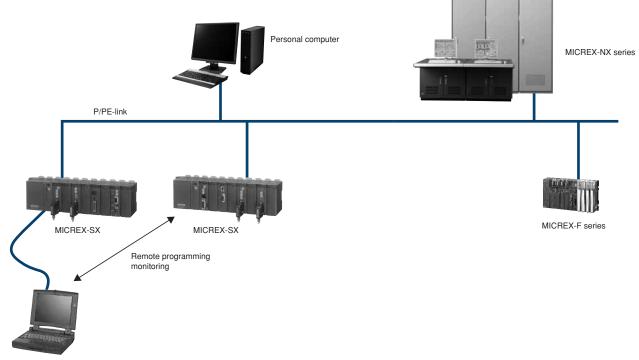
- Up to eight P/PE-link modules can be installed in a single system configuration. (For SPH200, up to two modules)
- N:N communications in the token passing method
  Data exchange between processors
- Broadcast communication, message communicationUser program upload/download and processor start/stop are
- possible from the host computer.Remote programming for other processor is possible via the
- Remote programming for other processor is possible via the P/PE-link.



#### Performance specifications

| Item                              | Specifications                       | Specifications                                 |  |  |
|-----------------------------------|--------------------------------------|--|--|--|
| Model                             | NP1L-PL1 (P link)                    | NP1L-PL1 (P link) NP1L-PE1 (PE link)           |  |  |
| No. of SX bus connectable modules | Max. 8 units/configuration           |  |  |  |
| No. of P/PE links                 | Max. 16 units                        | Max. 16 units Max. 64 units                    |  |  |
| Transmission line form            | Bus configuration (multi-drop)       |  |  |  |
| Transmission line                 | Coaxial cable                        | Coaxial cable Coaxial cable                    |  |  |
|                                   | Total length: Max. 250m              | Total length: Max. 500 m                       |  |  |
| Transmission system               | Half-duplex serial communication met | hod  |  |  |
| Data exchange method              | N:N (token passing) method, memory   | refresh method                                 |  |  |
| Transmission speed                | 5 Mbps                               |  |  |  |
| Data transfer                     | Broadcast communication, message     | communication                                  |  |  |
| Cable specifications              | Coaxial cable /5C-2V (conforming to  | Coaxial cable /5C-2V (conforming to JIS C3501) |  |  |
| Internal current consumption      | 24 V DC, 160 mA or less              | 24 V DC, 160 mA or less                        |  |  |
| Weight                            | Approx. 235 g (module), approx. 40 g | (P/PE-link connector)                          |  |  |

#### System configuration



Programming support tool

#### LE-net Module : NP1L-LE1 LE-net Loop2 Module : NP1L-LL2

#### Features

- Up to eight LE-net modules can be installed in a single system configuration. (For SPH200, up to two modules)
- LE-net is an original network of Fuji Electric. It is a lowpriced link module between processors to conduct communication with other nodes connected to the LE-net.
- Broadcast communication and message communication can be conducted.
- The LE-net can be connected either as a multi-drop network or a single loop redundant wiring network.
- If the transmission line is broken, a transmission error occurs in a multi-drop network, but in a loop network, data communication between nodes can continue. This enables construction of a highly reliable system at a relatively low cost.
- It is possible for the loop-2 module to make the LE-net modules redundant by using the redundancy maintenance



# FB. The single configuration and the redundant configuration can coexist within a loop.

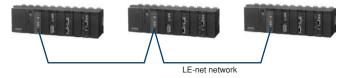
Note: Multi-drop networks, loop-2 networks cannot be connected with each other because each network uses a different transmission protocol. To connect them together, the transmission method must be unified.

#### Performance specifications

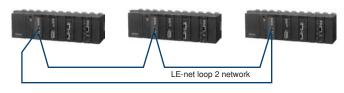
| Item                             | LE-net module  | Loop-2 module                                       |  |  |
|----------------------------------|--|---|--|--|
| Model                            | NP1L-LE1   | NP1L-LL2  |  |  |
| No. of node connections          | lax. 64 units  |   |  |  |
| Connection node number           | 0 to 63  |   |  |  |
| Connection distance              | 800 m/62.5 kbps  | Total extension: 500 m, between nodes: 100 m        |  |  |
| Transmission speed               | 500 m/125 kbps<br>250 m/250 kbps<br>100 m/500 kbps<br>40 m/Mbps              | 5 Mbps  |  |  |
| Transmission medium              | Shielded twisted pair cable  | Shielded twisted pair cable, category-5 cross cable |  |  |
|                                  | (T-link cable recommended)   |   |  |  |
| Transmission line format         | Multi drop   | Single loop redundant wiring                        |  |  |
| Transmission system              | Half-duplex, destination arrival receiving method on both sides              | ·   |  |  |
| Communication protocol           | N:N time slot data exchange communication (broadcast)                        |   |  |  |
|                                  | 1:1 message communication  |   |  |  |
| User data                        | Time slot frame: up to 96 bytes/node   | Time slot frame: up to 1536 bytes/node              |  |  |
| Frame size                       | Message frame: up to 122 bytes   | Message frame: up to 490 bytes                      |  |  |
| No. of connectable support units | Up to 2 units simultaneously, including those connected directly or remotely |   |  |  |
| Hardware redundancy              | _  | Provided  |  |  |
| Weight                           | Approx. 130 g (no connector)   | Approx. 140 g                                       |  |  |

#### System configuration

LE-net module

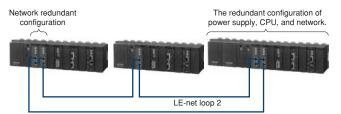


- · LE-net loop2 module
- (1) Basic system



#### (2) Duplex system

LE-net modules within the same baseboard can be made redundant by using the duplex maintenance FB. The single configuration and the redundant configuration can coexist within a loop.



NP1L-RS1 (Picture No. AF01-93)

### General Purpose Communication Module:

NP1L-RS

#### Features

- Can be combined with an extension FB for communications with diverse equipment without creating any communication control program.
- Communication port can be used as the loader connection port, which is effective in debugging from the SX bus expansion side installed at a distance.

### Performance specifications

Communication port type by module type

| Model              | NP1L-RS1            | NP1L-RS2            | NP1L-RS3             | NP1L-RS4           | NP1L-RS5            |
|--------------------|---------------------|---------------------|----------------------|--------------------|---------------------|
| Communication port | RS-232C x 1 channel | RS-232C x 1 channel | RS-232C x 2 channels | RS-485 x 1 channel | RS-485 x 2 channels |
|                    | RS-485 x 1 channels |                     |                      |                    |                     |

#### · Communication port specifications

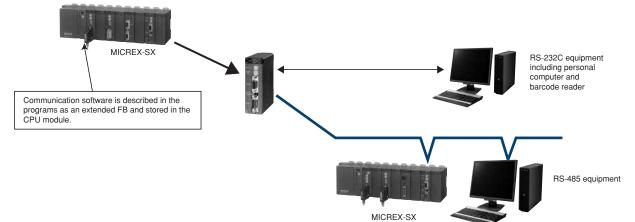
| Item                                   | Specifications   |                                    |   |  |  |  |
|--|--|------------------------------------|---|--|--|--|
| Port                                   | RS-232C RS-485   |                                    |   |  |  |  |
| No. of SX bus connectable modules      | Max. 16 units/configuration  | Max. 16 units/configuration        |   |  |  |  |
| Transmission system                    | Half-duplex /serial communication method*1   |                                    |   |  |  |  |
| Synchronization method                 | Start-stop synchronous transmission  |                                    |   |  |  |  |
| Transmission speed                     | 300/600/1,200/2,400/4,800/9,600/19,200/38,400/57,600/76,800/115,200 bps (115,200 bps or less in total of 2 channels) *2        |                                    |   |  |  |  |
| Transmission distance                  | 15 m or less   | 1 km or less (transmission speed : | : 19,200 bps or less)                         |  |  |  |
| No. of connectable modules             | 1:1 (including one external device)  | 1:N (Max. 31)                      |   |  |  |  |
| Connection method                      | D-sub, 9-pin connector (female)*3  | D-sub, 9-pin connector (male)*3    | Screw terminal block (M3) 20 poles (NP1L-RS5) |  |  |  |
| Transmission method                    | Depends on the application program (Expansion FB) in the CPU module  |                                    |   |  |  |  |
| Internal current consumption (24 V DC) | NP1L-RS1: 110 mA or less, NP1L-RS2: 90 mA or less, NP1L-RS3: 110 mA or less, NP1L-RS4: 80 mA or less, NP1L-RS5: 110 mA or less |                                    |   |  |  |  |
| Weight                                 | NP1L-RS1: Approx. 170 g, NP1L-RS2: Approx. 160 g, NP1L-R   | S3: Approx. 140 g, NP1L-RS4: Appro | ox. 160 g, NP1L-RS5: Approx. 190 g            |  |  |  |

\*1 The use of the non-procedure FB allows full-duplex communication on applications.

\*2 For transmission speeds of 300, 600, 76800, and 115200 bps, use FBs corresponding to the transmission speed.

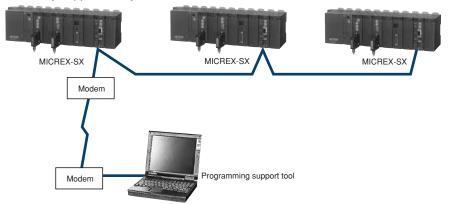
\*3 Connector fixing screws are mounted using metric screws (M2.6). Products using imperial screws are also available. Please contact our sales office for details (type ends with Z607).

#### System configuration



#### Support tool network function

Use of general-purpose communication modules makes it possible for multiple systems to be supported with one unit of personal computer loader or to remotely support the system via a modem.



#### RS-232C cable selection

Select an appropriate RS-232C cable according to the following specifications for both the PLC and external device.

- RS-232C connector specifications (connector shape, number of pins, male or female connector, metric or imperial connector fixing screws)
- · RS-232C connector pin assignment

The connector specifications and pin assignment for the PLC are shown below. For more information on cable selection, refer to Appendix 8 of the user's manual for the general purpose communications module (Manual No. FEH225j or newer versions of the manual).

#### [Connector specifications]

D-sub 9-pin, female (use male on cable side connector), metric screws (M2.6)

\*Commercially available cables with a D-sub9 pin connector usually make use of imperial screws, so it is necessary to replace the imperial screws with metric screws (M2.6).

#### [Connector pin assignment]

|   |                        | <b>`</b>     | Pin No. | Signal name | Signal direction PLC ←→ external device | Description                   |
|---|------------------------|--------------|---------|-------------|---|-------------------------------|
|   | (0)-                   | Metric screw | 1       | CD          | ←                                       | Carrier detect                |
|   | $\sim$                 |              | 2       | RD          | ←                                       | Receive data                  |
| 1 | 62                     | 6            | 3       | SD          | →                                       | Send data                     |
|   | 1881                   |              | 4       | ER          | →                                       | Data terminal ready           |
|   | 18ğ                    |              | 5       | SG          |   | Signal ground (common return) |
| 5 | 180)                   | 9            | 6       | DR          | +                                       | Data set ready                |
|   | $\sim$                 |              | 7       | RS          | <b>→</b>                                | Request to send               |
|   | $\widehat{\mathbf{O}}$ | Metric screw | 8       | CS          | +                                       | Clear to send                 |
| l | <u> </u>               | )            | 9       | RI          | +                                       | Call indication               |

#### **General Purpose Communication FB Software for FA Equipment**

Various communication protocols are available by combining the software with general purpose communication modules and storing the extended FB in the CPU module. This FB software can be downloaded from our website at no charge.

### Communication extension FB list

| Package category   | Extension FB type                              | Relevant equipment   | Extension FB name          |
|--------------------|--|--|----------------------------|
| Standard extension | No procedure                                   | FB which enables application programs to execute non-procedural                  | _C_free                    |
| FB                 |  | communication protocols.   | _Cfr252                    |
|                    |  |  | _Cfr128                    |
|                    |  |  | _Cfr64                     |
|                    |  |  | _Cfr32                     |
|                    |  |  | _Cfrpr (built-in protocol) |
|                    |  |  | _Cfrp2 (built-in protocol) |
|                    | Temperature controller communication procedure | Fuji Electric Co.: PYX, PYH  | _CfdPYX                    |
|                    | Inverter communication                         | Fuji Electric Co.: FRENIC5000  | _CfdFRN                    |
|                    | procedure                                      | For FVR-C11 (FGI-BUS)  | _CfdFVR                    |
|                    |  | For FVR-C11 (FGI-BUS) (Reduction of communication processing program size)       | _Cfvrpr                    |
|                    | MODBUS procedure                               | MICREX-SX works as a master station and communicates with MODBUS slave stations. | _C_modm                    |
|                    | MODBUS Ethernet                                | For MODBUS Ethernet master stations  | _C_emodm                   |
|                    | (TCP/IP) Communication FB                      | For MODBUS Ethernet slave stations   | _C_emods                   |
| For FA equipment   | Temperature controller                         | RKC INSTRUMENT INC.: REX-F, REX-D, FAREX-SR series                               | _CrkREX                    |
| General-purpose    | procedure                                      | OMRON Corporation: Digital temperature controller E5AX, E5XJ series              | _ComAX                     |
| communication FB   |  |  | _ComCK                     |
|                    |  | Yamatake-Honeywell Co.: Digitronik temperature controller SDC40A/40G series      | _CymSDC                    |
|                    | ID system procedure                            | OMRON Corporation: V600 series, V700 series                                      | _ComV6, _ComV7             |
|                    |  | Sharp Corporation: Microwave ID plate system DS series                           | _CshDS                     |
|                    |  | Yamatake-Honeywell Co.: Code recognition ID system WAM120 series                 | _CymWAM                    |
|                    |  | Idec Izumi Corp.: Data carrier system FP1A series                                | _CizFP                     |
|                    | Bar code reader                                | TOHKEN CO.: CD8200/8500, TLMS-3200RV series                                      | _CtkTCD                    |
|                    | procedure                                      | Nippon Electric Industry Co.: BCC2600 series                                     | _CndBCC                    |
|                    |  | Keyence Corp.: BL180, BL500, BL700 series  | _CkyBL                     |
|                    |  | IZUMI DATALOGIC CO.: Bar code reader DS series                                   | _CizDS                     |
|                    | SECS procedure                                 | SECS-procedure semiconductor manufacturing equipment (Support: SECS- I only)     | _C_SECS                    |
|                    | NC procedure                                   | Fanuc Ltd.: FANUC Series 18i   | _CDNC2                     |
|                    | Serial printer procedure                       | NEC Corporation: PC-PR201 series   | _C_print                   |

#### OPCN-1 Master Module : NP1L-JP1 OPCN-1 Slave Module : NP1L-JS1 OPCN-1 Interface Module : NP1L-RJ1

#### Features

#### NP1L-JP1

- · Up to eight units can be connected in a single system configuration.
- Up to 31 slave stations can be connected to a single master unit.
- Number of I/O points is a max. of 8192 points (512 words) For SPH200, up to 2048 points (128 words)
- The transmission speed can be switched. (1 M/500 k/250 k/125 kbps)

#### NP1L-JS1

- I/O data link through the OPCN-1 is possible between CPUs.
- Number of I/O points is a max. of 2048 points (128 words)

### NP1L-RJ1

 Slave station configuration, conforming to the OPCN-1 Standard, implements compact, economical, centralized

#### Communication specifications



remote I/O as a multi-vendor network.

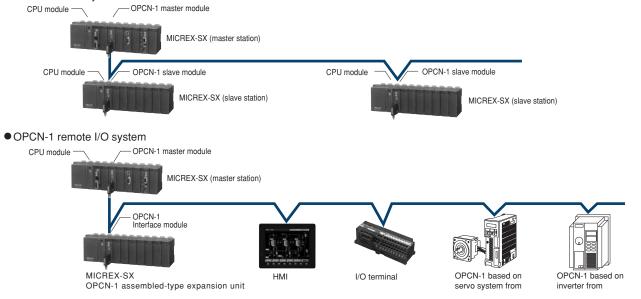
 Input filtering time of the input module can be set with DIP switch on the front.

| Item                                      | Specifications   |                                   |                            |  |
|---|--|-----------------------------------|----------------------------|--|
| Model                                     | NP1L-JP1   | NP1L-JS1                          | NP1L-RJ1                   |  |
| Applicable class                          | TYPE-M51 I   |                                   | TYPE-S51 I                 |  |
| No. of SX bus connectable modules         | Max. 8 units/configuration   |                                   | —                          |  |
| No. of connectable slaves                 | 31 units/master module   | —                                 |                            |  |
| Station number setting range              | 00 fixed   | 01 to 7F                          |                            |  |
| Transmission line form                    | Bus configuration (multi-drop)                                       |                                   |                            |  |
| Transmission line                         | Shielded twisted pair cable  |                                   |                            |  |
| Transmission system                       | łalf-duplex serial transmission, based on EIA RS-485                 |                                   |                            |  |
| Transmission speed (Max. total length) *1 | 25 kbps (1000 m)/ 250 kbps (800 m)/ 500 kbps (480 m)/ 1 Mbps (240 m) |                                   |                            |  |
| Encoding method                           | NRZI (Non Return to Zero Inverted)                                   |                                   |                            |  |
| Error check                               | ECS $(X^{16} + X^{12} + X^5 + 1)$ and retry                          |                                   |                            |  |
| Communication function                    | Initial setting service  | Initial setting service           |                            |  |
|   | I/O service  | I/O service                       |                            |  |
|   | Reset service  | Reset service                     |                            |  |
|   | JEM-TR192 service  | Simultaneous broadcast service    |                            |  |
|   | (data read/write service)  |                                   |                            |  |
| No. of I/O points                         | Normal mode: Max. 2032 points (127 words)                            | Maximum input: 64 word/slave, max | imum output: 64 word/slave |  |
|   | Extension mode or I/O Extension mode: Max. 8192 points (512 words)   |                                   |                            |  |
| No. of message points                     | Max. length per transmission: 250 bytes                              | —                                 |                            |  |
|   | (data section for the data read/write service)                       |                                   |                            |  |
| Internal current consumption              | 24 V DC, 130 mA or less  |                                   |                            |  |
| Weight                                    | Approx. 200 g (module), approx. 40 g (OPCN-1 connector)              |                                   |                            |  |

\*1 The transmission distance applies to T-KPEV-SB 1.25 mm<sup>2</sup> from Furukawa Electric Co. Note that the distance may vary depending on the cable characteristics.

#### System configuration

#### • OPCN-1 slave system



**DeviceNet Master Module** : NP1L-DN1 **DeviceNet Slave Module** : NP1L-DS1 **DeviceNet Interface Module : NP1L-RD1** 

### Features

### NP1L-DN1

- Up to eight units can be connected in a single system configuration.
- Up to 63 units of remote I/O equipment can be connected to a single master unit. • Number of I/O points is a max. of 8192 points (512 words)
- For SPH200, up to 2048 points (128 words)
- · The transmission speed can be switched.
- 125 kbps (500 m)/250 kbps (250 m)/500 kbps(100 m) NP1L-DS1
- I/O data link through the DeviceNet is possible between CPUs.
- · Number of I/O points is a max. of 2048 points (128 words)



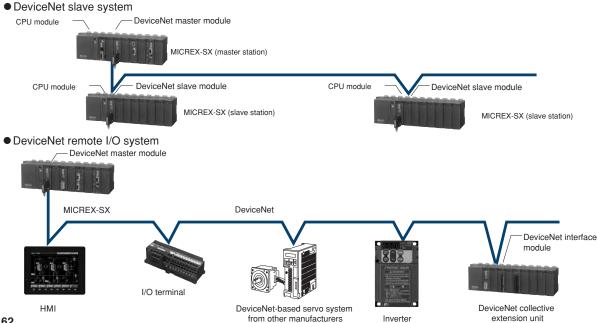
#### NP1L-RD1

· Realizes small, economic collective remote I/O as a DeviceNet slave station.

#### Communication specifications

| Item                              | Specifications  |   |          |  |  |  |
|-----------------------------------|---|---|----------|--|--|--|
| Model                             | NP1L-DN1  | NP1L-DS1                                  | NP1L-RD1 |  |  |  |
| No. of SX bus connectable modules | Iax. 8 units/configuration —  |   |          |  |  |  |
| No. of remote I/O stations        | 3/master module —   |   |          |  |  |  |
| MAC ID setting range              | 00 to 63  |   |          |  |  |  |
| Transmission line form            | Bus configuration (multi-drop), tree-structure, branch-structure  |   |          |  |  |  |
| Transmission line                 | Trunk (thick cable), drop (thin cable)  |   |          |  |  |  |
| Transmission system               | Half-duplex serial communication method   |   |          |  |  |  |
| Transmission speed (distance)     | 125 kbps (500 m)/ 250 kbps (250 m)/ 500 kbps(100 m)   |   |          |  |  |  |
| Media access control              | CSMA/NBA  | SMA/NBA                                   |          |  |  |  |
| Modulation                        | Base band   |   |          |  |  |  |
| Encoding method                   | Non-zero recovery using the bit stuff function NRZ (Non Return to Zero)   |   |          |  |  |  |
| Error check                       | FCS (Frame Check Sequence CRC-16)   |   |          |  |  |  |
| Communication function            | I/O message<br>• Poll command/response<br>• Change of state/Cyclic ACK not provided<br>Explicit message<br>(Implements the client/server function to set and diagnose remote I/O stations. Low priority communication traffic.) | Poll command/response<br>Explicit message |          |  |  |  |
| Vendor ID                         | 319 (Fuji Electric Co., Ltd.)   |   |          |  |  |  |
| Device type                       | Communication Adapter (Code: 0×0C)  |   |          |  |  |  |
| No. of I/O points                 | Normal mode: Max. 2048 points (128 words)   |   |          |  |  |  |
|                                   | Extension mode or I/O Extension mode: Max. 8192 points (512 words) Max. 2048 points (128 words) /1 slave  |   |          |  |  |  |
| No. of message points             | Max. length 492 bytes per transmission (Explicit message)   |   |          |  |  |  |
| Network current consumption       | 24 V DC, 45 mA or less (supplied from DeviceNet power supply)   |   |          |  |  |  |
| Internal current consumption      | 24 V DC, 90 mA or less  |   |          |  |  |  |
| Weight                            | Approx. 170 g   |   |          |  |  |  |

#### System configuration



#### T-link Master Module : NP1L-TL1 T-link Slave Module : NP1L-TS1 T-link Interface Module : NP1L-RT1

### Features

- NP1L-TL1
- Up to eight units can be connected in a single system configuration.
- Up to 64 units of slave equipment can be connected to a single master unit.
- Number of I/O points is a max. of 8192 points (512 words) For SPH200, up to 2048 points (128 words)
- T-link equipment for such as MICREX-F and FLEX-PC can be used. (Some types excluded.)

#### NP1L-TS1

- Data link by I/O data between CPUs through T-link is possible.
- Five different numbers of I/O points (1 word/1 word, 2 words/2 words, 4 words/4 words, 8 words/8 words, 32 words/32 words) can be selected according to application.



#### NP1L-RT1

 Realizes small, economic collective remote I/O as a T-link slave station.

#### Communication specifications

| Item                              | Specifications   |   |            |  |
|-----------------------------------|--|---|------------|--|
| Model                             | NP1L-TL1   | NP1L-TS1  | NP1L-RT1*3 |  |
| No. of SX bus connectable modules | Max. 8 units/configuration   |   | -          |  |
| No. of connectable slaves         | 32 units/master module*2   | -   |            |  |
| Transmission line form            | Bus configuration (multi-drop)   |   |            |  |
| Transmission speed                | Bus transmission line: Shielded twist pair cable                           | Bus transmission line: Shielded twist pair cable Maximum total length: 1000 m |            |  |
| (Max. total length)*1             | Optical transmission line: Quartz GI cable, multicomponent SI cable)       |   |            |  |
|                                   | (Optical connector FNC120/130 is needed for the optical transmission line) |   |            |  |
| Transmission system               | Half-duplex serial communication method                                    |   |            |  |
| Data exchange method              | 1:N (polling/selecting) method   | 1:N (polling/selecting) method  |            |  |
| Transmission speed                | 500 kbps   |   |            |  |
| Error check                       | $FCS(X^{16}+X^{12}+X^{5}+1)$   |   |            |  |
| No. of I/O points                 | Normal mode: Max. 2048 points (128 words)                                  |   |            |  |
|                                   | Extension mode or I/O Extension mode: Max. 81                              | Extension mode or I/O Extension mode: Max. 8192 points (512 words)            |            |  |
| No. of message points             | Max. length per transmission: 220 bytes                                    |   |            |  |
| Internal current consumption      | 24 V DC, 140 mA or less  | 24 V DC, 140 mA or less   |            |  |
| Weight                            | Approx. 200 g (module), approx. 40 g (T-link con                           | nector)   |            |  |

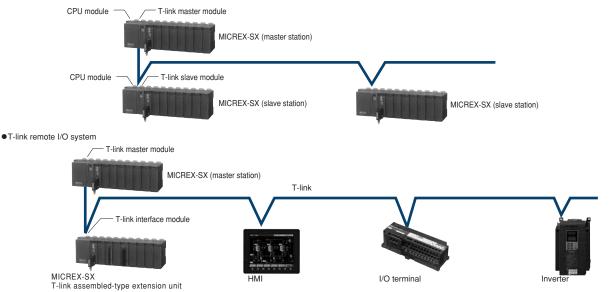
\*1 The transmission distance applies to T-KPEV-SB 1.25 mm<sup>2</sup> from Furukawa Electric Co.

Note that the distance may vary depending on the cable characteristics.

- \*2 Up to 64 units can be connected as slaves when using the T link electric repeater.
- \*3 The following I/O modules cannot be installed on the NP1L-RT1 base. NP1X3206-A, NP1Y32T09P1-A, NP1AX08-MR, NP1AX08V-MR, NP1AX08I-MR

#### System configuration





PROFIBUS-DP Master Module : NP1L-PD2 PROFIBUS-DP Slave Module : NP1L-PS1 PROFIBUS-DP Interface Module : NP1L-RP1

# Features NP1L-PD2

#### NP1L-PD2

 Open system Diverse slave products of PROFIBUS-DP can be connected. As for the DP slave, the compatibility authenticated by the PROFIBUS association has been confirmed. (The number of vendors exceeds 300.)

Flexible system configuration
 In addition to the basic configuration consisting of one DP master and multiple DP slaves, combinations with multiple DP masters and multiple DP slaves are possible, making it easier to distribute master functions.

Max. number of unit connections (including master stations) is 126. With 33 units or more, repeaters are required.

Transmission speed
 Can be selected from n

Can be selected from nine options:

9.6/19.2/93.75/187.5/500/1500/ 3000/6000/12000 kbps. (The upper limit depends on the type of the DP slave.)



#### NP1L-RP1

 This communication module realizes collective remote I/O as a PROFIBUS-DP slave station.

#### NP1L-PS1

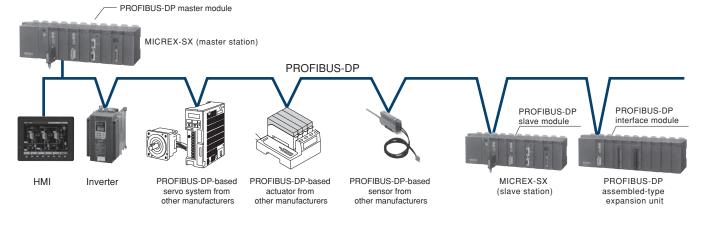
- I/O data link through the PROFIBUS-DP is possible between CPUs.
- A max. of 128 words can be controlled as an input/output total of I/O points.

#### Performance specifications

| Item                                      | Specifications           | Specifications  |               |             |                  |               |              |             |       |        |  |
|---|--------------------------|---|---------------|-------------|------------------|---------------|--------------|-------------|-------|--------|--|
| Model                                     | NP1L-PD2                 |   |               |             | NP1L-PS1         |               |              | NP1         | L-RP1 |        |  |
| No. of SX bus connectable modules         | Max. 8 units/configu     | ration  |               |             |                  |               |              | -           |       |        |  |
| Applicable standards                      | IEC 66158, EN 50170      | ), DIN 19245  |               |             |                  |               |              |             |       |        |  |
| Communication function                    | PROFIBUS-DP mast         | er (DPM1) fu  | nction        |             | PROFIBUS-D       | Slave funct   | ion          |             |       |        |  |
| No. of slave station connections          | Up to 32 units (up to    | 126 units with  | n repeaters)  |             | -                |               |              |             |       |        |  |
| Station No. (station address) setup range | 0 to 125                 |   |               |             | 0 to 99          |               |              |             |       |        |  |
| Transmission line form                    | Bus configuration (m     | us configuration (multi-drop)   |               |             |                  |               |              |             |       |        |  |
| Communication standard                    | Applicable to EN 501     | 70 and DIN 1  | 9245          |             |                  |               |              |             |       |        |  |
| Data exchange method                      | 1:N (polling/selecting   | g) method   |               |             |                  |               |              |             |       |        |  |
| Transmission speed                        | 9.6, 19.2, 93.75, 187.   | 5, 500, 1,500   | , 3,000, 6,00 | 0, 12,000 ( | kbps)            |               |              |             |       |        |  |
| Transmission distance                     | 1,200 m at the transr    | nission speed   | d of 9.6 bps; | 100 m at th | e transmission   | speed of 12   | Mbps (See th | ne table be | ow.)  |        |  |
|   | Baud rate (kbps)         | 9.6   | 19.2          | 93.75       | 187.5            | 500           | 1,500        | 3,000       | 6,000 | 12,000 |  |
|   | Distance/segment         | 1,200 m   | 1,200 m       | 1,200 m     | 1,000 m          | 400 m         | 200 m        | 100 m       | 100 m | 100 m  |  |
| Cable                                     | PROFIBUS-DP cable        | Э   |               |             |                  |               |              |             |       |        |  |
|   | (Shielded twist pair o   | able)   |               |             |                  |               |              |             |       |        |  |
| No. of I/O points                         | Normal mode: Max.        | 2048 points (   | 128 words)    | *1          | In total I/O: Ma | ax. 128 words | 3            |             |       |        |  |
|   | Extension mode or I/O ex | Extension mode or I/O extension mode: Max. 8160 points (510 words) (Each I/O: Max. 122 words) |               |             |                  |               |              |             |       |        |  |
| Internal current consumption              | 24 V DC, 200 mA or       | ess   |               |             | 24 V DC, 150     | nA or less    |              |             |       |        |  |
| Weight                                    | Approx. 250 g            |   |               |             | Approx. 180 g    |               |              |             |       |        |  |

\*1 SPH200 supports standard mode only.

#### System configuration



#### Configurator Software: Net Tool For Profibus

Used to download the system configuration information to the PROFIBUS-DP master module. Required to update the initial setup or system configuration.

Please purchase from:

HMS INDUSTRIAL NETWORKS 8 +81-45-478-5340

### **M-NET Communication Module: NP1L-MN1**

#### Features

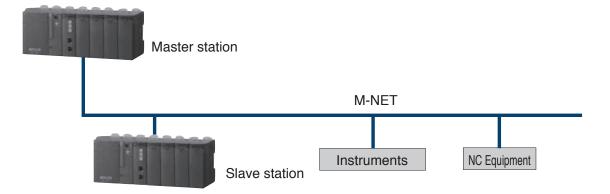
- The module is available as a master or slave station by switching the station No.
- · Up to seven child stations can be connected.
- A terminating resistor is built-in.



#### Specification

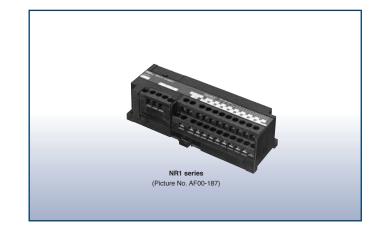
| Item                     | Description  |
|--------------------------|--|
| Number of channels       | 1 channel  |
| Main functions           | Parent/child station   |
| Transmission information | 256  |
| Transmission speed       | Normally connected with seven stations with 256 points: up to 100 ms per cycle |
| Form of connection       | 1:N (N: up to 7)   |
| Signal level             | EIA standard: RS-422   |
| Communication method     | Half-duplex system   |
| Synchronization method   | Asynchronous (async)   |
| Communication speed      | 19.2 kbps/57.6 kbps  |
| Transmission distance    | Up to 100 m  |
| Weight                   | Approx. 175 g (no connector)   |

### System configuration



#### I/O Terminal : NR1 Series

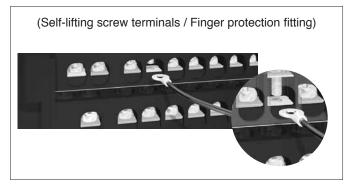
Compact type I/O terminal applicable to diverse field networks with a common frame size.



#### Features

- Compatible with diverse device level networks Device level network which performs high-speed communication of I/O information and messages between a programmable controller, a personal computer, and other controllers and an inverter, a servo, and MMI device, and other FA devices, among diverse networks consisting of an FA system, ranging from the computer level to the bit level. The I/O terminal corresponds to open device level networks (OPCN-1, LONWORKS) and Fuji Electric's original networks (T-link, SX bus).
- Easy maintenance Since removable terminal blocks are used as the terminal blocks for the communication section, power supply, and I/O, the main unit can be attached and removed easily.
- Preventing mis-wiring
- Uses different colors for the surface sheets of the main unit: input (white), output (black), and I/O mixture (zebra). Applicable networks are also displayed, enabling the unit type to be determined at a glance. • Enabling DIN rail attachment
- Not only usual screw attachment but also DIN rail attachment is possible. • Efficient safe terminal block structure
- This terminal block has terminal screws which are self-lifting after they are loosened, thus preventing screws from being lost at the time of wiring to the round amplifier terminal, increasing the wiring work efficiency.

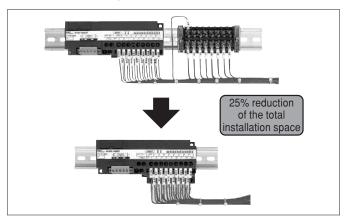
The use of power supply and I/O terminal blocks with the finger protection fitting (IP20) helps improve the safety of machines and equipment.



 Contributing to panel design standardization The unit frame is unified to a compact size of 148 x 50 x 40 (W x H x D: mm), allowing design standardization without worrying about external view modifications by I/O specifications and network specifications. Network modifications can be dealt with only by unit replacement. 25% reduction of total installation space "Common extension terminal block" which extends the

number of common terminals with one-touch operation is optionally available.

The use of "common extension terminal block" eliminates the need for a separate relay terminal block for common extension, reducing the total installation space by 25%.

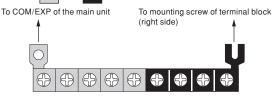


Common extension bar

Used to extend the common terminal block that is mounted on the lower side of the main unit. (NR1□Y-08R07DT excluded)

Model: NR1XV-CB1

The terminals are divided into two groups for electrical connection: and as shown below.



### Models

#### · NR1 series

| Product name | Product name Model (ordering cod |   | Specifications   |
|--------------|----------------------------------|---|--|
| OPCN-1       | 16-point input                   | NR1 🗆 X-1606DT  | 24 V DC, 16-point bi-directional input, detachable terminal block  |
| SX bus       | 8-point Ry output                | NR1 V-08R07DT 240 V AC/110 V DC, 8-point Ry output, detachable terminal block |  |
| T-link       | 16-point Tr output               | NR1 IY-16T05DT  | 24V DC, 16-point Tr sink output, detachable terminal block   |
|              | 8/8-point mixture                | NR1 UW-16T65DT  | 24 V DC, 8-point source input, 24 V DC, 8-point Tr sink output, detachable terminal block                          |
| LONWORKS     | 16-point input                   | NR1LX-1606DT  | 24 V DC, 16-point bi-directional input (4 points can be used as pulse inputs), detachable terminal block           |
|              | 8-point Ry output                | NR1LY-08R07DT   | 240 V AC/110 V DC, 8-point Ry output, detachable terminal block  |
|              | 9-point input/2-point output     | NR1LW-11R80DT   | 24 V DC, 9-point source input (4 points can be used as pulse inputs), 2-point Ry output, detachable terminal block |
| Option       | Option NR1XV-CB1                 |   | Common extension bar (9 pins)  |

### ■ Specifications

#### · Power supply specifications

| Item                          | Specification | pecifications   |   |                            |  |  |
|-------------------------------|---------------|---|---|----------------------------|--|--|
| Model                         | NR1 (NR1      | L excluded)   | NR1LX/NR1LW   | NR1LY                      |  |  |
| Rated input voltage           | 24 V DC       |   |   |                            |  |  |
| Allowable input voltage range | 21.6 to 26.4  | V DC  | 20.4 to 27.6 V DC   |                            |  |  |
| Dropout tolerance             | 1 ms or less  | (at 21.6 V)   | 1 ms or less (at 20.4 V)                                    |                            |  |  |
| Inrush power                  | 5 A, 1 ms or  | less  | 3 A, 5 ms or less   | 25 A, 5 ms or less         |  |  |
| Dielectric strength           |               | 1500 V AC, 1 minute<br>(Between power supply input terminal and frame ground)               |   |                            |  |  |
| Insulation resistance         |               | 10 MΩ or more with 500 V DC megger<br>Between power supply input terminal and frame ground) |   |                            |  |  |
| Power consumption             | SX bus        | NR1⊡X-1606DT: 1.4 W or less<br>NR1⊡Y-08R07DT: 3 W or less<br>NR1⊡Y-16T05DT: 1.4 W or less   | NR1LX-1606DT: 1.6 W or less<br>NR1LW-11R80DT: 1.6 W or less | NR1LY-08R07DT: 3 W or less |  |  |

### ■I/O specifications

#### Digital input terminal

| Item                   |        | Specifications          |                                |              |                                  |  |  |
|------------------------|--------|-------------------------|--------------------------------|--------------|----------------------------------|--|--|
| Model                  |        | NR1TX                   | NR1SX                          | NR1JX        | NR1LX                            |  |  |
| No. of input points    |        | 16 points               | 16 points                      | 16 points    | Di: 12 points<br>Pulse: 4 points |  |  |
| Rated voltage          |        | 24 V DC                 |                                |              |                                  |  |  |
| Max. allowed voltage   | e      | 26.4 V DC               | A V DC                         |              |                                  |  |  |
| Input format           |        | No polarity             |                                |              |                                  |  |  |
| Rated current 7 mA     |        | 7 mA                    |                                |              |                                  |  |  |
| Input impedance        |        | 3.3 kΩ                  |                                |              |                                  |  |  |
| Standard operation     | OFF→ON | 15 to 26.4V             |                                |              |                                  |  |  |
| range                  | ON→OFF | 0 to 5V                 |                                |              |                                  |  |  |
| Input delay time       | OFF→ON | 5 ms or less            | Batch change through parameter | 3 ms or less | 10 ms or less                    |  |  |
|                        | ON→OFF | 5 ms or less            | settings*1                     | 3 ms or less | 10 ms or less                    |  |  |
| Max. pulse input free  | luency | -                       |                                |              | 20 Hz                            |  |  |
| Common configuration 1 |        | 16 points/common        |                                |              |                                  |  |  |
| Insulation method      |        | Photocoupler insulation |                                |              |                                  |  |  |
| Delating condition     |        | None                    | None                           |              |                                  |  |  |
| Weight                 |        | Approx. 240 g           |                                |              |                                  |  |  |

\*1 (OFF to ON) - (ON to OFF): 1-1, 3-3 (default), 3-10, 10-10, 30-30, 100-100

#### Digital output terminal

| Item                            |        | Specifications                                     |  |  |
|---------------------------------|--------|--|--|--|
| Model                           |        | NR1 - Y-08R  | NR1 🗆 Y-16T  |  |
| No. of output points            |        | 8 points   |  |  |
| Output format                   |        | Relay  | Tr sink  |  |
| Rated voltage                   |        | 240 V AC 50/60 Hz 110 V DC                         | 24 V DC  |  |
| Max. allowed voltage            | e      | 264 V AC or less, 110 V DC or less                 | 19.2 to 30V DC   |  |
| Max. load current               |        | 30 V DC/ 240 V AC: 2 A/point 110 V DC: 0.2 A/point | 0.6 A/point (30 V DC), 4.8 A/common                          |  |
| Output delay time               | OFF→ON | 10 ms or less                                      | 1 ms or less   |  |
|                                 | ON→OFF | 10 ms or less                                      | 1 ms or less   |  |
| Leakage current whe             | en OFF | None   | Max. 0.1 mA  |  |
| Surge suppresser cir            | rcuit  | None   | Clamp diode  |  |
| Maximum opening/cl<br>frequency | losing | 1800 times/hour                                    | 3600 times/hour<br>(Restriction with induction load applied) |  |
| Common configuration            |        | 1 point/common                                     | 16 points/common   |  |
| Insulation method               |        | Relay insulation + Photocoupler insulation         | Photocoupler insulation                                      |  |
| Delating condition              |        | None   | None   |  |
| Weight                          |        | Approx. 250 g                                      | Approx. 240 g  |  |

#### Digital I/O terminal

| Item   |        | Specifications                         |  |                           |   |  |  |  |
|--|--------|--|--|---------------------------|---|--|--|--|
| Model  |        | NR1TW                                  | NR1SW                                    | NR1JW                     | NR1LW   |  |  |  |
| No. of I/O points  |        | Di: 8 points Do: 8 points              | Di: 8 points Do: 8 points                | Di: 8 points Do: 8 points | Di: 9 points Do: 2 points                             |  |  |  |
| I/O form   |        | Source input, sink output              | Source input, sink output                |                           |   |  |  |  |
| Rated input voltage  |        | 24 V DC                                |  |                           |   |  |  |  |
| Max. allowed voltag  | e      | 26.4 V DC                              |  |                           |   |  |  |  |
| Rated current  |        | 7 mA                                   |  |                           |   |  |  |  |
| Input impedance  |        | 3.3 kΩ                                 |  |                           |   |  |  |  |
| Standard operation   | OFF→ON | 15 to 26.4V                            |  |                           |   |  |  |  |
| range  | ON→OFF | 0 to 5 V                               |  |                           |   |  |  |  |
| Input delay time   | OFF→ON | 5 ms or less                           | Batch change through parameter           | 3 ms or less              | 10 ms or less   |  |  |  |
|  | ON→OFF | 5 ms or less                           | settings*1                               | 3 ms or less              | 10 ms or less   |  |  |  |
| Max. pulse input frequency   |        | -                                      | 20 Hz                                    |                           |   |  |  |  |
| Rated output voltage   |        | 24 V DC 240 V AC 50/60 F               |  |                           |   |  |  |  |
| Max. allowed voltage 19  |        | 19.2 to 30 V DC                        | 264 V AC or less<br>110 V DC or less     |                           |   |  |  |  |
| Max. load current  |        | 0.6 A/point (30 V DC), 4.8 A/common    |  |                           | 30 V DC/ 240 V AC: 2 A/point<br>110 V DC: 0.2 A/point |  |  |  |
| Output delay time  | OFF→ON | 1 ms or less                           | 10 ms or less                            |                           |   |  |  |  |
|  | ON→OFF | 1 ms or less                           |  |                           | 10 ms or less   |  |  |  |
| Leakage current wh   | en OFF | Max. 0.1 mA                            | None                                     |                           |   |  |  |  |
| Surge suppresser c   | ircuit | Clamp diode                            | Varistor                                 |                           |   |  |  |  |
| Maximum opening/closing 3600 times/hour (Restriction with<br>frequency |        | 3600 times/hour (Restriction with indu | ction load applied)                      |                           | 1800 times/hour                                       |  |  |  |
| Common configuration 8 points/common x 2 circuits                      |        | 8 points/common x 2 circuits           |  |                           | 1 point/common  |  |  |  |
| nsulation method   |        | Photocoupler insulation                | Photocoupler insulation Relay insulation |                           |   |  |  |  |
| Delating condition   |        | None                                   |  |                           |   |  |  |  |
| Weight   |        | Approx. 240 g                          |  |                           | Approx. 260 g   |  |  |  |

#### · Communication specifications

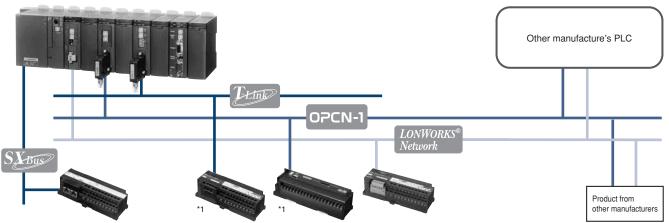
| Item                        | Specifications                                |  |   |  |
|-----------------------------|---|--|---|--|
|                             | OPCN-1  | T-link   | SX bus  | LonWorks                               |
| Transmission line format    | Bus type (multi-drop)                         | Bus type (multi-drop)                          | Bus type (ring)                               | Free topology (bus-type/star-type)     |
| Max. signal points          | 127 words (2032 points)/master                | 128 words (2048 points)/master                 | 512 words (8192 words)                        | 228 bytes                              |
|                             | 125 kbps/1 km                                 |  |   |  |
| Transmission speed/distance | 250 Kbps/800 m                                | 500 kbps/1 km                                  | 25 Mbps/25 m                                  | 78 kbps/500 to 2700 m                  |
|                             | 500 kbps/480 m                                |  |   |  |
|                             | 1 Mbps/240 m                                  |  |   |  |
|                             | (Changes with the switch)                     |  |   |  |
| No. of connected stations   | 31 stations                                   | 32 stations                                    | 254 stations (including CPU module) *2        | 64 units/segment                       |
| Electric characteristics    | EIA RS-485                                    | Dedicated pulse transfer method                | EIA RS-422                                    | -                                      |
| Transmission medium         | Shielded twisted pair cable                   | Shielded twisted pair cable                    | SX bus expansion cable                        | Twisted pair (1P-S)                    |
| Occupied word *1            | 8 points: 1 word, 16 points: 1 word, 32 point | s: 2 words, 8/8 (Mixture): 2 words, 16/16 (Mix | ture): 2 words, analog input: 8 words, analog | output: 4 words, NR1SF-HP4DT: 40 words |

\*1 When the master module of MICREX-SX series is used

\*2 The max. number of the I/O terminal (for SX bus) connections are 10 units each in the inside and outside per base board. Consumes the SX bus transmission power supply by 25 mA per I/O terminal.

#### System configuration

<MICREX-SX: SPH>



\*1 Please mount the terminating resistor with the accessory of the master module (2 pieces provided on the SX) if the I/O terminals for OPCN-1 or for T-link are a terminating station.

(The I/O terminals have not been fitted with terminating resistors.)

### Remote Terminal Master/Slave Module: NP1L-RM1

#### Features

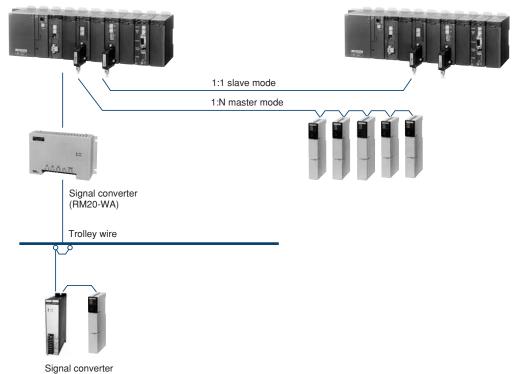
- Connectable to Fuji Electric's RM20 and RM21 remote terminal series.
- Data can be transmitted up to 5 km between master/slave modules and remote terminals.
- The use of a signal converter makes it possible to use existing, unoccupied cables and trolley lines.



#### Communication specifications

| Item             |                                    | Specifications   |  |  |  |
|------------------|------------------------------------|--|--|--|--|
| No. of SX bus co | nnectable modules                  | Max. 8 units/configuration   |  |  |  |
| No. of SX rem    | ote terminal link                  | 1 system   |  |  |  |
| Remote termi     | nal                                | 1:1 mode: Max. 64 words  |  |  |  |
| No. of connecta  | ble terminals/no. of signal points | 1:N or N:N mode: Max. 128 units or 1024 points   |  |  |  |
| No. of connect   | ctable remote                      | 1:1 mode: 1 slave/1 master   |  |  |  |
| terminals        |                                    | 1:N mode: RM20/21 series terminal units  |  |  |  |
| Remote           | Transmission system                | Time sharing cyclic multiplex transmission system  |  |  |  |
| terminal         | Signal/Transmission speed          | RZ signal/2400 baud (Built-in modulation/demodulation reference clock 7.2 K)                               |  |  |  |
| specification    | Transmission form                  | 1:1 transmission (connection of between the SX master and slave station)                                   |  |  |  |
|                  |                                    | 1:N or N:N transmission (Connects existing remote terminals. The NP1L-RM1 slave mode cannot be connected.) |  |  |  |
|                  | Signal transmission line           | Twisted pair cable (CPEV, KPEV), CVV, trolley wires  |  |  |  |
|                  | Transmission distance              | Φ0.9: 2.0 km (Max. 128 remote stations)  |  |  |  |
|                  |                                    | Φ1.2: 3.5 km (Max. 128 remote stations)  |  |  |  |
|                  |                                    | 2 mm <sup>2</sup> : 5.0 km (Max. 64 remote stations)   |  |  |  |
|                  |                                    | 2 to 5 km: Varies with the cable and connection configuration.   |  |  |  |
| External wire    | connections                        | Terminal block 6 poles   |  |  |  |
|                  |                                    | (For transmission wire connections, for 24 V DC external power supply connections, for grounding etc.)     |  |  |  |
| External powe    | er supply (for communication)      | 20 to 30 V DC, 3.6 VA (When 24 V DC: 0.15 A)   |  |  |  |
| Internal curre   | nt consumption                     | 24 V DC, 140 mA or less  |  |  |  |
| Weight           |                                    | Approx. 210 g  |  |  |  |

#### System configuration



(RM20-WB)

#### **USB Communication Module: NP1L-UC1**

#### Features

- Mounted on the base board to connect the CPU module (SPH2000/3000/3000D) with the programming support tool SX-Programmer.
- This module and the programming support tool are connected by a USB cable.
- The maximum number of these modules that can be connected in one configuration is 238.
- The programing support tool can be used while checking the actual state of IOs (actuator, control equipment) by mounting this module on each distributed base board.



#### Communication specifications

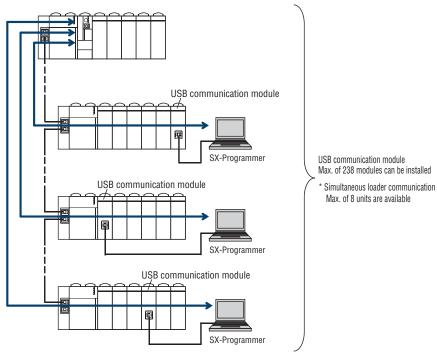
| Item   |                          | Specifications                                  |  |
|--|--------------------------|---|--|
| No. of SX bus connectable modules  |                          | Max. 238 units/configuration *1                 |  |
| USB interface No. of ports   |                          | 1 port  |  |
|  | Transmission speed       | 12 Mbps (USB1.1 Full Speed)                     |  |
|  | Transmission<br>distance | 3 m or less                                     |  |
|  | Connection method        | USB-B connector                                 |  |
| Max. number of units for loader communication<br>simultaneously<br>(Max. number of units for loader command<br>communication simultaneously) |                          | Up to 8 units *2                                |  |
| Combinable CPU   |                          | SPH2000, SPH3000, SPH3000D single CPU system *3 |  |
| Internal current consumption   |                          | 24 V DC 150 mA or less                          |  |
| Weight   |                          | Approx. 140 g                                   |  |

\*1 Cannot be mounted on a remote I/O base board, such as T-link, OPCN-1, DeviceNet, and PROFIBUS-DP.

\*2 Includes the number of other pieces of equipment such as HMI using the loader command communication.

\*3 Cannot be used in combination with SPH200/SPH300/SPH3000MM/SPH3000MG or in a multi-CPU system and CPU redundant system.

#### System configuration



#### Programming support tool SX-Programmer support version

The following version is required to use this module.

- Expert (D300win) V3 (Type: NP4H-SEDBV3)
   : V3.6.9 or later
- Standard (Type: NP4H-SWN) : V3.0.14 or later

#### SX bus Optical Link Module : NP1L-OL1/OL3 SX bus Optical Converter Unit : NP2L-OE1

#### Features

Using an SX bus optical link module/unit makes an SX bus transmission line optical and it possible to build a longdistance distributed system with the SX bus.

#### NP1L-OL1/OL3

 Mounted on the base board to transmit the SX bus signal as an optical signal.

#### NP2L-OE1

• This unit connects between the SX bus cable and optical fiber cable to transmit the SX bus signal as an optical sign.



#### Transmission specifications

| Item                         |                     | Specifications   |  |                          |   |  |
|------------------------------|---------------------|--|--|--------------------------|---|--|
| Model                        |                     | NP1L-OL1   | NP1L-OL3   |                          | NP2L-OE1  |  |
| No. of connectable modules   |                     | Max. 64 units/configuration (total No. of NP1L-OL1, NP1L-OL3 and NP2L-OE1) |  |                          |   |  |
| Optical fiber                | Туре                | PCF (Polymer Clad Fiber)   | Quartz glass multi mode (GI)                                       | Quartz glass single mode | PCF (Polymer Clad Fiber)  |  |
|                              | Core/Clad diameter  | 200 μm/230 μm  | 50/125 µm  | 10 µm or less/125 µm     | 200 μm/230 μm   |  |
| Min. bending radius *1       |                     | 50 mm  |  |                          |   |  |
|                              | Optical connector   | Type: F07  | SC connector   |                          | Type: F07   |  |
| Transmission distance *1     |                     | HC-20/07 made by Sumitomo Electric Industries:                             | 2 km max. between stations (total extension: 64 km)                |                          | HC-20/07 made by Sumitomo Electric Industries:                        |  |
|                              |                     | 400 m max. between stations (total extension: 12.8 km)                     | Multi mode: 2 km max. between stations (total extension: 64 km)    |                          | 400 m max. between stations (total extension: 12.8 km)                |  |
|                              |                     | HG-20/08 made by Sumitomo Electric Industries (discontinued product):      | Single mode: 10 km max. between stations (total extension: 320 km) |                          | HG-20/08 made by Sumitomo Electric Industries (discontinued product): |  |
|                              |                     | 800 m max. between stations (total extension: 25.6 km)                     |  |                          | 800 m max. between stations (total extension: 25.6 km)                |  |
| Internal current consumption |                     | 24 V DC, 54 mA or less   |  |                          | DC 24 V, 70 mA or less  |  |
| Power terminal               | Rated input voltage | -  |  |                          | 24 V DC (DC22.8 ~ 26.4 V)   |  |
| (External power supply)      | Inrush current      | -  |  |                          | 165 mA or less: When a switching power supply is used *3              |  |
| *2                           |                     |  |  |                          | 50 Ao-p-70 µs: When 24 V DC is directly turned ON                     |  |
| Weight                       |                     | Approx. 135 g  |  |                          | Approx. 155 g   |  |

\*1 The minimum bending radius may depend on the type of optical fiber cable used.

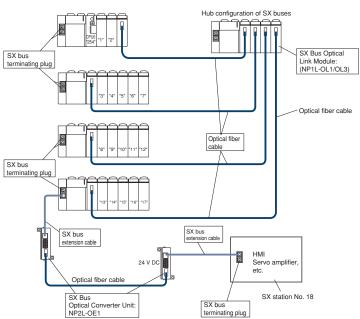
The transmission distance above is achieved at 25°C. The transmission distance is shorter at lower temperatures. For details, contact the optical fiber manufacturer. 2 As an external power supply, use a switching power supply (conforming to the UL standard) with "reinforced insulation" of 24 V DC 1 A or more for each unit.

- \*3 When 24 V DC is directly applied, the rush current is 50 Ao-p, 70  $\mu$ s (reference value). This value depends on power conditions.
- · Recommended cables and tools (For PCF)
- Optical fiber: HC-20/07 made by Sumitomo Electric Industries (type: H-PCF)

HG-20/08 (H-PCF type) made by Sumitomo Electric Industries (discontinued product)

- Optical connector: CF-2071 made by Sumitomo Electric Industries
- · Crimp tool: CAK-0057 made by Sumitomo Electric Industries

#### System configuration



• Replacing existing NP1L-OL2 with NP1L-OL3 The modules are connector compatible, but please replace both ends with NP1L-OL3.

The optical link element of NP1L-OL2 has an optical wavelength of 860 nm, whereas the optical link element of NP1L-OL3 has an optical wavelength of 1310 nm. Since the two modules are not compatible in this respect, both ends need to be replaced to enable communication via optical fiber.

When replacing NP1L-OL2 with NP1L-OL3, the optical fiber utilized with the NP1L-OL2 can be used as-is. Furthermore, CPU module programs and programming support tools can be used as-is without modification.

#### SX bus Electric Repeater Unit: NP2L-RP1

#### Features

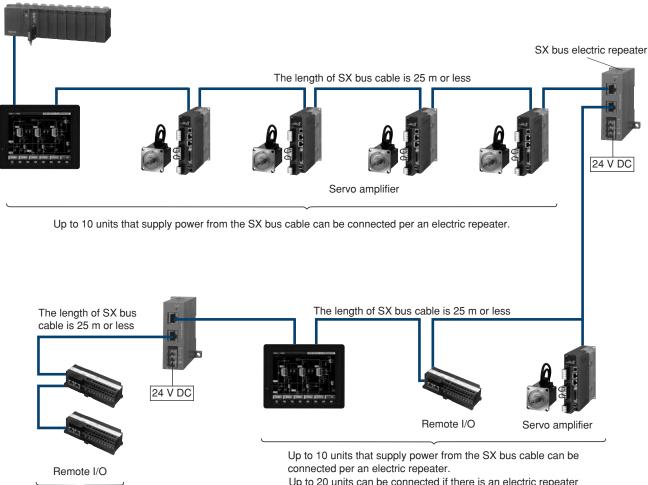
- · SX bus connection using another 25 m electric cable is enabled by correcting the signal waveforms of the SX bus electric cable.
- · Up to three units can be used in one SX system, increasing the total extension length of the SX bus electric cable to a max. of 100 m.



#### Specifications

| Item                           | Specifications    | Remarks   |
|--------------------------------|-------------------|---|
| Rated power supply voltage     | 24 V DC           | Uses externally supplied power                                  |
| Power supply voltage tolerance | 22.8 to 26.4 V DC | Uses externally supplied power                                  |
|                                |                   | When connecting servo and inverter: 24 to 26.4 V DC             |
| Current consumption            | Max. 1470 mA      | Current consumption: Approx. 70 mA                              |
|                                |                   | 24 V power supply to the SX bus cable: Up to two 700 mA systems |
| Dimension (W×H×D) [mm]         | 50 × 95 × 95      | _   |
| SX bus transmission distance   | 25 m              | Total extension of the SX bus cable connected to each connector |
| Max. number of usable units    | 3 units           | The max. total extension of the SX bus cable is 100 m.          |
| Weight                         | Approx. 150 g     |   |

#### System configuration example



Up to 10 units that supply power from the SX bus cable can be connected.

## Programmable Controllers MICREX-SX series Communication Module

#### SX bus Duplication Unit: NP2L-BH1

#### Features

- It is a unit to duplicate the SX bus cable from the base board. It is installed on the left side of the base board (adjacent to the SX bus connector of the base board) to physically separate the SX bus into 2 systems.
- The duplicated SX bus which allows the continued bus communication even when a line disconnection can be applicable to ships, power plants and vehicle systems that require high reliability.



SX bus connection of normal base board section

SX bus connection for this unit connection

#### Specifications

| Item                            | Specifications   |
|---------------------------------|--|
| Communication method            | SX bus communication (conforming to the SX bus transmission specifications)                |
| Number of systems               | 2 systems of IN and OUT  |
| Transmission speed              | 25 Mbps (conforming to the SX bus transmission specifications)                             |
| Interface connection shape      | SX bus extension connector (modular jack)  |
| No. of connectable modules      | Max. 10 units  |
| Connection distance             | Max. of 25 m distance between units, total length of 100 m                                 |
| Power supply                    | Unnecessary external power supply (24 V SX bus cable used)                                 |
| Station number setting function | Available (using the station address setting rotary switch on the unit)                    |
| Installation method             | Independent type (no slots on the base board occupied)                                     |
| Occupied number of I/O points   | Input: 16 points<br>(They are used for the status area and have no actual input function.) |
| Internal current consumption    | 24 V DC, 120 mA or less  |
| Pick-up power source            | Operated by 24 V DC from the SX bus cable.   |
| Weight                          | Approx. 500g   |

#### Duplication operation

Switch operation

When a broken wire is detected, the path is switched to another SX bus cable.



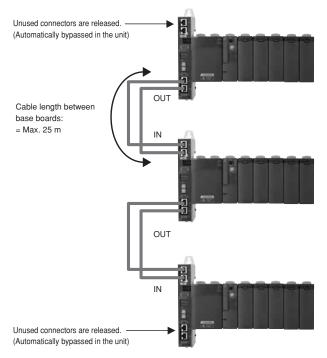
Bypass function

When the SX bus signals on both paths are stopped, the SX bus signals are looped back and the bypass connection is established in the duplication unit. (The SX bus disconnection is prevented.)





System configuration example



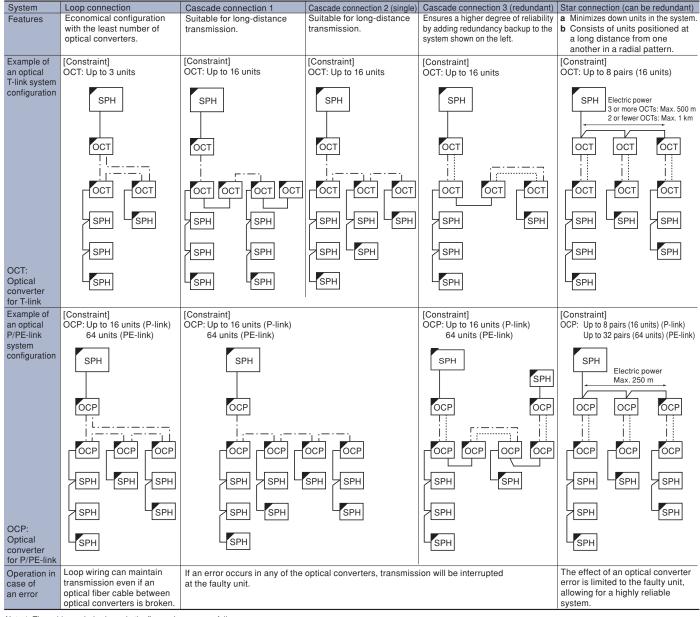
#### **Optical T-link and P/PE-link Systems**

The optical T-link and P/PE-link systems ensure a superior network configuration with distinguished noise resistance by making use of an optical converter and optical fiber cables.

The optical T-link and P-link systems have the following features.

- · System configurations, such as redundant optical lines, can be established.
- Since an electric transmission system and an optical transmission system can be mixed, you can build an economical system by adopting optical transmission systems only for the required portions.
- Optical link systems as shown in the table below can be configured according to your application.

#### Configuration example



Note 1: The cable symbols shown in the figure above are as follows:

Optical fiber cable (redundancy backup)
 Optical fiber cable (redundancy backup)
 Cable for a T-link or cable for a P-link
 Cable for a T-link or cable for a P-link

Note 2: Connect a terminal resistor for a T-link (100  $\Omega$ ) or for a P-link (75  $\Omega$ ) to each unit marked with  $\mathbf{r}$  in the figure.

Note 3: When a cable for a T-link or for a P/PE-link is not connected to an optical converter, connect a terminal resistor to the converter.

#### T-link Optical Converter: FNC160A-C20

#### Features

- This optical converter has two optical transmit/receive modules (two channels).
- The main power supply has a wide input ranging from 100 to 240 V AC/110 V DC.
- System configurations such as cascade connections (up to 16 units), loop connections (up to three units), star connections (up to 8 pairs), and redundant optical lines can be established.
- Function to detect optical transmission line breakage that enables the relay contact to turn on in case of a line breakage.
- This optical converter has a mounting hole compatible with the FNC100/110 and F  $\Box\Box$  140 modules.

#### Specifications



| Item             |                                      | Specifications  |
|------------------|--------------------------------------|---|
| Model compatible | No. of connectable modules           | 32 slave stations on a T-link per master  |
| with T-links     | Transmission speed                   | 500 kbps (RZ)   |
|                  | Cable                                | Shielded twisted pair cable   |
|                  | Terminal                             | 100 $\Omega$ terminal at both segment ends  |
|                  | Transmission distance                | Max. 1 km   |
|                  |                                      | 1 km when a pair of T-KPEV-SB 1.25 mm <sup>2</sup> cables manufactured by Furukawa Electric Co. is used |
|                  |                                      | 700 m when a pair of TKPEV-SB 0.75 mm <sup>2</sup> cables   |
| Compatible with  | Туре                                 | Multimode quartz glass fiber (2-core)   |
| optical fiber    | Refractive index profile             | Gl type   |
|                  | Core diameter/Clad diameter          | 50/125 μm   |
|                  | Numerical aperture                   | 0.2   |
|                  | Transmission loss                    | 3 dB/km   |
| Compatible with  | Optical connector                    | SC type connector   |
| optical modules  | Emission wavelength                  | 860 nm (typ)  |
|                  | Permissible loss (transmit, receive) | 10 dB or below (When 3 dB/km fiber is used: 3 km)   |
| Weight           |                                      | Approx. 1,500 g   |

#### P/PE-link Optical Converter: FNC360A-C20

#### Features

- · This optical converter has two optical transmit/receive modules (two channels).
- The main power supply has a wide input ranging from 100 to 240 V AC/110 V DC.
- For P-link system configurations, cascade connection (up to 16 units), loop connections (up to 16 units), and star connections (up to 8 pairs) can be established.
- For PE-link system configurations, cascade connections (up to 64 units), loop connection (up to 64 units), star connection (up to 32 pairs), and redundant optical.
- Function to detect optical transmission line breakage that enables the relay contact to turn off in case of a line breakage.
- This optical converter has a hole compatible with the FNC320A, FNC302A, FNC300, and FNC200 modules.



#### Specifications

| Item                  |                                      | Specifications  |
|-----------------------|--------------------------------------|---|
| Model compatible with | No. of connectable modules           | P-link: 16 units  |
| P/PE-links            |                                      | PE-link: 64 units   |
|                       | Transmission speed                   | 5 Mbps (RZ)   |
|                       | Cable                                | Coaxial cable (5C2V)  |
|                       | Terminal                             | $75 \Omega$ terminal at both segment ends                       |
|                       | Transmission distance                | P-link: Max. 250 m  |
|                       |                                      | PE-link: Max. 500 m Between stations: Min. 1 m                  |
| Compatible with       | Туре                                 | Multimode quartz glass fiber (2-core)                           |
| optical fiber         | Refractive index profile             | GI type   |
|                       | Core diameter/Clad diameter          | 50/125 µm   |
|                       | Numerical aperture                   | 0.2   |
|                       | Transmission loss                    | 3 dB/km   |
| Compatible with       | Optical connector                    | DL type connector   |
| optical modules       | Emission wavelength                  | 840 nm (typ)  |
|                       | Permissible loss (transmit, receive) | 10 dB or below (7.5 dB or below considering aged deterioration) |
| Weight                |                                      | Approx. 1,500 g   |

#### **Dummy Module: NP1F-DMY**

#### Features

- When your system will be expanded in the future, the dummy module can be used as a substitute for the extension module.
- If an active module has failed during operation of the system, the system can be restarted when you replace the failed module with the dummy module (which, however, cannot perform the functions of the failed module).



#### Specifications

| Item                              | Specifications   |
|-----------------------------------|--|
| Model                             | NP1F-DMY   |
| Position on which a substitutable | All modules except power supply module and CPU module                |
| module can be mounted.            | On a base board directly connected to SX bus                         |
|                                   | Cannot be mounted on a T-link base board or other remote I/O module. |
| No. of occupied words             | 0 words  |
| Internal current consumption      | 24 V DC, 26 mA or less   |
| Weight                            | Approx. 120 g  |

#### Multiuse Communication Module: NP1F-MU1

#### Features

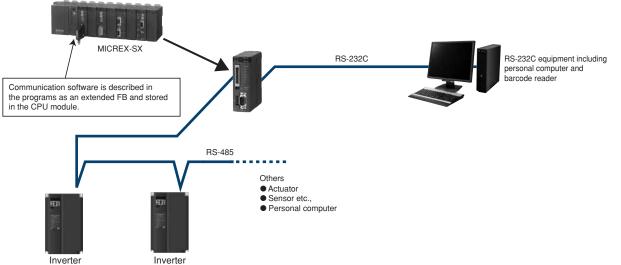
- High-speed communication (RS-485: Max. 460.8 kbps) with actuators and sensors can be implemented.
- Optimal communication with devices of various manufacturers can be implemented by freely creating a communication protocol. Protocols can be created by modifying the sample FB.
- Microcomputer circuit boards can be replaced by creating original firmware.



#### Performance specifications

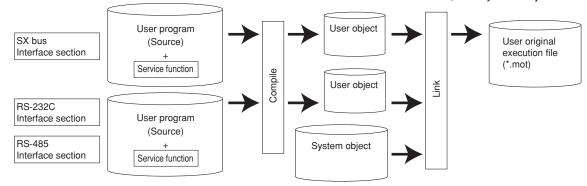
| Item                         | Specifications  |   |  |
|------------------------------|---|---|--|
| Model                        | NP1F-MU1  |   |  |
| Port                         | RS-232C   | RS-485  |  |
| No. of ports                 | 1 channel   | 1 channel   |  |
| Transmission system          | Half-duplex communication method                      |   |  |
| Synchronization method       | Start-stop synchronous transmission                   |   |  |
| Transmission speed           | 300/600/1,200/2,400/4,800/9,600/19,200/38,400/57,600/ | 300/600/1,200/2,400/4,800/9,600/19,200/38,400/57,600/115,200/230,400/ |  |
|                              | 115,200 bps   | 460,800 bps   |  |
| Transmission distance        | 15 m or less  | 1 km or less (transmission speed: 19.2 kbps or less)                  |  |
| No. of connectable modules   | 1:1 (including one external device)                   | 1:31 (Max.)   |  |
| Connection method            | D-sub, 9-pin connector (male) 6-pole terminal block   |   |  |
| Transmission system          | Transmission protocol by creating program             |   |  |
| Internal current consumption | 24 V DC, 80 mA or less                                |   |  |
| Weight                       | Approx. 175 g   |   |  |

#### System configuration



#### Outline of Original Firmware Development

Original high-speed communication modules can be built by combining user programs developed in the C language programming, service functions for multiuse communication modules that can be downloaded from websites, and system objects.



#### Flow Meter F/AD Conversion Module: NP1F-PI4

#### Features

- · Instantaneous and cumulative flows can be displayed at the same time.
- Various flow meters can be connected.
- · No-voltage semiconductor input (two-wire/three-wire)
- · Voltage input (two-wire/three-wire)
- Two-wire current input
- Two-wire contact input
- A transducer is unnecessary as the module is insulated with high pressure-resistance (1000 V AC) between channels.
- A displacement type flow meter (oval type flow meter) can be connected.

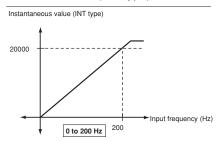
#### Specifications

|  | •  |  |  |
|--|--|--|--|
| Item   | Specifications   |  |  |
| Model  | NP1F-PI4   |  |  |
| No. of input points                          | 4 points   |  |  |
| Connected sensor<br>inputs                   | No-voltage contact pulse, 2-wired open-collector pulse, 3-wired open-collector<br>pulse, 2-wired voltage pulse, 3-wired voltage pulse, 2-wired current pulse   |  |  |
| Input frequency                              | 0 to 10 kHz  |  |  |
| Input wave form                              | Nearly square wave   |  |  |
| Pull-up resistor                             | 22 κΩ  |  |  |
| Max. allowed input                           | -1 to 30 V. 0 to 30 mA   |  |  |
| Min. pulse width                             | 50 µs or more (50 ms or more when filter is set)   |  |  |
| Input<br>signal (Relay/<br>level transistor) | Detection level: ON: 200 Ω or less, OFF: 100 kΩ or more<br>Contact capacity: When the sensor power supply is 13.5 V:<br>15 V DC, 15 mA or more<br>When the sensor power supply is 24 V:<br>30 V DC, 30 mA or more  |  |  |
| Voltage/<br>current pulse                    | Detection level: 3 Vp-p (Current input: Voltage-converted value<br>indicated to the left)  |  |  |
| Input impedance                              | Disabled (10 k $\Omega$ or more), 200 $\Omega$ , 500 $\Omega$ or 1 k $\Omega$ can be selected.   |  |  |
| Input pulse detection                        | AC coupling or rising-edge detection   |  |  |
| Integrated value update cycle                | 5 ms/4 points (1 ms, when for only integrated value mode)  |  |  |
| Response time                                | Integrated value update cycle + tact cycle<br>Instant value update cycle + tact cycle  |  |  |
| Sensor power supply<br>(Where Ta = 25°C) *1  | <ol> <li>1) Output voltage: 13.5 V DC ±15%/24 V DC ±15% (Selection of either one)</li> <li>2) Permissible current; when 13.5 V DC: 35 mA or less, when 24 V DC: 24 mA or less</li> <li>3) Short-circuit limitation current; when 13.5 V DC: approx. 40 mA, when 24 V DC: approx. 28 mA</li> <li>4) Ripple noise: Approx. 250 mV (p-p) or less</li> <li>5) Sudden change of the load: 3 V (0-P) or less (condition of sudden change of the load: 0 to 40 mA)</li> </ol> |  |  |
| Filter function                              | The filter for the chattering removal can be selected.<br>(time constant: approx. 4 ms)  |  |  |
| No. of occupied words                        | Input: 8 words + output 4 words  |  |  |
| Insulation method                            | Photo-coupler insulation and transformer insulation<br>(Between pulse input terminals and FG)<br>Transformer insulation (Between pulse input terminals and channels)   |  |  |
| Dielectric strength                          | <ul> <li>1000 V AC, 1 minute<br/>between pulse input terminals and FG (short circuit current: 10 mA)</li> <li>1000 V AC, 1 minute<br/>between pulse input terminals and channels (short circuit current: 10 mA)</li> </ul>   |  |  |
| Insulation resistance                        | <ol> <li>10 MΩ or more with 500 V DC megger<br/>between pulse input terminals and FG</li> <li>10 MΩ or more with 500 V DC megger<br/>between pulse input terminals and channels</li> </ol>   |  |  |
| Internal current<br>consumption *2           | 390 mA or less (When the sensor power supply is used.)<br>200 mA or less (When the sensor power supply is not used.)   |  |  |
|  | Basically, open  |  |  |
| Non use output treatment                     | basically, open  |  |  |
| Non use output treatment<br>Applicable cable | Use the twisted pair wire with the shield. (Wiring length: 500 m or less)  |  |  |
|  | 22-1   |  |  |

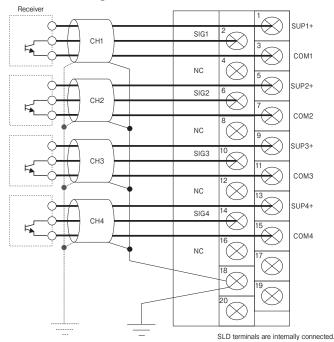
\*1 An ambient temperature during short circuit should be 40°C or less.
\*2 This can be reduced depending on the used number of channels and the used number of sensor power supplies.

#### Characteristic diagram

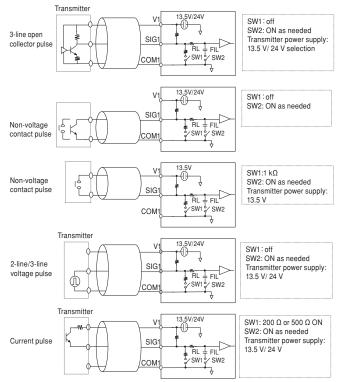
In the case of the input frequency range: 0 to 200 Hz and the instant value unit (INT type): 0 to 23000.



#### External wiring



#### Filter setting switching example



## Programmable Controllers MICREX-SX series Positioning Module

#### High-speed Counter Module: NP1F-HC□

#### Features

#### NP1F-HC2

- · High-speed input pulses can be counted up to 2 channels.
- Compatible with 3 types of input signals.
  1) 90° phase-difference pulse 2) Forward/reverse pulse
  3) Pulse + sign
- 4 types of operation modes
  - 1) Ring operation 2) Gating operation
  - 3) Compare detection operation
  - 4) Phase-Z detecting operation
- Since the input voltage for NP1F-HC2MR supports 5/12/24
   V DC, it becomes possible to standardize the external power supply at 24 V DC and to improve pulse input connectivity.
- The pulse input filter of **NP1F-HC2MR1** is set so that connection with the inverter FRENIC5000 VG7 of Fuji Electric is optimized.



#### NP1F-HC8

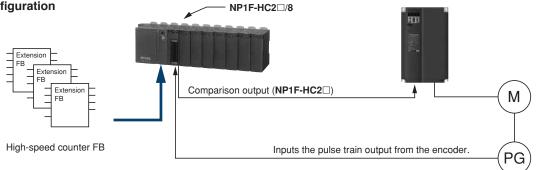
- · High-speed input pulses can be counted up to 8 channel, 50 kHz.
- Compatible with 3 types of input signals.
   1) 000 shace difference rules (2) Forward (assessmelta) (2) Full
- 1) 90° phase-difference pulse 2) Forward/reverse pulse 3) Pulse + sign3 types of operation modes
  - 1) Ring operation 2) Gating operation 3) Resetting operation

#### Performance specifications

| Item           |                             | Specifications  |                                    |   |                                       |
|----------------|-----------------------------|---|------------------------------------|---|---------------------------------------|
| Model          |                             | NP1F-HC2 NP1F-HC2MR NP1F-HC2MR1 NP1F-HC8  |                                    | NP1F-HC8  |                                       |
| Count input    | Input type                  | 2-phase signal (90° phase-di  | fference), forward /reverse sign   | al, coded pulse (Selected by th                   | e software)                           |
| signal         | Level                       | Open collector signal or differ   | rential signal (Differential signa | is based on NP1F-HC2 only)                        |                                       |
|                | Input voltage               | 5 V DC  | 5/12/24 V DC                       |   | 5 V DC                                |
| Counter        | Туре                        | Ring counter function, reset f  | unction, gate function, comparis   | son function (NP1F-HC2□), pha                     | ase Z detection (NP1F-HC2 $\Box$ )    |
|                | No. of channels             | 2 channels (independent)  |                                    |   | 8 channels (independent)              |
|                | Counting speed              | 500 kHz   | 200 kHz                            | 50 kHz  | 50 kHz                                |
|                | Counting range              | Signed 32-bit binary (800000  | 00H to 7FFFFFFH)                   |   | Signed 16-bit binary (8000H to 7FFFH) |
|                | Multiplication function     | x 4 (2-phase signal, 90° phase difference only)   |                                    |   |                                       |
|                | Reset operation             | Soft command  |                                    |   |                                       |
|                | Gating operation            | External input signal and soft command  |                                    |   |                                       |
|                | Compare detecting operation | Hard circuit and soft comman  | Hard circuit and soft command -    |   |                                       |
|                | Phase-Z detecting operation | External input signal and soft command -  |                                    | -   |                                       |
| Comparison     | No. of output points        | 1 point /channel     -       Same as the counting range     -       (Counted value) ≥ (Compared value) to Output ON     - |                                    | -   |                                       |
|                | Comparison range            |   |                                    | -   |                                       |
|                | Comparison contents         |   |                                    | -   |                                       |
|                | Comparison output           | Open collector output (sink type) 24 V DC   |                                    |   | -                                     |
| No. of occupie | ed words                    | Input: 8 words/Output: 8 words (total: 16 words)  |                                    | Input: 10 words/Output: 2 words (total: 12 words) |                                       |
| Internal curre | nt consumption              | 24 V DC, 85 mA or less  |                                    | 24 V DC, 100 mA or less                           |                                       |
| Weight         |                             | Approx. 140 g Approx. 195 g   |                                    | Approx. 195 g                                     |                                       |

#### Function item list

| Function                                | Description   |
|---|---|
| Linear operation (NP1F-HC2 )            | Counting operation for detecting underflow/overflow when the pulse count value is under/over the min./max. value.   |
|   | (Combination with the extension FB)   |
| Ring operation                          | Ring-type counting operation to set the min. value when the pulse count value exceeds the max. value or to set the max. value when the count value is less than the min. value. |
| Gating operation                        | Pulse counting operation activated only when the internal or external gate input is in the counting enabled state.  |
| Reset operation                         | Resetting the counter value to zero (0) by internal command.  |
| Compare detecting operation (NP1F-HC2□) | Comparing the preset compare value and a count value to output the result to the compare output.  |
| Phase-Z detecting operation (NP1F-HC2   | Reading a count value for each phase-Z detection.   |



## Programmable Controllers MICREX-SX series Positioning Module

#### Two-axis Pulse Train Output Positioning Control Module: NP1F-HP2

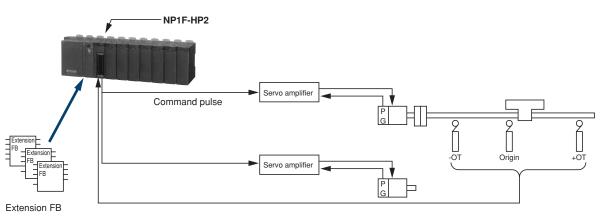
#### Features

- Combined with the servo amplifier motor of the pulse train command input type or the stepping motor driver allows high-precision positioning.
- Use of an extension FB facilitates embedding of the necessary functions including axis-independent singlefunction positioning to multi-axis simultaneous start positioning (pseudo linear interpolation).



#### Performance specifications

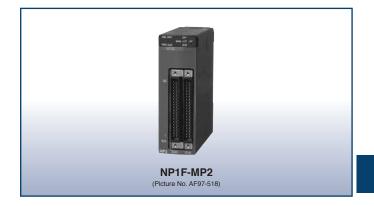
| Item                         |                      | Specifications  |
|------------------------------|----------------------|---|
| No. of control axes          |                      | 2 axes  |
| Positioning control          |                      | Open loop   |
| Acceleration/decelera        | tion characteristics | Trapezoidal acceleration/deceleration (at pulse generation mode)  |
| Max. position data           |                      | Max. 2 <sup>32</sup> -1 pulse /command                            |
| Pulse train command          | Command frequency    | 250 kHz   |
|                              | Frequency resolution | 16 bits/20 bits   |
|                              | Output type          | Open collector output (forward pulse + reverse pulse)             |
| Control functions            |                      | 1 type (Pulse generation mode)                                    |
| Combination actuator         |                      | Servo system prepared pulse train command input or stepping motor |
| No. of occupied words        |                      | Input: 8 words/Output: 8 words (total: 16 words)                  |
| Internal current consumption |                      | 24 V DC, 95 mA or less  |
| Externally supplied power    |                      | 24 V DC, 35mA or less   |
| Weight                       |                      | Approx. 180 g   |



# Two-axis Pulse Train Multiple Positioning Control Module: NP1F-MP2

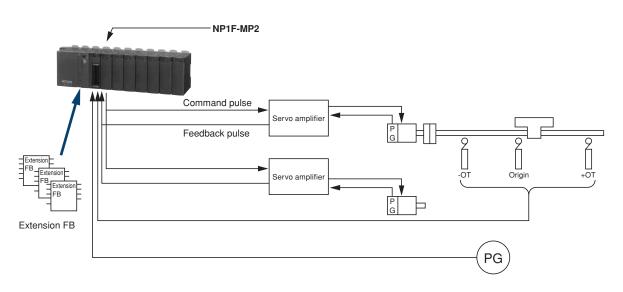
#### Features

- Combined with the servo amplifier motor of the pulse train command input type or the stepping motor driver allows high-precision positioning.
- Use of an extension FB facilitates embedding of the necessary functions including axis-independent single-function positioning to multi-axis simultaneous start positioning (pseudo linear interpolation), interpolation, and cam/running cut.
- Current position (current feedback value) can be detected with the feedback pulse. Two types of operation modes are available (pulse generation mode and position command mode)



#### Performance specifications

| Item                         |                      | Specifications  |  |
|------------------------------|----------------------|---|--|
| No. of control axes          |                      | 2 axes  |  |
| Positioning control          |                      | Open loop   |  |
| Acceleration/decelera        | tion characteristics | Trapezoidal acceleration/deceleration (at pulse generation mode)  |  |
| Max. position data           |                      | Max. 2 <sup>32</sup> -1 pulse/command   |  |
| Pulse train command          | Command frequency    | 250 kHz   |  |
|                              | Frequency resolution | 16 bits/20 bits   |  |
|                              | Output type          | Open collector output (forward pulse + reverse pulse)   |  |
| Feedback pulse               | Input frequency      | 00 kHz  |  |
|                              | Input type           | open collector input or differential signal (90° phase difference, phase A, B and phase Z)                      |  |
| Manual pulse unit            | Input frequency      | 500 kHz   |  |
|                              | Input type           | Open collector input or differential signal (90° phase difference, phase A, B or forward pulse + reverse pulse) |  |
| Control functions            |                      | 2 types (Pulse generation mode, positioning command mode)   |  |
| Combination actuator         |                      | Servo system prepared pulse train command input or stepping motor   |  |
| No. of occupied words        |                      | Input: 14 words/Output: 8 words (total: 22 words)   |  |
| Internal current consumption |                      | 24 V DC, 95 mA or less  |  |
| Externally supplied power    |                      | 24 V DC, 35mA or less   |  |
| Weight                       |                      | Approx. 200 g   |  |

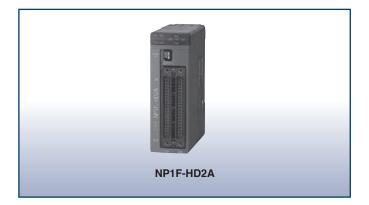


## Programmable Controllers MICREX-SX series Positioning Module

#### Two-axis High-speed Pulse Train Positioning Module (Differential Output): NP1F-HD2A

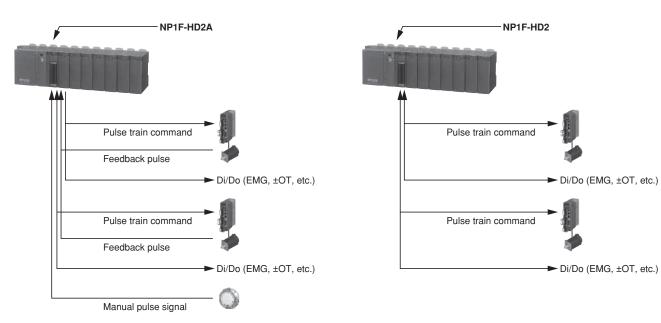
#### Features

- This positioning module operates at a maximum frequency of 5MHz, and performs positioning with a differential signal pulse train. The positioning of two axes can be controlled with a single module.
- Various functions are capable such as single-axis linear positioning, rotor positioning, two-axis linear interpolation positioning, feedback pulse count, and manual pulse input positioning.
- Devices requiring high-frequency pulse signals such as linear servomotors and direct drive servomotors can be controlled.



#### Performance specifications

| Item                         |                      | Specifications  |   |  |
|------------------------------|----------------------|---|---|--|
| Model                        |                      | NP1F-HD2A   | NP1F-HD2  |  |
| No. of controlled axes       |                      | Two axes  |   |  |
| Position control             |                      | Open loop control   |   |  |
| Acceleration/decelera        | tion characteristics | Trapezoidal acceleration/deceleration,  | Trapezoidal acceleration/deceleration                         |  |
|                              |                      | S-shape acceleration/deceleration   |   |  |
| Max. position data           |                      | 2 <sup>32</sup> -1 pulse/command  |   |  |
| Pulse train command          | Command frequency    | 5MHz  |   |  |
|                              | Frequency resolution | 24 bits   |   |  |
|                              | Output type          | Differential output (forward pulse + reverse pulse, 90° phase differential                            | ence 2-phase pulse multiplied by 4, pulse + direction signal) |  |
| Feedback pulse               | Input frequency      | 5MHz  | -   |  |
|                              | Input type           | Differential input (90° phase difference 2-phase pulse multiplied                                     | -   |  |
|                              |                      | by 1/2/4, forward pulse + reverse pulse)  |   |  |
| Manual pulse                 | Input frequency      | 5MHz  | -   |  |
|                              | Input type           | Differential input (90° phase difference 2-phase pulse multiplied                                     | -   |  |
|                              |                      | by 1/2/4, forward pulse + reverse pulse)  |   |  |
| Control function             |                      | Standalone PTP, two-axis linear interpolation, automatic origin                                       | Standalone PTP, override, manual operation                    |  |
|                              |                      | return, override, JOG operation   |   |  |
| Combination actuator         |                      | Servo system or stepping motor equipped with pulse train input function                               |   |  |
| No. of occupied words        |                      | Input: 18 words, output: 10 words (total: 28 words) Input: 8 words, output: 8 words (total: 16 words) |   |  |
| Internal current consumption |                      | 24 V DC, 70mA or less   |   |  |
| External power supply        |                      | 24 V DC, 20mA or less (supplied by external power supply)   |   |  |
| Weight Appro                 |                      | Approx. 180 g   |   |  |



# Two-axis Analog Multiple Positioning Control Module: NP1F-MA2

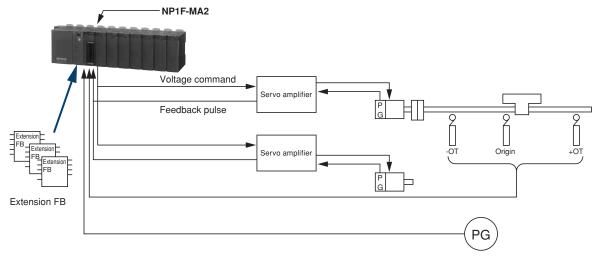
#### Features

- Combined with the servo amplifier motor of the pulse train command input type or the stepping motor driver allows high-precision positioning.
- Use of an extension FB facilitates embedding of the necessary functions including axis-independent single-function positioning to multi-axis simultaneous start positioning (pseudo linear interpolation), interpolation, and cam/running cut.
- 3 types of operation modes are available. (Pulse generation mode, position control mode, position instruction mode)



#### Performance specifications

| Item                  |                         | Specifications   |
|-----------------------|-------------------------|--|
| No. of control axes   |                         | 2 axes   |
| Positioning control   |                         | Semi-closed loop   |
| Acceleration/decel    | eration characteristics | Trapezoidal acceleration/deceleration (at pulse generation mode)   |
| Max. position data    |                         | Max. 2 <sup>32</sup> -1 pulse /command (at pulse generation mode)  |
| Speed command         | Command voltage         | Analog speed command (0 to ±10.24 V)   |
|                       | Signal type             | Analog voltage command   |
| Feedback pulse        | Input frequency         | 500 kHz  |
|                       | Input type              | Open collector input or differential signal (90° phase difference, phase A, phase B and phase Z)                       |
| Manual pulse unit     | Input frequency         | 500 kHz  |
|                       | Input type              | Open collector input or differential signal (90° phase difference, phase A, phase B, or forward pulse + reverse pulse) |
| Control functions     |                         | 3 types (Pulse generation mode, positioning command mode, positioning control mode)                                    |
| Combination actuator  |                         | Servo system prepared analog speed command input   |
| No. of occupied words |                         | Input: 14 words/Output: 8 words (total: 22 words)  |
| Internal current cor  | nsumption               | 24 V DC, 150 mA or less  |
| Weight                |                         | Approx. 200 g  |



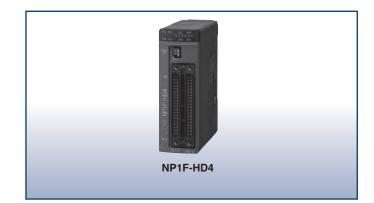
## Programmable Controllers MICREX-SX series Positioning Module

#### 4-axis High-speed Pulse Train Positioning Module (Differential Output): NP1F-HD4

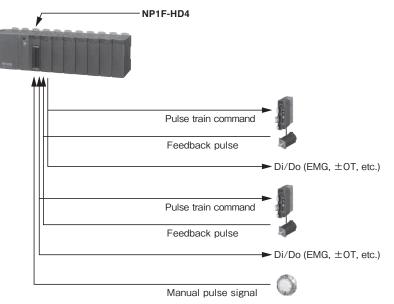
#### Features

- This positioning module operates at a maximum frequency of 5MHz, and performs positioning with a differential signal pulse train. The positioning of four axes can be controlled with a single module.
- Various functions are available such as single-axis linear positioning, rotor positioning, multi-axis linear interpolation positioning, two-axis circular interpolation positioning, helical interpolation positioning, position speed command positioning, feedback pulse count, manual pulse input positioning, PWM pulse output, automatic origin return, absolute position encoder control, electronic cam control and backlash correction.
- Devices requiring high-frequency pulse signals such as linear servomotors and direct drive servomotors can be controlled.

#### Performance specifications



| Item                  |                        | Specifications  |  |  |  |  |
|-----------------------|------------------------|---|--|--|--|--|
| Model                 |                        | NP1F-HD4  |  |  |  |  |
| No. of controlled axe | es                     | 4 axes  |  |  |  |  |
| Position control      |                        | Open loop control   |  |  |  |  |
| Acceleration/decele   | ration characteristics | Trapezoidal acceleration/deceleration, S-curve acceleration/deceleration  |  |  |  |  |
| Max. position data    |                        | 2 <sup>32</sup> -1 pulse/command  |  |  |  |  |
| Pulse train command   | Command frequency      | 5MHz  |  |  |  |  |
|                       | Frequency resolution   | 24 bits   |  |  |  |  |
|                       | Output type            | Differential output (forward pulse + reverse pulse, 90° phase difference 2-phase pulse multiplied by 4, pulse + direction signal) |  |  |  |  |
| Feedback pulse        | Input frequency        | 5MHz  |  |  |  |  |
|                       | Input type             | Differential input (90° phase difference 2-phase pulse multiplied by 1/2/4, forward pulse + reverse pulse)                        |  |  |  |  |
| Manual pulse          | Input frequency        | 5MHz  |  |  |  |  |
|                       | Input type             | Differential input (90° phase difference 2-phase pulse multiplied by 1/2/4, forward pulse + reverse pulse)                        |  |  |  |  |
| Control function      |                        | 1 type (Pulse generation mode)  |  |  |  |  |
| Combination actuate   | or                     | Servo system or stepping motor equipped with pulse train input function   |  |  |  |  |
| No. of occupied wor   | ds                     | Input: 36 words, output: 20 words (total: 56 words)   |  |  |  |  |
| Internal current cons | sumption               | 24 V DC, 120mA or less  |  |  |  |  |
| External power supp   | oly                    | 24 V DC, 95mA or less (supplied by external power supply)   |  |  |  |  |
| Weight                |                        | Approximately 190g  |  |  |  |  |



# 4-axis Pulse Train Output Positioning Control Unit: NR1SF-HP4DT

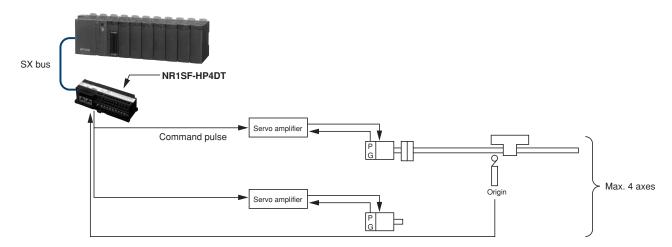
#### Features

- Combined with the servo amplifier motor of the pulse train command input type or the stepping motor driver allows high-precision positioning.
- Minimum program for data setting and command operation that does not need an extension FB allows you to control the positioning.

KR1SF-HP4DT

### Performance specifications

| Item                   |                          | Specifications   |  |  |  |  |
|------------------------|--------------------------|--|--|--|--|--|
| Model                  |                          | NR1SF-HP4DT  |  |  |  |  |
| No. of control axes    |                          | 4 axes   |  |  |  |  |
| Speed command          | Command signal           | Pulse train command  |  |  |  |  |
| Max. command frequency |                          | 250 kHz (conditions: shielded twist pair cable: 2 m or less) |  |  |  |  |
|                        | Output format            | Open collector, sink output                                  |  |  |  |  |
|                        | Max. load current        | 50 mA (24 V DC)  |  |  |  |  |
|                        | Insulation method        | Photocoupler insulation                                      |  |  |  |  |
|                        | Signal type              | Forward pulse (CW) + reverse pulse (CCW)                     |  |  |  |  |
| Feedback pulse inp     | out                      | None   |  |  |  |  |
| External pulse inpu    | t                        | None   |  |  |  |  |
| DI signal              | No. of points            | 8 points (2 points / axis)                                   |  |  |  |  |
|                        |                          | Origin LS (x 4 CH)   |  |  |  |  |
|                        |                          | Timing signal / Phase Z (x 4 CH)                             |  |  |  |  |
|                        | Input format             | Source input (non-voltage contact input)                     |  |  |  |  |
|                        | Input model              | DC (IEC 61131-2 type 2)                                      |  |  |  |  |
|                        | Rated current            | Approx. 4 mA (24 V DC)                                       |  |  |  |  |
|                        | Input impedance          | Αρρτοχ. 5.6 ΚΩ   |  |  |  |  |
|                        | Insulation method        | Photocoupler insulation                                      |  |  |  |  |
|                        | No. of points for common | 2 points (It allows with the common extension bar.)          |  |  |  |  |
| No. of occupied wo     | rds                      | Total: 40 words (input: 16 words / output: 24 words)         |  |  |  |  |
| Internal current cor   | sumption                 | 24 V DC, 20 mA or less                                       |  |  |  |  |
| Externally supplied    | power                    | 24 V DC, 150 mA or less                                      |  |  |  |  |
| Weight                 |                          | Approx. 230 g  |  |  |  |  |



## Programmable Controllers MICREX-SX series **Positioning Module**

#### Positioning Control Module Function List

| No.    | Item  | Function   | NP1F-HD4       | NP1F-HD2A      | NP1F-HD2 | NP1F-HP2  |                     | ZAMI-ALAN           | NP1F-MA2            |                        |                     | NR1SF-HP4DT |
|--------|---|--|----------------|----------------|----------|-----------|---------------------|---------------------|---------------------|------------------------|---------------------|-------------|
|        |   |  |                |                | 0        |           | Pulse<br>generation | Position<br>command | Pulse<br>generation | Positioning<br>control | Position<br>command |             |
| 1<br>2 | Pulse train command<br>Pulse generation mode                                      | Outputs the pulse train command signal for forward and reverse pulses.<br>References the pulse count and frequency data in the CPU module and carries out  | 0              | 0              | 0        | 0         | 0                   |                     | 0                   |                        |                     | 0           |
| 3      | positioning<br>Position control mode positioning                                  | positioning by generating the command pulse using the built-in pulse generator.<br>Directly references position and speed data in the CPU module and carries out   | 0              |                |          |           |                     |                     |                     | 0                      |                     |             |
| 4      | Position command mode   | positioning.<br>References position data in the CPU module and carries out positioning by generating   | 0              |                |          |           |                     | 0                   |                     |                        | 0                   |             |
|        | positioning<br>Automatic origin return behavior                                   | the command pulse using the built-in pulse generator.<br>It is possible to select and use the 6-pattern origin return behavior via the values set in   | 0              | 0              |          |           |                     |                     |                     |                        | $\vdash$            | -           |
|        | JOG operation behavior  | the internal registers.<br>Performs JOG via the values set in the internal registers.  | 0              | 0              | 0        |           |                     |                     |                     |                        |                     |             |
|        | Single-axis positioning behavior  | Performs single-axis positioning via the values set in the internal registers.   | $\overline{0}$ | $\overline{0}$ | 0        | 0         | 0                   |                     | 0                   |                        |                     | 0           |
|        | Two-axis linear interpolation   | Performs two-axis linear interpolation positioning via the values set in the internal  | 0              | $\overline{0}$ |          |           |                     |                     | 0                   |                        |                     | $\vdash$    |
|        | positioning behavior  | registers.   |                |                |          |           |                     |                     |                     |                        |                     |             |
|        | Circular interpolation positioning behavior                                       | Performs interpolation positioning by drawing an arc between the start point (present position) and end point (target position).   | 0              |                |          |           |                     |                     |                     |                        |                     |             |
|        | Helical (linear arc) interpolation positioning behavior                           | Performs interpolation by combining arc interpolation and linear interpolation.  | 0              |                |          |           |                     |                     |                     |                        |                     |             |
|        | Electronic cam behavior   | Performs synchronous positioning via the pre-registered cam pattern.   | 0              |                |          |           |                     |                     |                     |                        |                     |             |
|        | Single-axis positioning speed   | The movement speed can be changed during positioning via the values set in the   | Õ              | 0              | 0        | 0         | 0                   |                     | 0                   |                        |                     | 0           |
|        | override behavior<br>Single-axis positioning target<br>position override behavior | internal registers.<br>The target position can be changed during positioning via the values set in the internal registers.   | 0              | 0              | 0        | 0         | 0                   |                     | 0                   |                        |                     |             |
|        | Single-axis positioning interrupt   | Performs positioning by starting positioning in the interrupt mode and detecting the   | 0              | 0              | 0        | 0         |                     |                     |                     |                        |                     |             |
| 5      | positioning behavior<br>Present Value Count                                       | external interrupt input or Z-phase signal input.<br>Counts command pulses and detects the command present value (counts with pulse<br>multiplied by 4). Note 1)   | 0              | 0              | 0        | 0         | 0                   | 0                   | 0                   | 0                      | 0                   | 0           |
|        |   | Counts the feedback pulse and detects the feedback present value (counts with pulse multiplied by 4). Note 2)  | 0              | 0              |          |           | 0                   | 0                   | 0                   | 0                      | 0                   |             |
| 6      | Z-phase position detection (FB  | Detects the command position at the phase-Z rising edge (or falling edge).   | 0              | 0              | 0        | 0         | 0                   | 0                   |                     |                        |                     | 0           |
|        | based origin return behavior)   | Detects the deviation amount at the phase-Z rising edge (or falling edge).   |                |                |          |           | 0                   | 0                   | 0                   | 0                      | 0                   |             |
| _      |   | Detects the present feedback position at the phase-Z rising edge (or falling edge).  |                |                |          |           | 0                   | $\bigcirc$          | 0                   | 0                      | 0                   |             |
| 7      | Interrupt position detect (Interrupt positioning control operation)               | Detects the command position at the rising edge (or falling edge) of the external interrupt signal.  | 0              | 0              | 0        | 0         | 0                   | 0                   |                     |                        |                     | 0           |
|        |   | Detects the deviation value at the rising edge (or falling edge) of the external interrupt signal.   |                |                |          |           | 0                   | 0                   | 0                   | 0                      | 0                   |             |
|        |   | Detects the present feedback position at the rising edge (or falling edge) of the external interrupt signal.   |                |                |          |           | 0                   | 0                   | 0                   | 0                      | 0                   |             |
| 8      | Automatic-start frequency setting   | Allows the user to set the automatic-start frequency.  | 0              | 0              | 0        | 0         | $\bigcirc$          |                     | 0                   |                        |                     | 0           |
| 9      | Trapezoidal acceleration/<br>deceleration computation                             | Computes trapezoidal acceleration/deceleration.  | 0              | 0              | 0        | 0         | 0                   |                     | 0                   |                        |                     | 0           |
|        | S-shape acceleration/deceleration computation                                     | Computes the S-shape acceleration/deceleration.  | 0              | 0              |          |           |                     |                     |                     |                        |                     |             |
| 10     | Deceleration point automatic<br>computation                                       | Automatically computes the deceleration point.   | 0              | 0              | 0        | 0         | 0                   |                     | 0                   |                        |                     | 0           |
| 11     | Pulse output stop processing  | When the pulse output is interrupted, two types of trapezoidal deceleration (or S-shape deceleration) can be selected.<br>Note 3)  | 0              | 0              | 0        | 0         | 0                   |                     | 0                   |                        |                     | 0           |
| 12     | Emergency stop processing   | Carries out quick stop when an emergency stop error is detected.   | 0              | 0              | 0        | 0         | 0                   |                     |                     |                        |                     | 0           |
|        |   | Immediately stops the pulse output.  |                |                |          |           |                     | 0                   |                     |                        |                     |             |
|        |   | Immediately clears the speed command voltage to zero (0 V).  |                |                |          |           |                     |                     | 0                   | 0                      | 0                   |             |
| 13     | ±OT error detection   | Carries out deceleration and stop when a ±OT error is detected.<br>Immediately stops the pulse output.   | 0              | 0              | 0        | $ \circ $ | 0                   | 0                   | 0                   |                        | $\vdash$            | $ \circ $   |
|        |   | Performs exponential deceleration and stop.  |                |                |          |           |                     |                     |                     | 0                      | 0                   |             |
| 14     | Transmission error monitoring   | Monitors module control program errors on the CPU module. Carries out quick stop when a transmission error is detected.  | 0              | 0              | 0        | 0         | 0                   |                     | 0                   |                        |                     | 0           |
|        |   | Immediately stops the pulse output.  |                |                |          |           |                     | 0                   |                     |                        |                     |             |
| 15     | External pulse count  | Performs exponential deceleration.<br>Counts the external input pulse for manual pulse unit operation or synchronous   | 0              | 0              |          |           | 0                   | 0                   | 0                   | 0                      | 0                   |             |
|        |   | operation.   |                |                |          |           |                     |                     |                     |                        |                     |             |
| 16     | Positioning data first reading  | Up to 4 items of positioning data per axis can be registered in the FIFO buffer. The registered positioning data is executed sequentially.<br>It is also possible to make additional settings in the FIFO buffer during operation. | 0              |                |          |           | 0                   |                     | 0                   |                        |                     |             |
| 17     | External input signal detection   | Detects the input status of all DI signals.  | 0              | 0              | 0        | 0         | $\bigcirc$          | 0                   | 0                   | 0                      | 0                   | 0           |
| 18     | External output signal setting  | All DO signals can be switched with the CPU module.  | Ō              | Ō              | Õ        | Ō         | Õ                   | Ō                   | Ō                   | Õ                      | Ō                   | Õ           |
|        | PWM pulse output behavior   | The PWM pulse output can be implemented via the values set in the internal registers.  | 0              | 0              |          |           |                     |                     |                     |                        |                     |             |
|        | ABS encoder control behavior  | Absolute values can be obtained from the $\Sigma$ -7S Series encoder manufactured by   | 0              | 0              |          |           |                     |                     |                     | 7                      | 1 1                 |             |

Note 1) Counting is performed for NP1F-HD2, NP1F-HD2A and NP1F-HD4 with the single-phase or two-phase pulse multiplied by 4.

Note 2) Counting is performed for NP1F-HD2A and NP1F-HD4 with the single-phase or two-phase pulse multiplied by 1 and 4. Note 3) The S-shape deceleration only corresponds to NP1F-HD2A and NP1F-HD4.

#### Positioning Control Extension FB Software

This is extension FB software which presents a positioning function in combination with a positioning module.

This FB software can be downloaded from our website at no charge.

#### High-speed counter/multi-channel high-speed counter extension FB

This FB allows to use a high-speed counter module (NP1F-HC $\Box$ ). A multi-function FB and a simple-function FB are available.

#### Counter FB for high-speed input

This FB allows to use the pulse counter input function of the high-speed digital input module (NP1X3206-A).

#### Simple positioning control extension FB

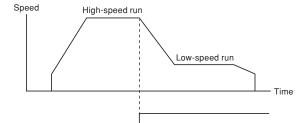
This is a simple positioning control FB for the digital output module (NP1Y32T09P1-A) containing a pulse train output function. It performs 1-axis PTP positioning with pulse train instructions.

#### Positioning FB

 1-axis PTP positioning FB (pseudo straight line interpolation function included) (SPH300)

This FB is used to accelerate up to the set speed and then reduce the speed and stop at the set position. With the extension FB, position control also is performed. Therefore, desired positioning is possible merely by setting a target position and speed through the sequence program. This FB also allows you to switch the speed by means of the override function (etc.) when in operation, and easily enables the reduction of feeding time through high-speed running and high-precision positioning through low-speed running. Moreover, the position and speed to be instructed can be set in units of mm or mm/s. Pulse number conversion of position data is performed with this FB, so that the ease of use is increased.

This is optimum for feed and assembly machines such as basic loaders and unloaders.



Override

In addition, the FB enables pseudo straight line interpolation motions through simultaneous initiation of two, three, or four axes. This usage is applicable to control of high-rise warehouses or assembly machines, for example. It also enables pseudo straight line interpolation motions regarding arbitrary two axes among multiple axes. The FB is also effective for controlling feed lines. This FB is applicable to a pulse train multiple positioning control module, analog multiple positioning control module, and pulse train output positioning control module.  Highly-functional 1-axis positioning FB (SPH300) This FB presents a 1-axis PTP positioning function combined with S-curve acceleration/deceleration and manual pulse run functions.

This FB is needed for electronic cam and traveling cut-off operation. This FB is applicable to a pulse train multiple positioning control module and analog multiple positioning control module. Compact 1-axis FB

This FB allows you to decrease the size of programs to be subjected to the pulse train multiple positioning control module and analog multiple positioning control module and reduce the data quantity in memory. It serves to perform 1-axis PTP positioning. This FB is optimum for application to SPH200.

#### Electronic cam FB (SPH300)

Positioning through cam motions has been adopted for control of various machines including packaging machines. Using this FB enables various cam mechanism motions (cam patterns), eliminating the need for any set-up change which is needed for a mechanical cam. Moreover, this FB enables motions which cannot be conducted by a mechanical cam.

Cam operation FB

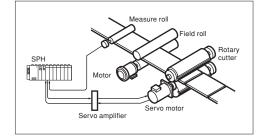
This FB serves to perform 1-axis cam positioning. It not only can be used as a substitute for conventional motions of a mechanical cam but also allows motions which cannot be conducted by a mechanical cam.

This FB is applicable to a pulse train multiple positioning control module and analog multiple positioning control module.

Moreover, the extension FB is available that contains a function needed for control of a traveling cut-off machine. Work which synchronizes with conveyor speed does not need the conveyor to be stopped and restarted, largely helping to increase the speed of a machine. This FB has been used for various kinds of machine control besides control of traveling cut-off machines. Using this machine eases synchronization control. This FB is applicable to a pulse train multiple positioning control module and analog multiple positioning control module.

#### Rotary shears control

Rotary shears control refers to cutoff control regarding a rollshaped cutoff section (cutter or press), by which materials that are continuously fed (film, paper, etc.) are cut off at the same speed as the feeding speed. This usage is applicable to packing machines and film manufacturing machines, for example. The figure below shows the configuration of a film cutoff machine which detects the speed of film moving through its measure roll and cuts off film at the same speed as the feeding speed.

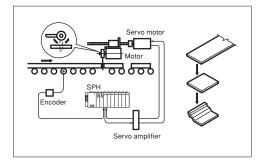


## Programmable Controllers MICREX-SX series Positioning Control Extension FB

#### · Flying shears control

Flying shears control refers to cutoff control regarding a cutoff section (cutter or press) containing ball screws or racks/ pinions, by which materials that are continuously fed (iron plates, external wall materials, clay, etc.) are cut off at the same speed as the feeding speed. This usage is applicable to metalworking machines, tile manufacturing machines, and painting machines, for example.

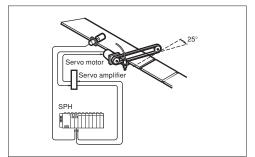
The figure below shows the configuration of a tile manufacturing machine which detects the speed of clay moving through its measure roll and cuts off clay while synchronizing its rotating knife blade with the clay's speed.



#### Flying cutter control

Flying cutter control refers to cutoff control regarding a cutoff section (cutter or water jet) containing ball screws, racks/ pinions, and chains by which materials that are continuously fed (film, paper, plastic, etc.) are cut off at a determined angle at the speed which is proportional to the feeding speed. This usage is applicable to board manufacturing machines, for example.

The figure below shows the configuration of a machine which detects the speed of paper or plastic moving through its encoder and cuts off the material by water jet synchronizing with the feeding speed of paper or plastic.



#### VARICAM FB

This FB enables VARICAM functions. It detects the angle (current value of works) of the main axis of a machine and switches On and Off output signals of the set angle (work position) of the main axis.

This FB is applicable to a pulse train multiple positioning control module, analog multiple positioning control module, and pulse train output positioning control module.

#### **Functional Extension FB Software**

#### Easily realizes functional extension by software

External fault diagnostic and adjustment system functions can also be implemented with software (an expansion FB) by using the enhanced processing functions of the CPU module. The software processing section is placed in the CPU section as an expansion FB and only the external equipment interface processing is separately performed in the I/O section. Thus, an optimum system can be configured according to the function of performance requirements.

#### Diagnostic FB

Necessary diagnosis can be conducted only by selecting an extended FB for each diagnostic function. If this software is stored in the CPU module for control programs, it is unnecessary to add any other special function module. When it is used in the multi-CPU configuration, independence of the control CPU can also be preserved.

For notification of the diagnostic results to the external equipment, Ethernet or a network of general-purpose communication modules or equivalent can also be used.

 Extension FB which implement the malfunction diagnostic functions

The following diagnostic and data sampling FBs are available:

- · Sequence/time diagnostic FB
- · Time diagnostic FB
- · Upper/lower limit diagnostic FB
- $\cdot\,$  Data sampling FB

#### PID FB

Instrumentation control and sequence control were conventionally separated with respect to both hardware and software. When packaged as an extended FB, this adjustment system computing function is a true linkage between instrumentation control and sequence control. In addition, the restriction on the control loop count has sufficient expandability in a multi-CPU configuration. The number of FBs that can be stored in a CPU module is limited by the number of program steps and the sampling rate.

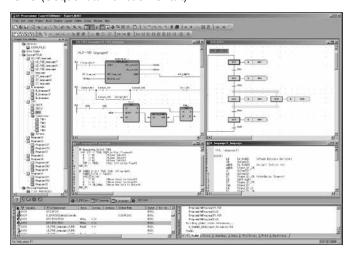
- · Extension FB realizing the temperature regulation system
  - · ON/OFF control FB
  - · PID FB with auto-tuning

#### Programming Support Tool Programming Support Tool: NP4H-SEDBV3 SX-Programmer Expert (D300win)

#### Features

 Completely conforms to the IEC61131-3 International Standard D300win supports five types of program representations completely conforming to the IEC61131-3 International Standard. It allows the programmer to code the proper combination of program representations for the control target.
 Supported representations

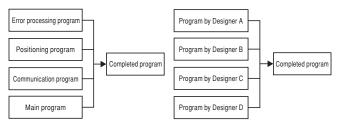
- IL (Instruction List)
- IL (INSTRUCTION LIST)
- LD (Ladder Diagram) FBD (Function Block Diagram)
- ST (Structured Text)
- SFC (Sequential Function Chart)



#### Structured programming

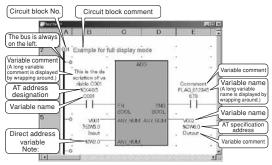
Programming in units of POU or worksheets allows the use of the structured design method by which a program is created by dividing it by functionality or process.

This method enables multiple designers to divide the program design among them so that a substantial reduction in the program creation time can be achieved.



• Ladder programming using key operations (grid fixed method) Ladder programming can be performed using familiar key operations:

- · Standard display mode (variable only)
- · Extended display mode (variable + AT specification address)
- All display mode (variable name + AT specification address + variable comment)

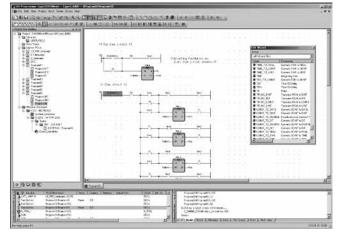


#### Note: If a direct address variable (= no variable name) is used, no variable comment is displayed, even if it is registered.

# Free description of programs and comments (Free editing style)

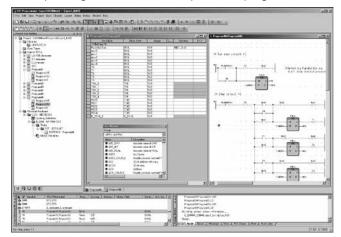
Programs can be described in any location on a worksheet to facilitate understanding of the processing relationships such as in linkage between the interlock condition and the sequence processing section/computing section, allowing efficient programming.

In addition, when a comment is described on a worksheet, the programmer can put a local comment for each circuit block as well as a comment in units of contacts, coils, or circuits, greatly contributing to ease of reading and understanding.



#### • Programming with variables (labels)

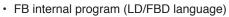
Differing from conventional programming, the Expert (D300win) Programming Support Tool uses label programming (addresses are automatically assigned) in which the address section is described like conventional comments, enabling program coding without being conscious of memory addressing. After the programming, any changes in address assignment can be accommodated by merely changing the corresponding label definition to update the program.

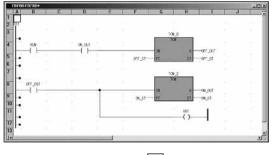


#### Integrates user-original circuits into an FB

Frequently used routine programs or circuits can be integrated into an FB so that the programmer can easily reuse them. For FB generation, the user can select a language compatible with IEC61131-3 supported by Expert (D300win) instead of a special language.

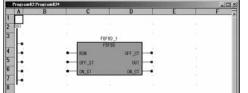
This is also effective for circuit standardization or structuring if a single control block is integrated into an FB.



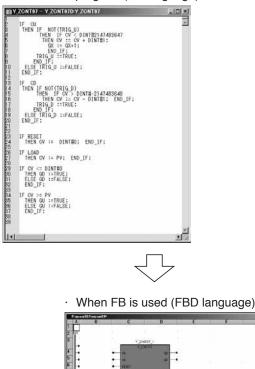




· When FB is used (FBD language)



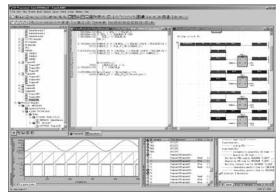
· FB internal program (ST language)



#### Simulation function

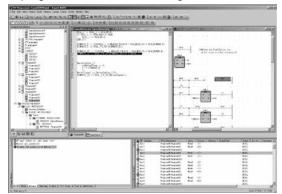
This tool makes it possible to carry out a program logic test using the software PLC function for simulation built in Expert (D300win), without using the actual unit.

It performs operating simulation of a program written with a programming language conforming to IEC 61131-3. It enables forced ON/OFF and monitoring of any signal, and exhibits its ability to remarkably improve the programming and debugging efficiency for the SX Series.



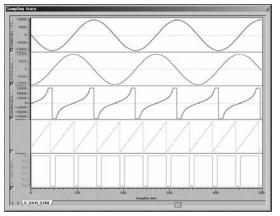
#### Error & jump check function

The tool performs a program syntax check at the time of program compilation to detect syntax errors. It is possible to jump to an error position by double-clicking an error detection section. This function, together with the cross-reference function and data watch window function, exhibits its strengths in program correction and testing.



#### Sampling trace

Sampling trace function saves variable (memory) data change during PLC is in RUN. It is possible to show sampling data on a sampling trace window as a graph. Sampling data is automatically saved with the project file. This saved sampling data can be exported as a CSV file (ASCII data).



#### Documentation function

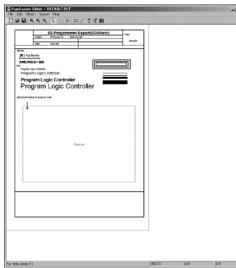
The documentation preparation function has been substantially improved. Not only can it print drawing numbers, dates, page, and drawing borders, but also company logos and comments. It also augments the print preview function, which allows the user to verify the print state on the screen before beginning printing, and the scaled printing function which eliminates the need to select the paper size.

#### · Layout function

The layout function allows the user to print a program list in a free, user-original format. The created layout can be stored as a layout library, which can be used when necessary.

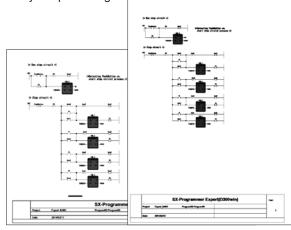
Frame creation: Program list can be printed with frames. The frames can be freely designed facilitating reproduction of a conventionally used drawing sheet.
Company logo: Company logo can be attached to a document. It is created as BMP data and pasted to the frames.
Drawing number: Drawing number can be placed in a specified position within the frame.
Page number: Page number can be placed in a specified position within the frame.

Comment: Comments can be placed in a specified position within the frame.



#### Preview function

Use of the preview function before printing allows the user to verify the print image.



#### · Scaled printing

Documents can be printed in enlarged or reduced size. The paper size can be freely selected according to the purpose. The number of programs printed on a single sheet can be freely adjusted to provide uniform documentation.

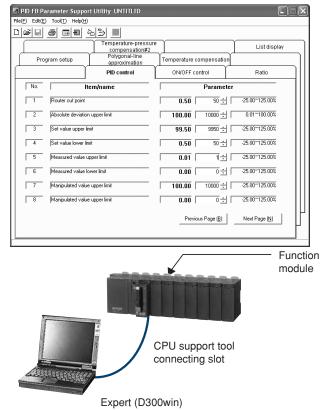
#### • Function module support

The function module support (built-in each extended FB software package) has been realized as a common support tool. Thus, a dedicated loader is not required.

• Sharing program definitions including variable names Labels and files defined/created with the Expert (D300win) programming support tool can be used as they are from the function module support tool. This makes it possible to not only reduce the programming workload, but also unify management of programs.

· Sharing the support tool connection port

The function module support tool can be used even when the IEC programming support tool remains connected to the CPU module (without being connected to the function module). The support function can be used only by starting the function module support tool. Parameter transmission between the CPU module and the function module is carried out by the extended FB.



#### HMI linkage function

Screen creation for the Programmable Operation Display (POD) can be performed using variable names set with Expert (D300win).

· HMI screen creation software

HMI screen creation software and Expert (D300win) run on a personal computer, which is the common platform.



#### Multi-user support

A development environment that allows multiple users to simultaneously access a source project and has a mechanism for exclusive access control is offered.

Exclusive control of projects is automatically performed by support tool operations.

- Management, registration, and creation of client projects
   with respect to a server project
- Check-in/check-out in units of POU
- Compatible with a Japanese and English OS

Compatible with a Japanese OS and English OS using the same format.

#### EtherCAT configurator

Enables configuration of EtherCAT network by starting the EtherCAT configurator from Expert (D300win).

- Batch management of EtherCAT master and slave configuration with simple operations from the tree view
- Flexible system configurations with Fuji Electric original networks (SX bus, E-SX bus, T-link, etc.)

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#### Operating environment

| Item                          |   | Specifications  |  |  |  |
|-------------------------------|---|---|--|--|--|
| Hardware IBM-PC/AT compatible |   | IBM-PC/AT compatible  |  |  |  |
| CPU                           |   | Processor or SoC (at least 1 GHz)   |  |  |  |
| Hard disk                     |   | Free space of 30 Gbytes or more   |  |  |  |
| CD-ROM unit                   |   | 1 unit (x 4 speed or faster), media: ISO 9660 format                              |  |  |  |
| Memory capacity               |   | 32-bit OS: 2 GB or more; 64-bit OS: 4 GB or more                                  |  |  |  |
| Keyboard                      |   | 109-key keyboard (or 101-key keyboard for English OS)                             |  |  |  |
| Mouse                         |   | USB mouse, bus mouse, or PS2 mouse  |  |  |  |
| Indicator                     | icator 800 x 600-dots resolution or higher (1024 x 768-dots resolution or higher recommended) |   |  |  |  |
| Communication                 | RS-232C   | 9600 to 57600 kbps (default setup according to resource model selection)          |  |  |  |
| interface                     | Ethernet  | Possible  |  |  |  |
|                               | ISDN  | Possible (analog port is used)  |  |  |  |
|                               | USB   | Possible with V1.1 (Target CPU: SPH300 (NP1PS- R), SPH300EX, SPH2000 and SPH3000) |  |  |  |
|                               | P/PE-link   | Possible with V2.0  |  |  |  |
|                               | SX bus  | Possible  |  |  |  |
|                               | FL-net  | Possible  |  |  |  |
| OS                            | *1  | Windows 7/8/8.1/10  |  |  |  |
| Portability                   |   | Depends on commercial mobile personal computer.                                   |  |  |  |
| Environmental durabili        | ity   | Depends on environmental conditions of commercial personal computer.              |  |  |  |

\*1 Windows 10 (Ver. 1511 or higher) is required for the engineering of the SPH5000EC using the programing support tool.

#### System configuration SPH2000/SPH3000/ SPH3000MM/SPH3000MG/ SPH5000M/SPH5000H/ SPH5000EC Н Ethernet crossover cable (commercial product) SPH300 (R type)/ SPH300EX/SPH3000/ Personal computer USB/USB-miniB cable (commercial product) SPH3000MM 무미 С USB/USB-B cable (commercial product) SPH200/SPH300/ ann SPH300EX/SPH2000/ SPH3000/SPH3000MM/ SPH3000MG/SPH5000M/ SPH5000H/SPH5000EC/ Assist tool connection cable: NW0H-CA3 BACnet MS/TP CPU\* USB/RS-422 signal converter: NP4H-CVU TR Expert USB/USB-mini B cable : 30cm (Supplied with NP4H-CVU)

\*Only for Japan's doemestic market

## Programmable Controllers MICREX-SX series Programming Support Tool Standard

#### Programming Support Tool: NP4H-SWN SX-Programmer Standard

#### Features

#### Familiar user interface

The user interface and ladder programming support SPB programming equivalent to a FLEX-PC Windows-compatible PC loader. Support for full-keyboard operation is also handy for on-site

debugging and maintenance.



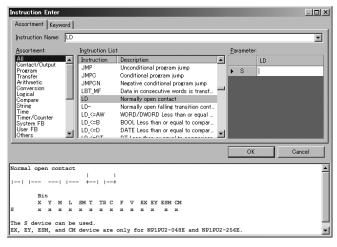
#### Multi-language support

Program representations support the LD language, which is most standard. The ST and FBD programming languages are also supported. Programming in units of POU in which the structured design method is applicable can be performed.

#### Intuitive screen operation

## The easy-to-see and understandable layout enables you to intuitively operate the screen.

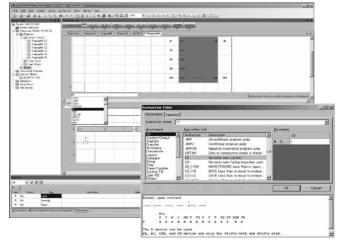
- Command word input is simplified by the command jog bar and the command word candidate narrow-down function based on a keyword search.
- · Multiple sheet display and a flexible layout help improve operation efficiency.
- Input can be completed on a single screen because operands can be input in succession.
- · Operation help corresponding to the screen displayed makes a manual no longer necessary.



#### Supports a variety of input methods

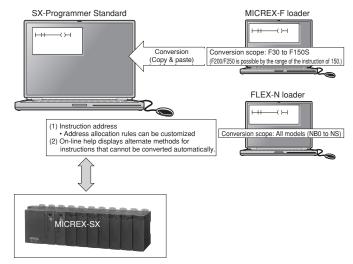
Standard supports three input methods, and you can select the optimum input method for the situation.

- Data can be input simply by operating the mouse wheel and clicking the mouse button. You can register any command words you desire.
- Even if you do not know a command word, you can easily narrow down command words through a keyword search.
- Candidates can be automatically displayed by mnemonic input mainly using the keyboard and the Intellisense function.



#### Leverage your program assets

You can make good use of program assets for the MICREX-F and FLEX-PC series of our PLC. For circuits and commands not supported by Standard, alternative methods are described in the Help section.



#### Resume function

When the SPH starts to run, it automatically displays the position last edited or monitored.

When you go on-line, monitoring starts at the position you were monitoring last time.

When you are off-line, the system transitions to edit mode displaying the point you were editing last time.

#### Password function

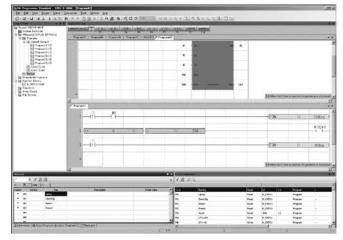
By setting an access authentication password for on-line functions, operation of the PLC can be limited to three levels, i.e., level 1, level 2, and level 3.

## Programmable Controllers MICREX-SX series Programming Support Tool Standard

#### Device editor

Device information is displayed on a single screen, for example, in the form of a list of the operating states of devices, enabling you to save time in memory management.

- · Key operations are similar to those in Excel.
- · All addresses can be displayed.
- The device editor not only displays the operating state of devices but also enables you to edit programs.



#### Collation function

You can display details of different points on programs and edit by referring to collation results.

- You can quickly check different points with the aid of a filter display of collation results.
- · You can edit a program while checking different points.
- With the Update button, programs can be promptly updated to the latest comparison results after editing.

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|   | Millio Designational<br>Control Control Control<br>Control Control Control<br>Control Control Control<br>Control Control Control<br>Control Control Control<br>Control Control Control Control<br>Control Control C | Salara Hujari<br>Salara Hujari<br>Maha Salara<br>Salara Shida Salara<br>Salara Shida Salara Shida<br>Salara Shida Salara Shida<br>Salara Shida Salara Shida   | Enter Breter Mider<br>Dates Breter Mider<br>Dates for the Self-Bar<br>Dates Data Data Date   | 1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1<br>1 | ich<br>Sefe<br>Derfig<br>Seret<br>Reet<br>Auf<br>Ch-P  | 111111   | Kjons<br>Kjons<br>Kjons<br>Kjons<br>Kjons<br>Kjons   | l           | Rayan<br>Angan<br>Angan<br>Angan<br>Angan<br>Angan<br>Angan<br>Angan  |              |
|   | PT1 DelaTex<br>Jama Delman<br>Later Delman<br>Later Delman<br>Later Delman<br>Later Delman<br>Later Delman<br>Later Delman<br>Later Delman  | Salara Hujari<br>Salara Hujari<br>Maha Salara<br>Salara Shida Salara<br>Salara Shida Salara Shida<br>Salara Shida Salara Shida<br>Salara Shida Salara Shida   | La Desert Bracker Die Ger<br>Diesert Bracker Die Ger<br>Dieser Her rag Vell Ger<br>Grone Die Gerbeite<br>Of Lawren Gerbeite  |   | Sile<br>Unit<br>Social<br>Invest<br>Mart<br>Crive<br>Crive<br>Social<br>Invest   | 1111111  | K, persis<br>K, persis<br>K, persis<br>K, persis<br>K, persis<br>K, persis<br>K, persis<br>K, persis<br>K, persis<br>K, persis   | l           | Figure<br>Figure<br>Figure<br>Figure<br>Figure<br>Figure<br>Figure<br>Figure                                      |              |
|   | PT1 Dist Tex<br>Locks Downson<br>Locks Downson<br>Locks Downson<br>Locks Downson<br>Locks Downson<br>Locks Downson<br>Locks Downson<br>Locks Downson<br>Locks Downson<br>Locks Downson  | A Server Repert   | Example of the second s |   | Constant<br>Series<br>Description<br>Preset<br>work<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Description<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset             | 1111111  | K, Delta<br>K, Delta<br>K, Delta<br>K, Delta<br>K, Delta<br>K, Delta<br>K, Delta<br>K, Delta   | l           | Tagan<br>Tagan<br>Tagan<br>Tagan<br>Tagan<br>Tagan<br>Tagan<br>Tagan<br>Tagan<br>Tagan                            |              |
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Constant<br>Series<br>Description<br>Preset<br>work<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Con-Pt<br>Description<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset<br>Preset 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## • Compatible with a Japanese and English OS

Compatible with a Japanese OS and English OS using the same format.

#### Operating environment

| Item                  |           | Specifications   |
|-----------------------|-----------|--|
| Hardware              |           | IBM-PC/AT compatible   |
| CPU                   |           | Processor or SoC (at least 1 GHz)  |
| Hard disk             |           | Free space of 200 Mbytes or more   |
| CD-ROM unit           |           | 1 unit (x 4 speed or faster), media: ISO 9660 format                                   |
| Memory capacity       |           | 32-bit OS: 1 GB or more; 64-bit OS: 2 GB or more                                       |
| Keyboard              |           | 109-key keyboard (or 101-key keyboard for English OS)                                  |
| Mouse                 |           | USB mouse, bus mouse, or PS2 mouse   |
| Indicator             |           | 800 x 600-dots resolution or higher (1024 x 768-dots resolution or higher recommended) |
| Communication         | RS-232C   | 9600 to 57600 kbps (default setup according to resource model selection)               |
| interface             | Ethernet  | Possible   |
|                       | ISDN      | Possible (analog port is used)   |
|                       | USB       | Possible with V1.1 (Target CPU: SPH300 (NP1PS- R), SPH300EX, SPH2000 and SPH3000)      |
|                       | P/PE-link | Possible with V2.0   |
|                       | SX bus    | Possible   |
|                       | FL-net    | Possible   |
| OS                    |           | Windows 7/8/8.1/10   |
| Portability           |           | Depends on commercial mobile personal computer.  |
| Environmental durabil | ity       | Depends on environmental conditions of commercial personal computer.                   |

#### System configuration

Standard

For information on how to connect Standard with PLC, refer to "System configuration" in Expert.



#### PCI-Bus-Based FL-net (OPCN-2) Ver. 2.0 Board: NP3L-FL3PCS

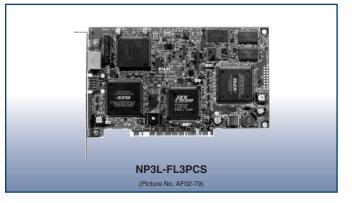
#### Features

- Two different communication functions by application With cyclic communication, this board supports both the common memory function, which allows each node to share the same data, and the message communication function, which exchanges only the necessary information when required.
- Large capacity common memory The capacity of the common memory is 8 Kbits and 8 Kwords.
- High reliability by the master-less method Since no master exists, participation and removal of each node can freely be performed without affecting communication of other nodes. The power of any node can be turned ON or OFF, allowing easy maintenance.

#### Performance specifications

Performance and specifications of the built-in board type FL-net board NP3L-FL3PCS are equivalent to those of the module type NP1L-FL3.

For details on performance and specifications, refer to "FL-net



(OPCN-2) Ver. 2.0 Module: NP1L-FL3" in this catalog. This board conforms, however, only to the transmission specification 10BASE-T, 100BASE-TX, and not to 10BASE5.

#### Operating environment

| Item                          | Specifications   |
|-------------------------------|--|
| Hardware                      | IBM-AT compatible *1   |
| CPU                           | Intel Pentium 233 MHz or higher  |
| Hard disk                     | Free space of 10 Mbytes or more (and necessary disk capacity for Expert (D300win)) |
| CD-ROM unit                   | 1 unit (x 4 speed or faster), media: ISO 9660 format                               |
| Memory capacity               | 64 Mbytes or more (256 Mbytes or more recommended for Expert (D300win) operation)  |
| Keyboard                      | 101 English keyboard   |
| Mouse                         | USB mouse, bus mouse, or PS2 mouse   |
| Indicator                     | 800 x 600-dots resolution or higher  |
| OS                            | Windows2000/XP/NT 4.0/7  |
| Environmental durability      | Depends on environmental conditions of commercial personal computer.               |
| Language for user application | Microsoft Visual Basic   |
| software development          | Microsoft Visual C++   |
| Communication protocol        | TCP/IP protocol  |
| Weight                        | Approx. 140 g  |

\*1 The board size supports a full-size PCI slot (For more information, refer to the Dimensions "PCI-bus based board" in this catalog).

#### PCI-Bus-Based LE-net Loop 2 Board: NP3L-LL2PCS

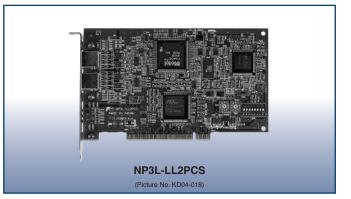
#### Features

- LE-net is an original network of Fuji Electric. It is a low-priced link board between processors to conduct communication with other nodes connected to the LE-net.
- Broadcast communication and message communication can be conducted.
- The LE-net can be connected either as a multi-drop network or a single loop redundant wiring network. The loop network includes a loop-2 network in which the user data send/ receive area is extended. For this board, the loop-2 mode has been adopted.
- If the transmission line is broken, a transmission error occurs in a multi-drop network, but in a loop network, data communication between nodes can continue. This enables construction of a highly reliable system at a relatively low cost.

#### Performance specifications

Performance and specifications of the built-in board type LEnet loop 2 board NP3L-LL2PCS are equivalent to those of the module type NP1L-LL2.

However, the board cannot be made redundant.



• Since this board uses the loop-2 mode, LE-net loop-2 modules can be connected to the same system.

For details of performance and specifications, refer to "LE-net loop 2 Module: NP1L-LL2" in this catalog.

| Item                          | Specifications   |
|-------------------------------|--|
| Hardware                      | IBM-AT compatible*1  |
| CPU                           | Intel Pentium 300 MHz or higher                                      |
| Hard disk                     | Free space of 10 Mbytes or more                                      |
| CD-ROM unit                   | 1 unit (x 4 speed or faster), media: ISO 9660 format                 |
| Memory capacity               | 128 Mbytes or more recommended                                       |
| Keyboard                      | 101 English keyboard   |
| Mouse                         | USB mouse, bus mouse, or PS2 mouse                                   |
| Indicator                     | 800 x 600-dots resolution or higher                                  |
| OS                            | Windows2000/XP/NT 4.0  |
| Environmental durability      | Depends on environmental conditions of commercial personal computer. |
| Language for user application | Microsoft Visual Basic   |
| software development          | Microsoft Visual C++   |
| Communication protocol        | TCP/IP protocol  |
| Weight                        | Approx. 130 g  |

\*1 The board size supports a full-size PCI slot (For more information, refer to the Dimensions "PCI-bus based board" in this catalog).

## Operating environment

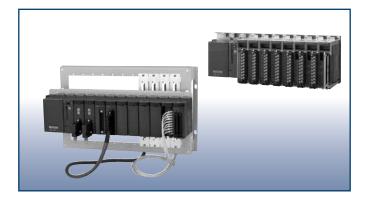
#### Renewal Tool: NP8RE

#### Outline

This renewal tool (I/O terminal conversion unit) makes the MICREX-F F250, F120-F150S, F120H/F80H, F70, F55, and FLEX-PC NJ series I/O wiring usable with MICREX-SX series units as they are.

#### Features

- Significantly reduced I/O wiring work
   Since I/O wiring is usable as it is, wiring work and checking can be omitted, and wiring work time can be significantly reduced to 1/5.
- Speedy board modifications on site The dimensions of the frame of the renewal tool are the same as those of the MICREX-F series base board. You do not have to perform any on-site additional work such as drilling.



 Easy mounting and replacement, easy checking of state indication LEDs

SX series modules are designed to be mounted on the renewal tool and can be replaced with a single motion. The state indication LEDs can also be checked.

- · Flexible layout
- SPH modules can be mounted not only on but also beside and above the renewal tool. You can arrange them any way that you wish according to the field layout.

#### Model list

MICREX-F F250/F120S/F140S/F150S/F120H/80H series compatible

| Name                                   | Model          | Specification outline   |
|--|----------------|---|
| Frame set                              | NP8REFSS-02    | NP8REFSB-02 x 1 unit, NP8REFSF-02 x 1 unit  |
| (SPH mounting board + base unit)       | NP8REFSS-04    | NP8REFSB-04 x 1 unit, NP8REFSF-04 x 1 unit  |
|  | NP8REFSS-06    | NP8REFSB-06 x 1 unit, NP8REFSF-06 x 1 unit  |
|  | NP8REFSS-08    | NP8REFSB-08 x 1 unit, NP8REFSF-08 x 1 unit  |
| SPH mounting board                     | NP8REFSF-02    | Base unit for NP8REFSF-02 (spacer, screw, washer, and nut included, four pieces each) |
|  | NP8REFSF-04    | Base unit for NP8REFSF-04 (spacer, screw, washer, and nut included, four pieces each) |
|  | NP8REFSF-06    | Base unit for NP8REFSF-06 (spacer, screw, washer, and nut included, four pieces each) |
|  | NP8REFSF-08    | Base unit for NP8REFSF-08 (spacer, screw, washer, and nut included, four pieces each) |
| Base unit                              | NP8REFSB-02    | Attachable base: For FSB084H  |
| (Unit for mounting conversion adapter) | NP8REFSB-04    | Attachable base: For FSB124H, FSB086H   |
|  | NP8REFSB-06    | Attachable base: For FSB126H, FSB088H   |
|  | NP8REFSB-08    | Attachable base: For FSB128H, FSB156S-2, FSB154S-4, FSB110H                           |
| Conversion adapter                     | NP8REFSA-204   | 20-pole terminal block, for DC signals  |
|  | NP8REFSA-202   | 20-pole terminal block, for AC signals  |
|  | NP8REFSA-384   | 38-pole terminal block, for DC signals  |
|  | NP8REFSA-382   | 38-pole terminal block, for AC signals  |
| Conversion cable                       | NP8REFSC-164X1 | 16 points, for DC input (SPH side: Terminal block)                                    |
| (Cable length: 600 mm)                 | NP8REFSC-164Y1 | 16 points, for DC output (SPH side: Terminal block)                                   |
| (NP8REFSC-324W1 only: 200 mm)          | NP8REFSC-164Y2 | 16 points, for DC output (SPH side: Terminal block)                                   |
|  | NP8REFSC-162W1 | For both input and output, for analog signals (SPH side: Terminal block)              |
|  | NP8REFSC-324X1 | For DC input (SPH side: Terminal block)   |
|  | NP8REFSC-324X2 | For DC input (SPH side: Connector)  |
|  | NP8REFSC-324Y1 | 32 points, for DC output (SPH side: Connector)  |
|  | NP8REFSC-324W2 | 32 points, for DC output (SPH side: Connector)  |
|  | NP8REFSC-164W1 | 16 points, for relay independent-output (SPH side: Terminal block)                    |
|  | NP8REFSC-324W1 | 32 points, for both input and output (SPH side: Connector) (Cable length: 200 mm)     |
|  | NP8REFSC-322X1 | 32 points, for AC input (SPH side: Terminal block)                                    |
|  | NP8REFSC-322Y1 | 32 points, for AC output (SPH side: Terminal block)                                   |

#### • MICREX-F series base compatible base units, SPH base boards, and number of conversion adapter attachments

| Base (MICREX-F)    | Base unit (frame set)        | Usable MICREX-SX SPH base board | Number of conversion adapter attachments |
|--------------------|------------------------------|---------------------------------|--|
| FSB084H            | NP8REFSB-02<br>(NP8REFSS-02) | NP1BS-06                        | Max. 5 units                             |
| FSB124H            | NP8REFSB-04                  | NP1BS-06, NP1BS-08, NP1BS-08S,  | Max. 7 units                             |
| FSB086H            | (NP8REFSS-04)                | NP1BS-08D                       |  |
| FSB126H            | NP8REFSB-06                  | NP1BS-06, NP1BS-08, NP1BS-08S,  | Max. 9 units                             |
| FSB088H            | (NP8REFSS-06)                | NP1BS-08D                       |  |
| FSB128H, FSB156S-2 | NP8REFSB-08                  | NP1BS-08, NP1BS-08S, NP1BS-11,  | Max. 11 units                            |
| FSB154S-4, FSB110H | (NP8REFSS-08)                | NP1BS-11S, NP1BS-13, NP1BS-13S  |  |

For details, refer to the User's Manual "Renewal Tool NP8REFS Series" (Manual No. FEH320).

Compatible I/O module, conversion adapter, and conversion cable

| Types         | Relevant PLC type |                       | Conversion adapter | Conversion cable            | I/O           |  |
|---------------|-------------------|-----------------------|--------------------|-----------------------------|---------------|--|
|               | MICREX-F          | MICREX-SX             |                    |                             | No. of points |  |
| nput          | FTU110B, FTU113B  | NP1X1606-W            | NP8REFSA-204       | NP8REFSC-164X1              | 16 points     |  |
| P             | FTU130B, FTU133B  | NP1X1607-W            | NP8REFSA-204       | NP8REFSC-164X1              | 16 points     |  |
|               | FTU150B           | NP1X1610              | NP8REFSA-202       | NP8REFSC-162W1              | 16 points     |  |
|               | FTU160B           | NP1X1611-RI           | NP8REFSA-202       | NP8REFSC-162W1              | 16 points     |  |
|               | FTU135C, FTU136C  | NP1X1607-W × 2 units  | NP8REFSA-384       | NP8REFSC-324X1              | 32 points     |  |
|               | FTU155C           | NP1X1610 × 2 units    | NP8REFSA-382       | NP8REFSC-322X1              | 32 points     |  |
|               | FTU165C           | NP1X1611-RI × 2 units | NP8REFSA-382       | NP8REFSC-322X1              | 16 points     |  |
|               | FTU120C, FTU123C  | NP1X3202-W            | NP8REFSA-384       | NP8REFSC-324X2              | 32 points     |  |
|               |                   | NP1X3206-W            |                    |                             | · ·           |  |
|               | FTU121C, FTU122C  | NP1X3202-W            | NP8REFSA-384       | NP8REFSC-324X2              | 32 points     |  |
|               | FTU127C           | NP1X3202-W NP1X3206-W | -                  | NP8REFSC-324W1              | 32 points     |  |
|               | FTU125A, FTU126A  | NP1X6406-W            | -                  | NP8REFSC-324W1 (Two needed) | 64 points     |  |
|               | FTU140B           | NP1X0805              | NP8REFSA-202       | NP8REFSC-082X1              | 8 points      |  |
|               | FTU143B           | NP1X0805              | NP8REFSA-202       | NP8REFSC-082X1              | 8 points      |  |
| Dutput        | FTU210B           | NP1Y16T09P6           | NP8REFSA-204       | NP8REFSC-164Y1              | 16 points     |  |
|               |                   | NP1Y16T10P2           |                    |                             |               |  |
|               | FTU211B           | NP1Y16T09P6           | NP8REFSA-204       | NP8REFSC-164Y1              | 16 points     |  |
|               |                   | NP1Y16T10P2           |                    |                             |               |  |
|               | FTU212B           | NP1Y16T09P6           | NP8REFSA-204       | NP8REFSC-164Y1              | 16 points     |  |
|               |                   | NP1Y16T10P2           | NP8REFSA-204       | NP8REFSC-164Y1              | 16 points     |  |
|               | FTU213B           | NP1Y16T09P6           | NP8REFSA-204       | NP8REFSC-164Y1              | 16 points     |  |
|               | FTU215B, FTU216B  | NP1Y16U09P6           | NP8REFSA-204       | NP8REFSC-164Y2              | 16 points     |  |
|               | FTU250B, FTU251B  | NP1Y16R-08            | NP8REFSA-202       | NP8REFSC-162W1              | 16 points     |  |
|               | FTU260B, FTU262B  | NP1Y16R-08            | NP8REFSA-202       | NP8REFSC-162W1              | 16 points     |  |
|               | FTU263B           | NP1Y08R-00 × 2 units  | NP8REFSA-382       | NP8REFSC-164W1              | 16 points     |  |
|               | FTU257B, FTU258B  | NP1Y16R-08 × 2 units  | NP8REFSA-382       | NP8REFSC-322Y1              | 32 points     |  |
|               | FTU266B, FTU267B  | NP1Y16R-08 × 2 units  | NP8REFSA-382       | NP8REFSC-322Y1              | 32 points     |  |
|               | FTU221C, FTU223B  | NP1Y32T09P1           | NP8REFSA-384       | NP8REFSC-324Y1              | 32 points     |  |
|               | FTU224B, FTU233B  |                       |                    |                             |               |  |
|               | FTU226B           | NP1Y32U09P1           | NP8REFSA-384       | NP8REFSC-324Y1              | 32 points     |  |
|               | FTU227C           | NP1Y32T09P1           | -                  | NP8REFSC-324W1              | 32 points     |  |
|               | FTU222A           | NP1Y64T09P1           | -                  | NP8REFSC-324W1 (Two needed) | 64 points     |  |
| nput/output   | FTU611C           | NP1W3206T             | NP8REFSA-384       | NP8REFSC-324W2              | 32 points     |  |
| nixed         | FTU612A           | NP1W6406T             | -                  | NP8REFSC-324W1 (Two needed) | 64 points     |  |
| Analog input  | FTU340A-FTU343A   | NP1AXH8V-MR           | NP8REFSA-202       | NP8REFSC-162W1              | 8 points      |  |
|               | FTU344A           | NP1AXH8I-MR           | NP8REFSA-202       | NP8REFSC-162W1              | 8 points      |  |
| Analog output | FTU440A-FTU443A   | NP1AYH8V-MR           | NP8REFSA-202       | NP8REFSC-162W1              | 8 points      |  |

For details, refer to the User's Manual "Renewal Tool NP8REFS Series" (Manual No. FH320).

#### MICREX-F F70 series compatible

| Name               | Model        | Specification outline  |
|--------------------|--------------|--|
| Base adapter       | NP8RE70B-02  | For NC1B02 (Mounting screws included)                            |
|                    | NP8RE70B-04  | For NC1B04, NC1B02 (Mounting screws included)                    |
|                    | NP8RE70B-06  | For NC1B06, NC1B04, NC1B02 (Mounting screws included)            |
|                    | NP8RE70B-08  | For NC1B8, NC1B06, NC1B04 (Mounting screws included)             |
|                    | NP8RE70B-10  | For NC1B10, NC1B08, NC1B06 (Mounting screws included)            |
| Conversion adapter | NP8RE70A-201 | 16 points, for DC input/output (Terminal cover included)         |
|                    | NP8RE70A-202 | 16 points, for AC input/output (Terminal cover included)         |
|                    | NP8RE70A-203 | 8 points, for relay independent-output (Terminal cover included) |
|                    | NP8RE70A-204 | 2 points/ 4 points, for analog input (Terminal cover included)   |
|                    | NP8RE70A-205 | 2 points, for analog output (Terminal cover included)            |
|                    | NP8RE70A-401 | 32 points, for DC input/output                                   |
|                    | NP8RE70A-402 | 64 points, for DC input/output                                   |

#### · MICREX-F series base compatible base units and SPH base boards

| Base (MICREX-F) Base adapter |             | Usable MICREX-SX SPH base board |
|------------------------------|-------------|---------------------------------|
| NC1B02                       | NP8RE70B-02 | 3-slot base board               |
| NC1B02, NC1B04               | NP8RE70B-04 | 6-slot base board               |
| NC1B02, NC1B04, NC1B06       | NP8RE70B-06 | 8-slot base                     |
| NC1B04, NC1B06, NC1B08       | NP8RE70B-08 | 8/11-slot base                  |
| NC1B06, NC1B08, NC1B10       | NP8RE70B-10 | 11/13-slot base                 |

#### Compatible I/O module and conversion adapter

| Types  | Relevant I/O module type |                         | Conversion adapter | No. of I/O |
|--------|--------------------------|-------------------------|--------------------|------------|
|        | MICREX-F                 | MICREX-SX               |                    | points     |
| Input  | NC1X1604 (at 24 V DC)    | NP1X1606-W *1           | NP8RE70A-201       | 16 points  |
|        | NC1X1604-W (at 24 V DC)  | NP1X1606-W *1           | NP8RE70A-201       | 16 points  |
|        | NC1X1610                 | NP1X1610-RI             | NP8RE70A-202       | 16 points  |
|        | NC1X1611                 | NP1X1611-RI             | NP8RE70A-202       | 16 points  |
|        | NC1X3202-W               | NP1X3202-W              | NP8RE70A-401       | 32 points  |
|        | NC1X3204                 | NP1X3206-W (at 24 V DC) | NP8RE70A-401       | 32 points  |
|        | NC1X3204-3               | NP1X3206-W (at 24 V DC) | NP8RE70A-401       | 32 points  |
|        | NC1X3206                 | NP1X3206-W              | NP8RE70A-401       | 32 points  |
|        | NC1X3206-S               | NP1X3206-W              | NP8RE70A-401       | 32 points  |
|        | NC1X6404                 | NP1X6406-W              | NP8RE70A-402       | 64 points  |
|        | NC1X6406                 | NP1X6406-W              | NP8RE70A-402       | 64 points  |
|        | NC1X6406-S               | NP1X6406-W              | NP8RE70A-402       | 64 points  |
|        | NC1X6406-W               | NP1X6406-W              | NP8RE70A-402       | 64 points  |
| Output | NC1Y16R-08               | NP1Y16R-08              | NP8RE70A-201       | 16 points  |

| Types                 | Relevant I/O module type |             |    | Conversion adapter | No. of I/O                    |
|-----------------------|--------------------------|-------------|----|--------------------|-------------------------------|
|                       | MICREX-F                 | MICREX-SX   |    |                    | points                        |
|                       | NC1Y16T05P5-1            | NP1Y16T09P6 |    | NP8RE70A-201       | 16 points                     |
|                       | NC1Y16U05P5-1            | NP1Y16U09P6 |    | NP8RE70A-201       | 16 points                     |
|                       | NC1Y16S                  | NP1Y16R-08  | *2 | NP8RE70A-202       | 16 points                     |
|                       | NC1Y08R-00               | NP1Y08R-00  |    | NP8RE70A-203       | 8-point relay-<br>independent |
|                       | NC1Y32T05P1              | NP1Y32T09P1 | *3 | NP8RE70A-401       | 32 points                     |
|                       | NC1Y32U05P1              | NP1Y32U09P1 | *3 | NP8RE70A-401       | 32 points                     |
|                       | NC1Y64T05P1-1            | NP1Y64T09P1 | *3 | NP8RE70A-402       | 64 points                     |
| Input/output<br>mixed | NC1W6406T                | NP1W6406T   | *3 | NP8RE70A-402       | 64 points                     |
| Analog input          | NC1AX04-MR               | NP1AXH4-MR  |    | NP8RE70A-204       | 4 points                      |
| Analog output         | NC1AY02-MR               | NP1AYH2-MR  |    | NP8RE70A-205       | 2 points                      |

\*1 This renewal tool is unusable when the signal level is at 12 V DC.
 \*2 The output element is changed from the SSR to the relay.

\*3 It does not support 5 V DC.
 For details, refer to the User's Manual "Renewal Tool for F55/F70 Series" (Manual No. FH323).

#### MICREX-F F55 series compatible

| Name               | Model        | Specification outline   |  |
|--------------------|--------------|---|--|
| Base adapter       | NP8RE55B-04  | For NV1P-042, NV1P-044, NV1E-042, NV1E-044 (Mounting screws included) |  |
|                    | NP8RE55B-06  | For NV1P-062, NV1P-064, NV1E-062, NV1E-064 (Mounting screws included) |  |
|                    | NP8RE55B-08  | For NV1P-082, NV1P-084, NV1E-082, NV1E-084 (Mounting screws included) |  |
|                    | NP8RE55B-08L | For NV1P-082, NV1P-084, NV1E-082, NV1E-084 (Mounting screws included) |  |
| Conversion adapter | NP8RE55A-181 | 16 points, for DC input and relay output (8 points x 2 common)        |  |
|                    | NP8RE55A-182 | 16 points, for DC output  |  |
|                    | NP8RE55A-183 | 8 points, for relay independent-output                                |  |
|                    | NP8RE55A-184 | 8 points, for AC input  |  |
|                    | NP8RE55A-185 | 8 points, for SSR output  |  |
|                    | NP8RE55A-186 | 4 points, for analog input  |  |
|                    | NP8RE55A-187 | 2 points, for analog voltage output                                   |  |
|                    | NP8RE55A-188 | 2 points, for analog current output                                   |  |
| NP8RE70A-401       |              | 32 points, for DC input/output  |  |
|                    | NP8RE55A-402 | 32 points, for DC input/output  |  |

#### MICREX-F series base compatible base units and SPH base boards

| Base (MICREX-F)                        | Base adapter | Usable MICREX-SX SPH base board |
|--|--------------|---------------------------------|
| NV1P-042, NV1P-044, NV1E-042, NV1E-044 | NP8RE55B-04  | NP1BS-06                        |
| NV1P-062, NV1P-064, NV1E-062, NV1E-064 | NP8RE55B-06  | NP1BS-08, NP1BS-08S             |
| NV1P-082, NV1P-084, NV1E-082, NV1E-084 | NP8RE55B-08  | NP1BS-11, NP1BS-11S             |
|  | NP8RE55B-08L | NP1BS-13, NP1BS-13S             |

#### Compatible I/O module and conversion adapter

| Types         | Relevant I/O module type |             | Conversion adapter        | No. of I/O points             |
|---------------|--------------------------|-------------|---------------------------|-------------------------------|
|               | MICREX-F                 | MICREX-SX   |                           |                               |
| Input         | NV1X1604-W               | NP1X1606-W  | NP8RE55A-181              | 16 points                     |
|               | NV1X1604                 | NP1X1606-W  | NP8RE55A-181              | 16 points                     |
|               | NV1X1604-3               | NP1X1606-W  | NP8RE55A-181              | 16 points                     |
|               | NV1X0811                 | NP1X0811    | NP8RE55A-184              | 8 points                      |
|               | NV1X0810                 | NP1X0810    | NP8RE55A-184              | 8 points                      |
|               | NV1X3204                 | NP1X3206-W  | NP8RE70A-401              | 64 points where 32 points x 2 |
|               | NV1X3204 ×2              | NP1X6406-W  |                           |                               |
|               | NV1X3206                 | NP1X3206-W  |                           |                               |
|               | NV1X3206 ×2              | NP1X6406-W  |                           |                               |
|               | NV1X3204-W               | NP1X3206-W  |                           |                               |
|               | NV1X3204-W ×2            | NP1X6406-W  |                           |                               |
| Output        | NV1Y16R-08               | NP1Y16R-08  | NP8RE55A-181              | 16 points                     |
|               | NV1Y16T05P5              | NP1Y16T09P6 | NP8RE55A-182              | 16 points                     |
|               | NV1Y16U05P5              | NP1Y16U09P6 | NP8RE55A-182              | 16 points                     |
|               | NV1Y08R-00               | NP1Y08R-00  | NP8RE55A-183              | 8 points                      |
|               | NV1Y08S                  | NP1Y08S     | NP8RE55A-185              | 8 points                      |
|               | NV1Y32T05P1              | NP1Y32T09P1 | Case where NP8RE70A-401 > | 2 Case where 32 points x 2    |
|               | NV1Y32T05P1 ×2           | NP1Y64T09P1 | NP8RE70A-402              | 64 points                     |
| Analog input  | NV1AX04-MR               | NP1AX04-MR  | NP8RE55A-186              | 4 points                      |
| Analog output | NV1AY02V-MR              | NP1AY02-MR  | NP8RE55A-187              | 2 points                      |
|               | NV1AY02I-MR              | NP1AY02-MR  | NP8RE55A-188              | 2 points                      |

For details, refer to the User's Manual "Renewal Tool for F55/F70 Series" (Manual No. FH323).

· FLEX-PC NJ series compatible

| Name               | Model        | Specification outline  |
|--------------------|--------------|--|
| Base adapter       | NP8RENJB-03  | For NJ-BP3, NJ-BE3 (Mounting screws included)  |
|                    | NP8RENJB-05  | For NJ-BP5, NJ-BT5, NJ-BE5 (Mounting screws included)  |
|                    | NP8RENJB-08  | For NJ-BP8, NJ-BT8, NJ-BE8 (Mounting screws included)  |
|                    | NP8RENJB-08L | For NJ-BP8, NJ-BT8, NJ-BE8 (Mounting screws included)  |
| Conversion adapter | NP8RENJA-181 | 16 points, for DC input and relay output (Mounting brackets, one conversion PC board, terminal labels, junction connectors included) |
|                    | NP8RENJA-182 | 16 points, for DC output (Mounting brackets, one conversion PC board, terminal labels, junction connectors included)                 |
|                    | NP8RENJA-183 | 8 points, for relay output (Mounting brackets, one conversion PC board, terminal labels, junction<br>connectors included)            |
|                    | NP8RENJA-184 | For multi-range analog input (Mounting brackets, one conversion PC board, terminal labels, junction connectors included)             |
|                    | NP8RENJA-185 | For multi-range analog output (Mounting brackets, one conversion PC board, terminal labels, junction connectors included)            |
|                    | NP8RENJA-241 | 32 points, for DC input/output (One conversion PC board included)  |
|                    | NP8RENJA-242 | 32 points, for DC input/output of two units (Two conversion PC boards included)  |

#### NJ series base compatible base units and SPH base boards

| Base (FLEX-PC) | Base adapter | Usable MICREX-SX SPH base board |
|----------------|--------------|---------------------------------|
| NJ-BP3         | NP8RENJB-03  | NP1BS-06                        |
| NJ-BE3         |              |                                 |
| NJ-BP5         | NP8RENJB-05  | NP1BS-08, NP1BS-08S             |
| NJ-BT5         |              |                                 |
| NJ-BE5         |              |                                 |
| NJ-BP8         | NP8RENJB-08  | NP1BS-11, NP1BS-11S             |
| NJ-BT8         | NP8RENJB-08L | NP1BS-13, NP1BS-13S             |
| NJ-BE8         |              |                                 |

#### Compatible I/O module and conversion adapter

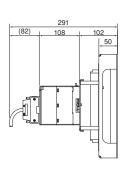
| Types         | Relevant I/O module type |             | Conversion adapter | No. of I/O    |
|---------------|--------------------------|-------------|--------------------|---------------|
|               | FLEX-PC NJ               | MICREX-SX   |                    | points        |
| nput          | NJ-X16-1                 | NP1X1606-W  | NP8RENJA-181       | 16 points     |
|               | NJ-X16-1S                | NP1X1606-W  | NP8RENJA-181       | 16 points     |
|               | NJ-X16-4                 | NP1X1610    | NP8RENJA-181       | 16 points     |
|               |                          | NP1X1610-RI | NP8RENJA-181       | 16 points     |
|               | NJ-X16-5                 | NP1X1611    | NP8RENJA-181       | 16 points     |
|               |                          | NP1X1611-RI | NP8RENJA-181       | 16 points     |
|               | NJ-X32-1                 | NP1X3206-W  | NP8RENJA-241       | 32 points     |
|               | NJ-X32-1 ×2              | NP1X6406-W  | x 2: NP8RENJA-242  | 32 points x 2 |
|               | NJ-X32-1S                | NP1X3206-W  | NP8RENJA-241       | 32 points     |
|               | NJ-X32-1S ×2             | NP1X6406-W  | x 2: NP8RENJA-242  | 32 points x 2 |
| Output        | NJ-Y16-R16               | NP1Y16R-08  | NP8RENJA-181       | 16 points     |
|               | NJ-Y16-SF1               | NP1Y16R-08  | NP8RENJA-181       | 16 points     |
|               | NJ-Y16-TF2               | NP1Y16T09P6 | NP8RENJA-182       | 16 points     |
|               | NJ-Y16-TF2S              | NP1Y16U09P6 | NP8RENJA-182       | 16 points     |
|               | NJ-Y8-R                  | NP1Y08R-00  | NP8RENJA-183       | 8 points      |
|               | NJ-Y32-T1                | NP1Y32T09P1 | NP8RENJA-241       | 32 points     |
|               | NJ-Y32-T1 ×2             | NP1Y64T09P1 | x 2: NP8RENJA-242  | 32 points x 2 |
|               | NJ-Y32-T1S               | NP1Y32U09P1 | NP8RENJA-241       | 32 points     |
|               | NJ-Y32-T1S ×2            | NP1Y64U09P1 | x 2: NP8RENJA-242  | 32 points x 2 |
| nput/output   | NJ-XY32-1                | NP1W6406T   | NP8RENJA-241       | 32 points     |
| nixed         | NJ-XY32-1 ×2             |             | x 2: NP8RENJA-242  | 32 points x 2 |
|               | NJ-XY32-1SS              | NP1W6406U   | NP8RENJA-241       | 32 points     |
|               | NJ-XY32-1SS ×2           |             | x 2: NP8RENJA-242  | 32 points x 2 |
| nalog input   | NJ-AX4-MR                | NP1AX04-MR  | NP8RENJA-184       | 4 points      |
| Analog output | NJ-AY2V-MR               | NP1AYH4V-MR | NP8RENJA-185       | 2 points      |
|               | NJ-AY4V-MR               | NP1AYH4V-MR | NP8RENJA-185       | 4 points      |

## Dimensions

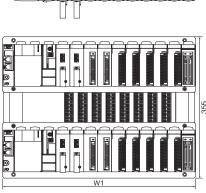
MICREX-F F250/F120S/F140S/F150S/F120H/80H series compatible

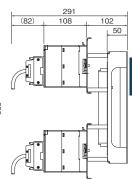
Mounting example with the frame set (base unit + SPH mounting board)

- · Base unit (mounting 1 SX base unit)



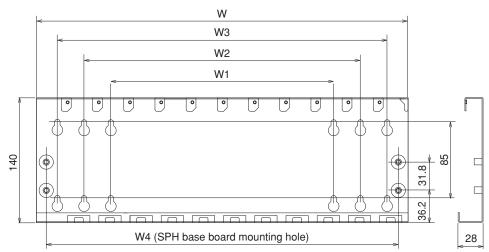
· Base unit (mounting 2 SX base units)





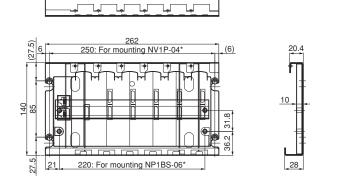
|            |    | Frame set                                |             |             |             |     |   |
|------------|----|--|-------------|-------------|-------------|-----|---|
| Model      |    | NP8REFSS-08                              | NP8REFSS-06 | NP8REFSS-04 | NP8REFSS-02 |     |   |
| Dimensions | W1 | Mounting dimensions of base unit         | 480         | 407         | 334         | 261 |   |
|            | W2 | Mounting dimensions of base unit         | 465         | 392         | 319         | 246 | - |
|            | W3 | Outside dimensions of SPH mounting board | 485         | 377         | 310         | 240 | - |

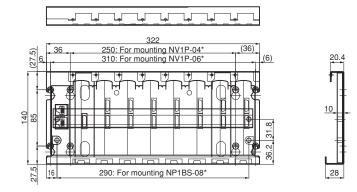
• MICREX-F F70 series compatible



| Base adapter type | Dimension (mm)                   |                              |        |  |         |  |  |  |
|-------------------|----------------------------------|------------------------------|--------|--|---------|--|--|--|
|                   | Width of the entire base adapter | F70 base mounting holes (Num |        | SX base mounting holes (Number of slots) |         |  |  |  |
|                   | W W1 W2 W3                       |                              |        |  |         |  |  |  |
| NP8RE70B-02       | 207                              | 189(2)                       | -      | -  | 115(3)  |  |  |  |
| NP8RE70B-04       | 277                              | 189(2)                       | 259(4) | -  | 220(6)  |  |  |  |
| NP8RE70B-06       | 347                              | 189(2)                       | 259(4) | 329(6)                                   | 290(8)  |  |  |  |
| NP8RE70B-08       | 417                              | 259(4)                       | 329(6) | 408(8)                                   | 395(11) |  |  |  |
| NP8RE70B-10       | 487                              | 329(6)                       | 408(8) | 469(10)                                  | 465(13) |  |  |  |

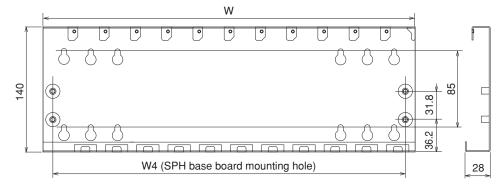
• MICREX-F F55 series compatible





| Base adapter type | Dimension (mm) |     |    |  |
|-------------------|----------------|-----|----|--|
|                   | W              | Н   | D  |  |
| NP8RE55B-04       | 262            | 140 | 28 |  |
| NP8RE55B-06       | 322            | 140 | 28 |  |
| NP8RE55B-08       | 417            | 140 | 28 |  |
| NP8RE55B-08L      | 487            | 140 | 28 |  |

#### · FLEX-PC NJ series compatible



| Base adapter type | Dimensions (mm) |     |      |  |
|-------------------|-----------------|-----|------|--|
|                   | W               | Н   | D    |  |
| NP8RENJB-03       | 250             | 140 | 28.6 |  |
| NP8RENJB-05       | 326             | 140 | 28.6 |  |
| NP8RENJB-08       | 439             | 140 | 28.6 |  |
| NP8RENJB-08L      | 485             | 140 | 28.6 |  |

#### MICREX-F Size I/O Module (Renewal Tool): NP8 ---

#### Outline

Features

This module is an I/O module with a size equivalent to MICREX-F series FTU module. This renewal tool makes the MICREX-F F120-150S series I/O wirings usable with MICREX-SX series units as they are.

The dimensions of the base board mounting hole for the

The same terminal block as one of the MICREX series FTU module is used, so the existing terminal block of the

performance is inherited from the MICREX-F series.

base board. Also, the depth length is minimized.

control panel are the same as those of the MICREX-F series

MICREX series can be connected as it is. Also, the electrical

No control panel modification is required

No wiring change is required



- Easy module replacement and signal check The module placed on the front allows you to check signals regularly and to quickly replace the module in an emergency.
- Can be used as an extension unit in MICREX-F series system

This module has a function allowing to logically change the bit order of terminal block signal wiring. MICREX-F processor modules can be replaced in T-link extension unit as they are.

## Specifications

Input specifications

| Item Specifications                                 |   |  |  |  |  |
|---|---|--|--|--|--|
| Model NP8SX-113                                     |   | NP8SX-113  | NP8X-120   | NP8X-123   |  |
| No. of input points (Common 1 configuration)        |   | 16 points (8 points/common 2 circuits)   | 32 points (16 points/common 2 circuits)  |  |  |
| Rated voltage                                       |   | 12 to 24 V DC/AC   | 12 to 24 V DC/AC   | 12 to 24 V DC  |  |
| Max. allowed volta                                  | age                                       | 30 V DC  | 26.4 V DC/AC   | 26.4 V DC  |  |
| Input format  |   | No polarity No polarity  |  |  |  |
| Rated current                                       |   | 4mA (at 12 V DC), 10mA (at 24 V DC)  | nA (at 12 V DC), 10mA (at 24 V DC) 4mA (at 12 V DC), 10mA (at 24 V DC)   |  |  |
| Input impedance                                     |   | 2.2kΩ  | 2.2κΩ  |  |  |
|   | OFF to ON                                 | 9.6 to 30 V 9.6 to 26.4 V  |  |  |  |
| operation range                                     | ON to OFF                                 | 0 to 5.5 V   |  |  |  |
| Input delay time                                    | OFF to ON                                 | 3 ±1.5 ms (hard filter time) + (soft filter time)  | 10 ms (hard filter time) + (soft filter time)  | 3 ±1.5 ms (hard filter time) + (soft filter time)  |  |
|   | ON to OFF                                 | The soft filter time can be changed in the parameter settings.   | The soft filter time can be changed in the parameter settings.   | The soft filter time can be changed in the parameter settings.   |  |
|   |   | (OFF to ON) - (ON to OFF): None (default), 0.1-0.1<br>ms, 1-1 ms, 3-3 ms, 3-10 ms, 10-10 ms, 30-30 ms,<br>100-100 ms | (OFF to ON) - (ON to OFF): None (default), 0.1-0.1<br>ms, 1-1 ms, 3-3 ms, 3-10 ms, 10-10 ms, 30-30 ms,<br>100-100 ms | (OFF to ON) - (ON to OFF): None (default), 0.1-0.1<br>ms, 1-1 ms, 3-3 ms, 3-10 ms, 10-10 ms, 30-30 ms,<br>100-100 ms |  |
| Insulation method                                   | Insulation method Photocoupler insulation |  |  |  |  |
| Internal current consumption 2                      |   | 24 V DC, 40mA or less (all points ON)  | 24 V DC, 70mA or less (all points ON)  |  |  |
| Depth   |   | Low-profile model  | Standard model   |  |  |
| Weight Approx. 220 g (not including terminal block) |   | Approx. 220 g (not including terminal block)   | Approx. 500 g (not including terminal block)   |  |  |

| Item   |                          | Specifications                               |  |                         |  |  |
|--|--------------------------|--|--|-------------------------|--|--|
| Model NP8SX-143 N  |                          | NP8SX-143                                    | NP8X-155                                     | NP8X-165                |  |  |
| No. of input points (Common configuration)                         |                          | 8 points (8 points/common 1 circuit)         | 32 points (16 points/common 2 circuit)       |                         |  |  |
| Rated voltage  |                          | 110 V DC                                     | 100/120 V AC                                 | 200/240 V AC            |  |  |
| Max. allowed volta   | age                      | 140 V DC or less                             | 132 V AC                                     | 264 V AC                |  |  |
| Input format   |                          | No Polarity                                  | AC input                                     |                         |  |  |
| Rated current  | Rated current 5 mA/point |  | 10mA(at 100 V AC, 50Hz)                      | 10mA(at 200 V AC, 50Hz) |  |  |
| Input impedance  |                          | 20kΩ   | 10kΩ (50Hz),9kΩ (60Hz)                       | 22kΩ (50Hz),18kΩ (60Hz) |  |  |
|  | OFF to ON                | 80-140V                                      | 80 to 132 V                                  | 16 to 264 V             |  |  |
| operation range  | ON to OFF                | 0-22V  | 0 to 35 V                                    | 0 to 70 V               |  |  |
| Input delay time   | OFF to ON                | 3 ±1.5 ms (hard filter time)                 | 10 ms or less                                |                         |  |  |
| ON to OFF  |                          |  |  |                         |  |  |
| Insulation method Photocoupler insulation                          |                          | Photocoupler insulation                      | ·  |                         |  |  |
| Internal current consumption 24 V DC, 70mA or less (all points ON) |                          | 24 V DC, 70mA or less (all points ON)        | 24 V DC, 50mA or less (all points ON)        |                         |  |  |
| Depth Low-profile model  |                          | Low-profile model                            | Standard model                               |                         |  |  |
| Weight Approx. 530 g (not including terminal block)                |                          | Approx. 530 g (not including terminal block) | Approx. 550 g (not including terminal block) |                         |  |  |

#### Specifications

#### · Output specifications

| Item<br>Model  | Specifications NP8Y-266   | NP8Y-250                            |                                       | NP8Y-263                    |  | NP8S)        | -263  |
|--|---|-------------------------------------|---------------------------------------|-----------------------------|--|--------------|---|
| No. of output points (Common configuration)            | 32 points (8 points/common 4 circuits)  | 16 points (8 points/con             | mon 2 circuits)                       |                             | oints are independent)                   | 117031       | -200  |
| Output format  | Relay output  | Triac output                        |                                       | Relav output                |  |              |   |
| Rated voltage  | 240 V AC, 24 V DC   | 100 to 240 V AC                     |                                       | 240 V AC, 24 V DC           |  |              |   |
| Voltage tolerance                                      | 264 V AC or less, 30 V DC or less   | 85 to 264 V AC                      |                                       |                             | ess, 30 V DC or less                     |              |   |
| Max. load current                                      | 264 V AC: 1A/point, 5A/common   | 2A/point, 5A/common                 |                                       | 264 V AC: 2A/point          |  |              |   |
|  | 30 V DC: 1A/point, 5A/common  |                                     |                                       | 30 V DC: 2A/p               |  |              |   |
| Output delay time OFF to ON                            |   | 1 ms or less                        |                                       | 10 ms or less               | · /                                      |              |   |
| ON to OFF  |   | 10 ms or less                       |                                       | 10 ms or less               |  |              |   |
| Leakage current when OFF                               | 0.1mA or less (at 200 V AC/60 Hz)   | 1mA or less (at 200 V /             |                                       |                             | (at 200 V AC/60 Hz)                      |              |   |
| Surge suppressor circuit                               | Varistor  | CR absorber + varistor              | •                                     | Varistor                    |  |              |   |
| Maximum opening/closing frequency<br>Insulation method |   | Dhotocoupler inculation             |                                       | 3600 times/ho               | ur<br>n, photocoupler insulat            | ian          |   |
| Internal current consumption                           | Relay insulation, photocoupler insulation<br>24 V DC, 120mA or less (all points ON) | Photocoupler insulation             | 1                                     |                             | A or less (all points ON                 |              |   |
| No. of occupied words                                  | SX bus direct connection: 2 words   | SX bus direct connecti              | on: 2 words                           |                             | connection: 2 words                      | )            |   |
| No. of occupied words                                  | Remote I/O link: 2 words  | Remote I/O link: 1 wor              |                                       | Remote I/O lin              |  |              |   |
| Depth  | Standard model  |                                     |                                       |                             |  | Low-pr       | ofile model   |
| Weight   | Approx. 630 g (not including terminal block)  | Approx. 620 g (not includi          | ng terminal block)                    | Approx. 500 g (r            | not including terminal block             | () Approx.   | 340 g (not including terminal block)  |
| Item   | Specifications  |                                     |                                       |                             |  |              |   |
| Model  | NP8Y-221 NP8Y-2   | 223                                 | NP8Y-226                              |                             | NP8Y-257                                 |              | NP8SY-260   |
| No. of output points (Common configuration)            |   | 220                                 | NI 01-220                             |                             | 32 points (8 points/commor               | 4 circuits)  | 16 points (8 points/common 2 circuits)  |
| Output format  | Transistor output sink type   |                                     |                                       |                             | Triac output                             |              | Relay output  |
| Rated voltage  |   | -48 V DC                            | (12) 24-60 V DC                       | >                           | 100 to 240 V AC                          |              | 240 V AC, 24 V DC   |
| Voltage tolerance                                      | 4.75 to 26.4 V DC 19 to 60  |                                     | 19 to 66 V DC                         |                             | 85 to 264 V AC                           |              | 264 V AC or less, 30 V DC or less   |
| Max. load current                                      | 5 V DC: 0.03A/point, 0.48A/common 12 V DC:  | 0.15A/point, 2.4A/common            | 12 V DC: 0.15A/point                  |                             | 0.6A/point, 2.4A/comm                    | non          | 264 V AC: 2A/point, 8A/common   |
|  | 12 to 24 V DC: 0.1A//point, 1.6A/common 24 to 48 V                                  | / DC: 0.2A//point, 3.2A/common      | 24 to 60 V DC: 0.2A//                 | point, 3.2A/common          |  |              | 30 V DC: 2A/point, 8A/common  |
| Output delay time OFF to ON                            |   |                                     |                                       |                             | 1 ms or less                             |              | 10 ms or less (30 V DC)   |
| ON to OFF  |   |                                     |                                       |                             | 10 ms or less                            |              | 10 ms or less (30 V DC)   |
| Leakage current when OFF                               | 0.1mA or less   |                                     | -                                     |                             | 1mA or less (at 200 V A                  |              | 0.1mA or less (at 200 V AC/60 Hz)   |
| Surge suppressor circuit                               | Diode   |                                     |                                       |                             | CR absorber and varis<br>1800 times/hour | stor         | Varistor  |
| Maximum opening/closing frequency<br>Insulation method | Photocoupler insulation   |                                     |                                       |                             | 1800 times/nour                          |              | 3600 times/hour<br>Relay insulation, photocoupler insulation                      |
| Internal current consumption                           | 24 V DC, 70mA or less (all points ON)   |                                     |                                       |                             | 24 V DC, 120mA or less (all              | pointe (NI)  | 24 V DC, 50mA or less (all points ON)   |
| No. of occupied words                                  | SX bus direct connection: 2 words   |                                     |                                       |                             | SX bus direct connection                 | . /          | SX bus direct connection: 2 words   |
| No. of occupied words                                  | Remote I/O link: 2 words  |                                     |                                       |                             | Remote I/O link: 2 words                 |              | Remote I/O link: 1 words  |
| Depth  | Standard model  |                                     |                                       |                             |  |              | Low-profile model   |
| Weight   | Approx. 430g (not including terminal block) Approx. 53                              | 30 g (not including terminal block) | Approx. 320 g (not incl               | uding terminal block)       | Approx. 530 g (not including terr        | minal block) | Approx. 370g (not including terminal block)                                       |
| <ul> <li>Analog input specified</li> </ul>             | fications   |                                     |                                       |                             |  |              |   |
| <u> </u>   |   |                                     |                                       |                             |  |              |   |
|  | Specifications NP8AX-340MR  |                                     |                                       |                             |  |              | NP8AX-344   |
|  | 8 channels  |                                     |                                       |                             |  |              | NF0AA-344   |
|  | 0 to 5 V 0 to 10 V  | V                                   | -5 V to +5 V                          |                             | -10 V to +10 V                           |              | 0 to 20mA   |
|  | 0 to 4000   | •                                   | -2000 to 2000                         |                             | 10 1 10 1 10 1                           |              | 0 to 4000   |
|  | BCD 4 digits with ± sign/BIN switching  |                                     | 2000 10 2000                          |                             |  |              |   |
| Resolution   | 12 bits   |                                     |                                       |                             |  |              |   |
|  | 8 words (input)   |                                     |                                       |                             |  |              |   |
|  | ±0.2% (0 to 55 °C)  |                                     |                                       |                             |  |              | ±0.3% (0 to 55 °C)  |
| Response time  | 1.2 ms or less/8 points + tact cycle (ms)   |                                     |                                       |                             |  |              |   |
| Internal current consumption                           | 24 V DC, 40mA   |                                     |                                       |                             |  |              |   |
| External terminal                                      | Detachable terminal block (M3.5, 20 pole  | es)                                 |                                       |                             |  |              |   |
| Depth  | Standard model  |                                     |                                       |                             |  |              |   |
| Weight   | Approx. 500 g or less (not including term   | inal block)                         |                                       |                             |  |              |   |
| <ul> <li>Analog output spec</li> </ul>                 | cifications   |                                     |                                       |                             |  |              |   |
|  | Specifications  |                                     |                                       |                             |  |              |   |
|  | NP8AY-440MR   |                                     |                                       |                             |  |              |   |
|  | 8 channels  |                                     |                                       |                             |  |              |   |
|  |   | 0 to 10 1/                          | 1                                     | E V to / E V                |  | 10.17        |   |
|  |   | 0 to 10 V                           |                                       | -5 V to +5 V                |  | -10 V to     | +10 V   |
| 0  | 0 to 4000   |                                     | · · · · · · · · · · · · · · · · · · · | -2000 to 2000               |  |              |   |
| <u> </u>   | BCD 4 digits with ± sign/BIN switching  |                                     |                                       |                             |  |              |   |
| Resolution   | 12 bits   |                                     |                                       |                             |  |              |   |
|  | 8 words (input)   |                                     |                                       |                             |  |              |   |
| Overall accuracy                                       | ±0.2% (0 to 55 °C)  |                                     |                                       |                             |  |              |   |
| Response time  | 1.2 ms or less/8 points + tact cycle (ms)   |                                     |                                       |                             |  |              |   |
| Internal current consumption                           |   |                                     |                                       |                             |  |              |   |
| External terminal                                      | Detachable terminal block (M3.5, 20 poles)  |                                     |                                       |                             |  |              |   |
| Depth  | Standard model  |                                     |                                       |                             |  |              |   |
|  | Approx. 500 g or less (not including term   | inal block)                         |                                       |                             |  |              |   |
|  |   |                                     |                                       |                             |  |              |   |
| Weight   |   |                                     |                                       |                             |  |              |   |
|  |   |                                     |                                       |                             |  |              |   |
| Weight   |   | Weight [g]                          | B                                     | ase board for S             | KFi                                      | ixing scre   | w mounting space (W x H) [mm]   |
| Weight Mounting dimens                                 | ions of base board<br>External dimension (W x H x D) [mm]                           | Weight [g]<br>1,500                 |                                       | ase board for S)<br>3 slots |  |              | w mounting space (W x H) [mm]<br>Same as FSB128/FSB110H                           |
| Weight Mounting dimens Type                            | ions of base board<br>External dimension (W x H x D) [mm]<br>508 x 260 x 36         |                                     | 10                                    |                             | 46                                       | 65 x 150     | w mounting space (W x H) [mm]<br>Same as FSB128/FSB110H<br>Same as FSB126/FSB088H |

 
 NP8B-06
 263 x 260 x 36
 800
 6 slots
 246 x 150
 Same as FSB084

 Note: The mounting base board is a unit used to fasten a MICREX-F sized I/O module to a MICREX-SX Series base board (NP1B□-□□). When using a MICREX-F sized I/O module, please install a MICREX-SX Series base board in addition to the mounting base board.

8 slots

319 x 150 Same as FSB124/FSB086H

1,000

NP8B-08

336 x 260 x 36

#### Power Supply Unit for FLT-ASFKA

NP8S-LC

#### Outline

This unit serves to provide power for the conversion adapter (FLT-ASFKA), which is used to connect a PC loader through the T-link.

A board-mounting type (model: NP8S-LC1) and a tabletopmounting type (model: NP8S-LC2) are available.

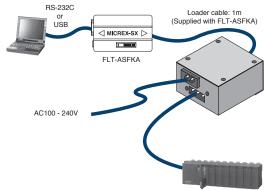
### Specifications

#### · General specifications

| Item   |                      | Specifications   |                             |  |
|--|----------------------|--|-----------------------------|--|
| Туре   |                      | NP8S-LC1   | NP8S-LC2                    |  |
| Physical Operating<br>environmental ambient<br>conditions Storage<br>temperature |                      | 0 to +50°C   |                             |  |
|  |                      | -20 to 70°C  |                             |  |
|  | Relative humidity    | 30 to 90%RH (without conder  | nsation)                    |  |
|  | Contamination level  | Contamination level 2  |                             |  |
|  | Corrosion resistance | No corrosive gas is present, r   | no organic solvent adhesion |  |
|  | Operating altitude   | Altitude of 2000 m or less, air pressure of 70 kPa<br>(equivalent to an altitude of 3000 m) during trans         |                             |  |
| Insulation me  | ethod                | Photocoupler, transformer  |                             |  |
| Voltage resis  | tance                | 2000 V AC, one minute (between the AC input section (batch) and the output connector (batch))                    |                             |  |
| Insulation resistance  |                      | 500 V DC, 10 M $\Omega$ or more (Ordinary temperature, ordinary humidity)  |                             |  |
| Installation   | Structure            | Board-mounting, tabletop-mo  | unting                      |  |
| conditions   | Cooling method       | Natural cooling  |                             |  |
| Dimension  |                      | Board-mounting: 70 mm (W) x 44.4 mm (H) x 77 mm (D)<br>Tabletop-mounting: 90 mm (W) x 46.6 mm (H) x 77 mm<br>(D) |                             |  |
| Weight   |                      | Approx. 288 g Approx. 280 g  |                             |  |

#### System configuration example

T-link slave system



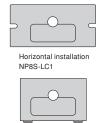
# NP8S-LC1 NP8S-LC2

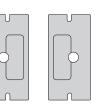
#### · Power supply specifications

|  | Item         |                         | Specifications                       |  |
|--|--------------|-------------------------|--------------------------------------|--|
|  | Power supply | Rated input voltage     | 100 to 240V AC (Note)                |  |
| specifications Allowable voltage range |              | Allowable voltage range | 85 to 264V AC                        |  |
|  |              | Power consumption       | At 100V AC: 0.11A, At 200V AC: 0.06A |  |

Notes : The AC cable supplied with the product is for 100V AC. When using 200V AC power, separately prepare a cable for 200V AC.

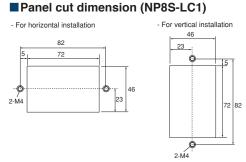
#### Installation method





Vertical installation NP8S-LC1

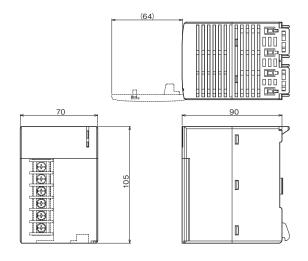
Only for tabletop installation (horizontal installation) NP8S-LC2



## Programmable Controllers MICREX-SX series Dimensions

#### Dimensions

- (1) Power supply module
- 1) NP1S-22, NP1S-42



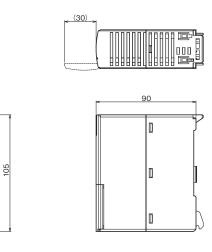
2) NP1S-91, NP1S-81

35

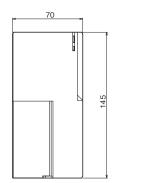
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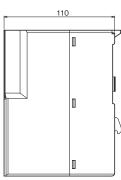
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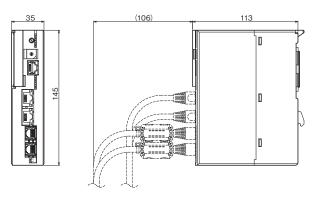


## 3) NP1S-22S, NP1S-62S

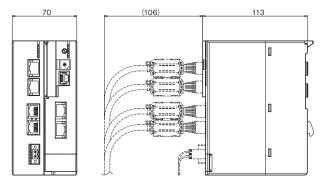




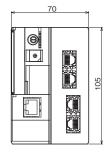
- (2) CPU module
- 1) SPH5000M/SPH5000EC NP1PA1-096E, NP1PA1-128E, NP1PA1-256E, NP1PA1-512E, NP1PA1C-096E, NP1PA1C-128E, NP1PA1C-256E, NP1PA1C-512E

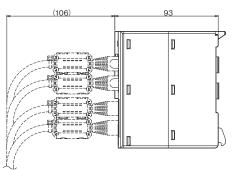


2) SPH5000H NP1PU1-512H

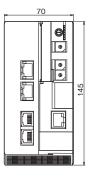


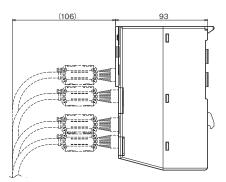
3) SPH3000MM NP1PU2-048E, NP1PU2-256E





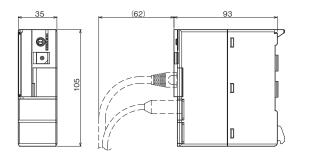
4) SPH3000MG
 NP1PU1-256NE



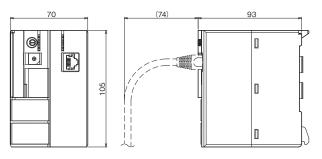


## Programmable Controllers MICREX-SX series Dimensions

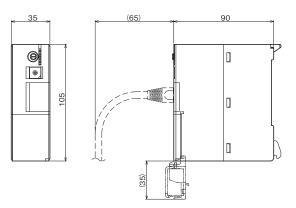
5) SPH300/SPH2000/SPH3000/SPH3000D NP1PU-048EZM, NP1PU-096EZM, NP1PU-128EZM, NP1PU-256EZM, NP1PU-048E, NP1PU-128E, NP1PU-256E, NP1PM-48R, NP1PM-48E, NP1PM-256E, NP1PM-256H, NP1PS-32, NP1PS-32R, NP1PS-74R, NP1PS-117R, NP1PS-245R



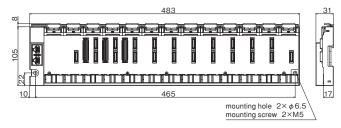
#### 6) SPH300EX NP1PS-74D



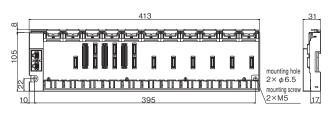
#### 7) SPH200 NP1PH-08, NP1PH-16



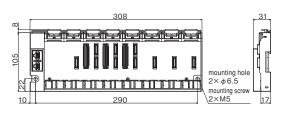
- (3) Base board
- 1) EP-bus-based board 13 slots NP1BE-13, NP1BX-13



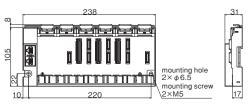
2) EP-bus-based board 11 slots NP1BE-11



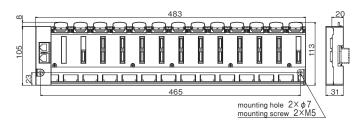
3) EP-bus-based board 8 slots NP1BE-08



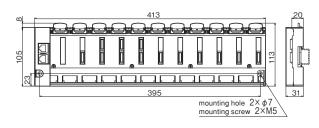
4) EP-bus-based board 6 slots NP1BE-06



 Base board 13 slots NP1BP-13, NP1BS-13, NP1BP-13S, NP1BS-13S, NP1BP-13D, NP1BS-13D

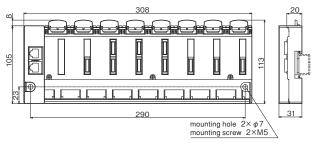


 Base board 11 slots NP1BS-11, NP1BS-11S, NP1BS-11D

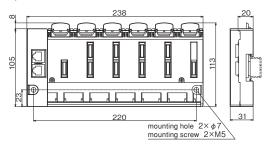


## Programmable Controllers MICREX-SX series Dimensions

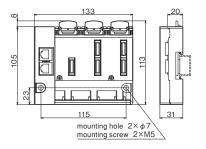
7) Base board 8 slots NP1BS-08, NP1BS-08D, NP1BS-08S



8) Base board 6 slots NP1BS-06



9) Base board 3 slots NP1BS-03



(4) Base board mounting bracket (accessories for base board)

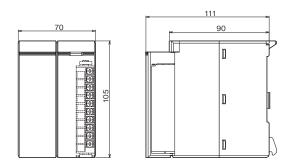
| Туре   | L (mm) |
|--|--------|
| For NP1BE-13, NP1BX-13 / NP1BP-13 / NP1BS-13 / NP1BP-13S / | 476.5  |
| NP1BS-13S / NP1BS-13D / NP1BP-13D                          |        |
| For NP1BE-11 / NP1BS-11 / NP1BS-11S / NP1BS-11D            | 406.5  |
| For NP1BE-08 / NP1BS-08 / NP1BS-08S / NP1BS-08D            | 301.5  |
| For NP1BE-06 / NP1BS-06                                    | 231.5  |
| For NP1BS-03   | 126.5  |
|  |        |



(5) Base board mounting stud NP8B-ST



- (6) I/O module
- 1) Digital I/O module NP1X0805

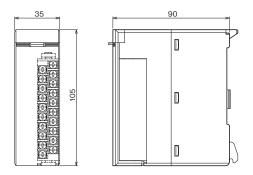


 2) 16-point module Digital I/O module NP1X1606-W, NP1X1607-W, NP1X1610, NP1X1610-RI, NP1X1611-RI
 Digital automatical

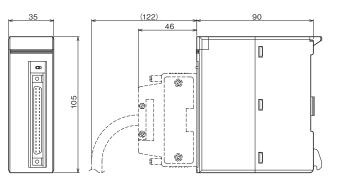
Digital output module NP1Y08T0902, NP1Y16T09P6, NP1Y16T10P2, NP1Y08U0902, NP1Y16U09P6, NP1Y08S, NP1Y16S-08 NP1Y16R-08, NP1Y08R-00 Digital I/O module NP1W1606T, NP1W1606U Analog input module

NP1AX04-MR, NP1AXH4-MR, NP1AX08V-MR, NP1AX08I-MR

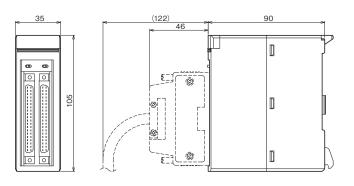
Analog output module NP1AY02-MR, NP1AYH2-MR



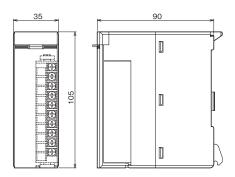
 32-point module Digital input module NP1X3206-W, NP1X3202-W Digital output module NP1Y32T09P1, NP1Y32U09P1 Digital I/O module NP1W3206T, NP1W3206U High-speed digital input module NP1X3206-A Pulse train output built-in digital output module NP1Y32T09P1-A



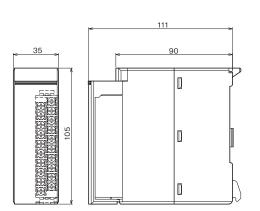
64-point module
 Digital input module NP1X6406-W
 Digital output module NP1Y64T09P1, NP1Y64U09P1
 Digital I/O module NP1W6406T, NP1W6406U



5) 8-point module Digital input module **NP1X0810, NP1X0811** Digital output module **NP1Y08R-04** 



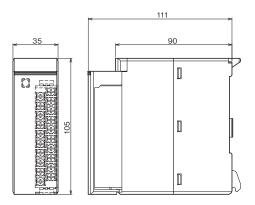
6) Terminal block protrusion module Analog input module NP1AXH8V-MR, NP1AXH8I-MR, NP1AXH8VG-MR, NP1AXH8IG-MR Analog output module NP1AYH4V-MR, NP1AYH4I-MR, NP1AYH4VG-MR, NP1AYH4IG-MR, NP1AYH8V-MR, NP1AYH8I-MR Analog input/output module NP1AYH6-MR Resistance thermometer element input module NP1AXH4-PT Resistance thermometer element input module NP1AXH4-PT Thermo-couple input module NP1AXH4-TC Thermo-couple input module NP1AXH8G-TC



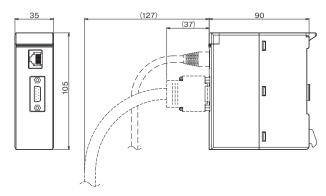
Distributor module NP1AXH4DG-MR

Flow meter F/AD conversion module NP1F-PI4

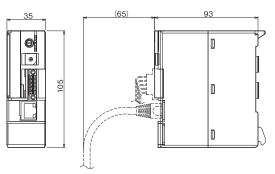
7) Duplex analog output module NP1AYH8VHR-MR



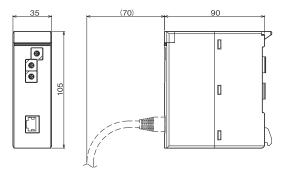
- (7) Communication module
- 1) Web module NP1L-WE1 Ethernet module NP1L-ET1



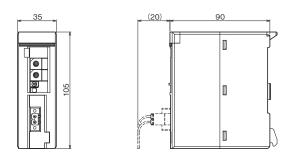
2) Web memory module NP1L-WS1



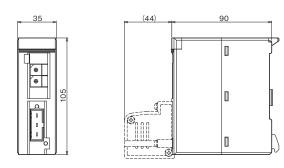
3) FL-net (OPCN-2) module NP1L-FL3



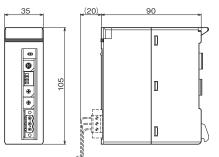
4) LONWORKS interface module NP1L-LW1



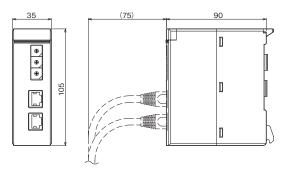
5) P-link module NP1L-PL1 PE-link module NP1L-PE1 OPCN-1 master module NP1L-JP1 OPCN-1 slave module NP1L-JS1 OPCN-1 interface module NP1L-RJ1 T-link master module NP1L-TL1 T-link slave module NP1L-TS1 T-link interface module NP1L-RT1



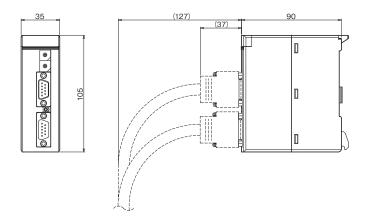
6) LE-net module NP1L-LE1



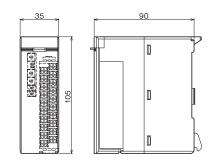
7) LE-net loop 2 module NP1L-LL2



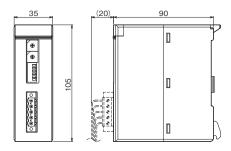
8) General purpose communication module NP1L-RS1/2/3/4 PROFIBUS-DP master module NP1L-PD2, PROFIBUS-DP slave module NP1L-PS1 PROFIBUS-DP interface module NP1L-RP1



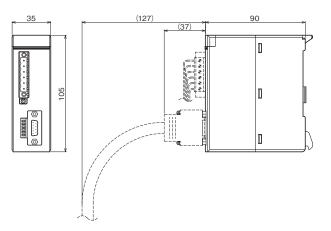
9) General purpose communication module NP1L-RS5



10) DeviceNet master module NP1L-DN1 DeviceNet slave module NP1L-DS1 DeviceNet interface module NP1L-RD1

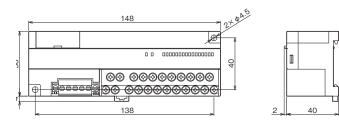


11) Multi-use communication module NP1F-MU1 M-NET communication module NP1L-MN1

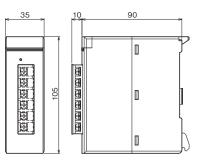


#### 12) NR1 Series

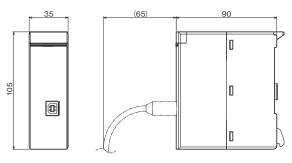
NR1JX-1606DT, NR1JY-08R07DT, NR1JY-16T05DT, NR1JW-16T65DT, NR1SX-1606DT, NR1SY-08R07DT, NR1SY-16T05DT, NR1SW-16T65DT, NR1TX-1606DT, NR1TY-08R07DT, NR1TY-16T05DT, NR1TW-16T65DT, NR1LX-1606DT, NR1LY-08R07DT, NR1LW-11R80DT, NR1SF-HP4DT



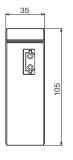
13) Remote terminal master/slave module NP1L-RM1

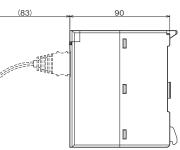


#### 14) USB communication module NP1L-UC1

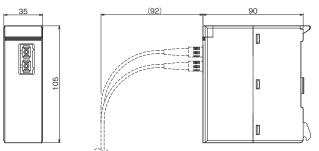


#### 15) SX bus optical link module NP1L-OL1

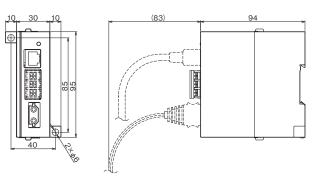




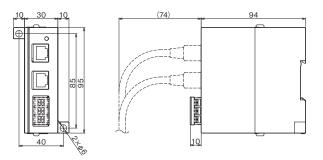
#### NP1L-OL3



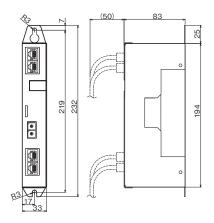
16) SX bus optical link converter NP2L-OE1



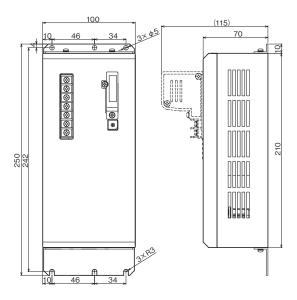
17) SX bus electric repeater NP2L-RP1



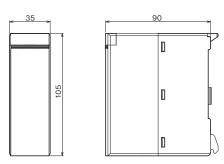
#### 18) SX bus duplication unit NP2L-BH1



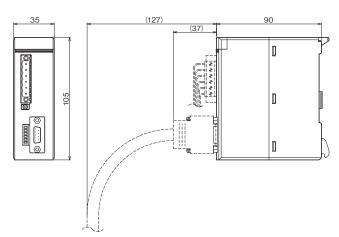
19) T-link optical converter FNC160A-C20 P/PE-link optical converter FNC360A-C20



- (8) Function module/unit
- 1) Dummy module **NP1F-DMY**

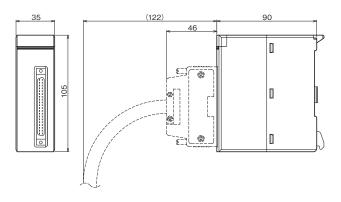


2) Multi-use communication module NP1F-MU1 M-NET communication module NP1L-MN1

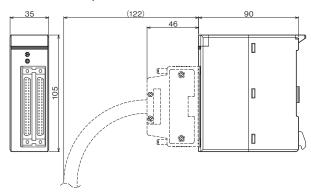


(9) Positioning control module/unit

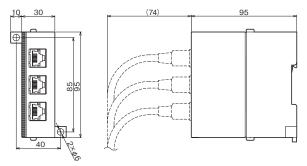
 High-speed counter module NP1F-HC2, NP1F-HC2MR, NP1F-HC2MR1 Multi-channel high-speed counter module NP1F-HC8



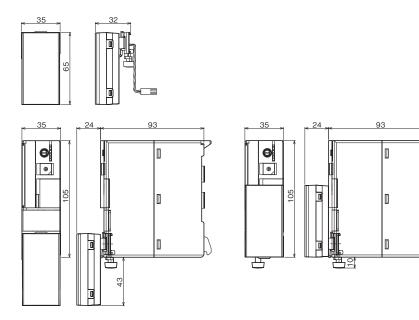
2) Positioning control module NP1F-MA2, NP1F-MP2, NP1F-HP2 NP1F-HD2A, NP1F-HD4



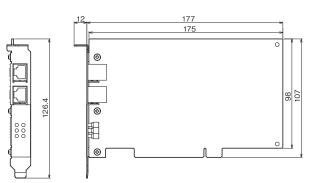
3) SX bus T-branch unit NP8B-TB



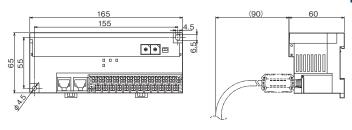
(10) Option1) Battery box NP8P-BTS



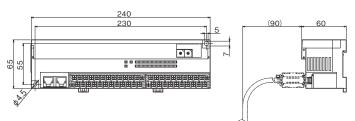
- (11) PCI-bus-based board
- 1) LE-net loop 2 board NP3L-LL2PCS FL-net (OPCN-2 board) NP3L-FL3PCS



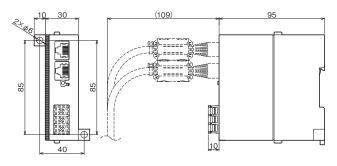
- (12) E-SX bus based
- 1) Analog input/output unit NU2AXH2-MR, NU2AYH2V-MR



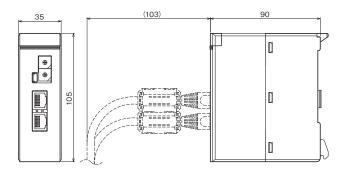
2) Digital I/O unit NU2X3206-W, NU2Y32T09P6



3) Auxiliary power supply unit NU2V-PA1

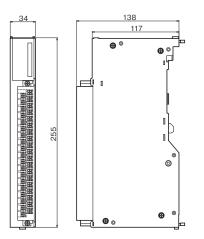


4) Integrated type interface module NP1L-RU1, NP1L-RU1H

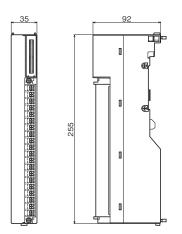


(13) F size I/O module

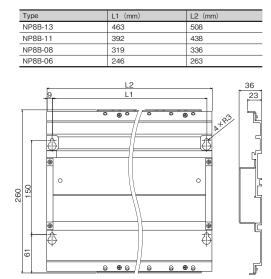
NP8X-120ZC, NP8X-123ZC, NP8X-155ZC, NP8X-165ZC, NP8Y-221ZC, NP8Y-223ZC, NP8Y-226ZC, NP8Y-250ZC, NP8Y-257ZC, NP8Y-263ZC, NP8Y-266ZC NP8AX-340MR, NP8AX-344, NP8AY-440MR



NP8SX-113ZC, NP8SX-143ZC, NP8SY-260ZC, NP8SY-263ZC



MICREX-F Size I/O Module mounting base board NP8B-06, NP8B-08, NP8B-11, NP8B-13



#### Type/Ordering codes

| • | SPH5000M/SPH5000H/SPH3000MG/SPH3000MM E-SX bus product |
|---|--|
|---|--|

|                             |   |  |   |  |  |   |  |               | Stan                                   | dards | 5  |    |
|-----------------------------|---|--|---|--|--|---|--|---------------|--|-------|----|----|
| Product na                  | ame   | Model  | Specifications and names  |  |  |   |  | Ordering code | CE<br>*2                               | UL    | LR | NK |
| CPU<br>module               | SPH5000H  | NP1PU1-512H  | SPH5000H Program memory capacity<br>User ROM/USB/Ethernet                                 | 512 k step   | Data backup battery<br>SX bus terminating plug × | <2<br>ode setting)  | processing speed 6 ns<br>or higher per instruction<br>Applied instruction<br>processing speed 5 ns | NP1PU1-512H   | 0                                      |       |    |    |
|                             | SPH3000MG   | NP1PU1-256NE   | Program memory capacity 256K steps<br>User ROM/USB/Ethernet/SX-Net adapt<br>Points: 73728 | ted, Max. No. of I/O   | battery (Built-in)<br>SX bus terminating plug    | g ×2  | Processing speed   | NP1PU1-256NE  | 0                                      | 0     |    |    |
|                             | SPH3000MM   | NP1PU2-048E  | Program memory capacity 48K steps x User ROM/USB/Ethernet adapted, Max.                   | Initiations and names       code       *2       cUL       *3         5000H Program memory capacity 512 k step       Accessories: Instruction Manual Data backup battery       Basic instruction processing speed 6 ns or higher per instruction       NP1PU1-512H       ○       I         37       SX bus terminating plug x2       Screwdriver (CPU No, CPU mode setting)       processing speed 6 ns or higher per instruction       NP1PU1-512H       ○       I       I         37       SX bus terminating plug x2       Screwdriver (CPU No, CPU mode setting)       processing speed 6 ns or higher per instruction       NP1PU1-250NE       ○       I <t< td=""></t<> |  |   |  |               |  |       |    |    |
|                             |   | NP1PU2-256E  | Program memory capacity 256K steps<br>User ROM/USB/Ethernet adapted, Max.                 |  |  |   | 9 ns –   | NP1PU2-256E   | 0                                      | 0*7   | LR |    |
|                             | SPH5000M  | NP1PA1-096E  | Program memory capacity 96K steps   |  |  |   |  |               | 0                                      | 0     |    |    |
|                             |   | NP1PA1-128E  | Program memory capacity 128K steps  | Ethernet<br>Max. No. of I/O Points:  |  |   |  | NP1PA1-128E   | 0                                      | 0     |    |    |
|                             |   | NP1PA1-256E  | Program memory capacity 256K steps  | 512 k step       Accessories: Instruction Manual Data backup battery<br>SX bus terminating plug x2<br>Screwdriver (CPU No., CPU mode setting)<br>Connector dust caps x7       Basic instruction robust caps x1<br>Applied instrue<br>processing sp<br>or higher per it         ed, Max. No. of I/O       Accessories: Data backup<br>battery (Built-in)<br>SX bus terminating plug x2<br>Screwdriver (for the CPU<br>setting)       Basic instrue<br>Processing<br>9 ns -         vo. of I/O Points: 139264       Accessories: Instruction Manual<br>SX bus terminating plug x2<br>Screwdriver (for the CPU<br>setting)       Basic instrue<br>Processing<br>9 ns -         vo. of I/O Points: 139264       Accessories: Instruction Manual<br>SX bus terminating plug x2<br>Screwdriver (CPU No., CPU mode setting)<br>Batery holder       Basic instrue<br>Processing<br>4 ns -         ************************************   | 4 115 -  | NP1PA1-256E   | 0  | 0             |  |       |    |    |
|                             |   | NP1PA1-512E  | Program memory capacity 512K steps  | RS-422 port for  |  |   |  | NP1PA1-512E   | 0                                      | 0     |    |    |
| E-SX bus                    | extension   | NU1C-P3  | 300 mm cable  |  |  |   |  | NU1C-P3       | -                                      |       |    |    |
| cable<br>*1                 |   | NU1C-P6  | 600 mm cable  |  |  |   |  | NU1C-P6       | -                                      |       |    |    |
| I                           |   | NU1C-P8  | 800 mm cable  |  |  |   |  | NU1C-P8       | -                                      |       |    |    |
|                             |   | NU1C-02  | 2,000 mm cable  |  |  |   |  | NU1C-02       | -                                      |       |    |    |
|                             |   | NU1C-05  | 5,000 mm cable  |  |  |   |  | NU1C-05       | -                                      |       |    |    |
|                             |   | NU1C-10  | 10,000 mm cable   |  |  |   |  | NU1C-10       | -                                      |       |    |    |
|                             | SPH5000M NP1PA1<br>NP1PA1<br>NP1PA1<br>NP1PA1<br>NP1PA1<br>NP1PA1<br>NP1C-1<br>NU1C-1<br>NU1C-1<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2<br>NU1C-2 | NU1C-15  | 15,000 mm cable   |  |  |   |  | NU1C-15       | -                                      |       |    |    |
|                             |   | NU1C-25  | 25,000 mm cable   |  |  |   |  | NU1C-25       | CE         UL         LR           22H |       |    |    |
|                             |   | NU1C-50  | 50,000 mm cable   |  |  |   |  | NU1C-50       | -                                      |       | LR |    |
|                             |   | NU1C-A0  | 100,000 mm cable  |  |  |   |  | NU1C-A0       | -                                      |       |    |    |
| Duplex E-S<br>type interfac |   | NP1L-RU1H  | E-SX bus duplex   |  |  | Ordering<br>code         CE<br>*2         UL<br>cul<br>cul         LR<br>*3         N           Manual<br>x2<br>node setting)         Basic instruction<br>processing speed 6 ns<br>or higher per instruction<br>processing speed 5<br>ns -         NP1PU1-512H         □         I |  |               |  |       |    |    |
| Communie                    | cation module   | NP1L-RU1   | E-SX bus integrated type interface mod  | lule   |  |   |  | NP1L-RU1      | 0                                      |       |    |    |
| E-SX bus                    |   | NU2X3206-W   | 24 V DC, 32 points, 7 mA, 0 to 100 ms   | variable   | S  | Screw terr  | ninal  | NU2X3206-W    | 0                                      | 0     |    |    |
| Separate  <br>unit          | placement   | NU2Y32T09P6  | Transistor sink , 12 to 24 V DC, 32 poin  | ts, 0.6 A/point, 4 A/comm  | on S   | Screw terr  | ninal  | NU2Y32T09P6   | 0                                      | 0     |    |    |
| unit                        |   | NU2AXH2-MR   | High-speed multiple-range input 2 ch, r   | esolution: 15 bits, 25 μs c  | conversion period S                              | Screw terr  | ninal  | NU2AXH2-MR    | 0                                      | 0     |    |    |
|                             |   | NU2AYH2V-MR  | High-speed multiple-range output 2 ch, res  | solution: 15 bits (voltage), 2   | 5 µs conversion period                           | Screw tern  | ninal  | NU2AYH2V-MR   | 0                                      | 0     |    |    |
|                             |   | Model         Specifications and<br>Specifications and<br>User ROM/USB/E           NP1PU1-512H         SPH5000H Progra<br>User ROM/USB/E           M         NP1PU1-256NE         Program memory<br>User ROM/USB/E           NP1PU2-048E         Program memory<br>User ROM/USB/Et           NP1PU2-256E         Program memory<br>User ROM/USB/Et           NP1P41-096E         Program memory<br>User ROM/USB/Et           NP1PA1-096E         Program memory           NP1PA1-128E         Program memory           NP1PA1-512E         Program memory           NP1PA1-512E         Program memory           NU1C-P3         300 mm cable           NU1C-P6         600 mm cable           NU1C-15         15,000 mm cable           NU1C-15         15,000 mm cable           NU1C-15         50,000 mm cable           NU1C-15         50,000 mm cable           NU1C-15         50,000 mm cable           NU1C-15         50,000 mm cable           NU1C-25         25,000 mm cable           NU1C-30         100,000 mm cable           NU1C-40         100,000 mm cable           NU1C-40         100,000 mm cable           NU1C-40         100,000 mm cable           NU1C-50         50,000 mm cable           NU1C-40 | High-speed counter unit, 4 Mbps (line c   | lriver), 1 Mbps (open colle  | ector 5 V/12 V/24 V DC)                          |   |  | NU2F-HC2      | 0                                      | 0     |    |    |
|                             |   | NU2V-PA1   | Auxiliary power unit E-SX bus built-in 2  | 4 V DC power supply  |  |   |  | NU2V-PA1      | 0                                      | 0     |    |    |
| ROM card                    |   | NP8PSD-002   | User ROM card SD memory card for SI   | PH3000/SPH3000 MM/SI   | PH3000MG, Capacity 2                             | GB  |  | NP8PSD-002    | -                                      | -     | -  | -  |

\*1 Any length of cable is applicable. Contact our sales representatives for details.
 \*7 There is no cUL certification.

#### SPH product

|   | ame   | Model  | Specifications and names  |  |   |  | Ordering code   |   | UL<br>CUL   |   | Τ |
|---|---|--|---|--|---|--|---|---|---|---|---|
|   | SPH200  | NP1PH-08   | Program memory capacity 8K steps  |  | Accessories:<br>Memory backup battery   | Basic instruction<br>Processing speed                              | NP1PH-08  | 0   | 0   | Õ                                       | ( |
|   |   |  | Max. number of I/O points: 8192 points  |  | (built-in)  | 70 ns –  | NP1PH-16  | $\cap$                                    | $\cap$  |   | ╈ |
|   |   | NP1PH-16   | Program memory capacity 16K steps   |  | SX bus terminating plug 2 pieces  |  | NP1PH-16  | 0   | 0   | 0                                       | 0 |
| ŀ   | SDH300  | ND1D5-32   | Max. number of I/O points: 8192 points<br>Program memory capacity 32K steps   |  | Screwdriver (for the CPU  | Basic instruction  | ND1D9-32  | 0   | 0   | 0                                       | ( |
| ľ   | 361300  | NF1F3-32   | Max. number of I/O points: 8192 points  |  | setting)  | Processing   | INF IF 3-32   | Ρ   | 0   | P                                       | ľ |
|   |   | NP1PS-32B  | Program memory capacity 32K steps   |  |   | speed  | NP1PS-32R   | 0   | 0   | 0                                       |   |
|   |   | 11110-0211   | User ROM/USB adapted, Max. No. of I/O po  | nints: 8192 noints   |   | 20 ns –  |   |   |   | ľ                                       |   |
| SPH300<br>SPH300<br>SPH200<br>SPH200<br>SPH200<br>SPH300<br>SPH300<br>SPH300<br>SPH300<br>SPH300  |   | NP1PS-74R  | Program memory capacity 74K steps   |  |   |  | NP1PS-74R   | 0   | 0   | 0                                       | 1 |
|   |   |  | User ROM/USB adapted, Max. No. of I/O po  | pints: 8192 points   |   |  |   |   |   | ľ                                       |   |
|   |   | NP1PS-117R   | Program memory capacity 117K steps  |  |   |  | NP1PS-117R  | 0   | 0   | 0                                       | đ |
|   |   |  | User ROM/USB adapted, Max. No. of I/O po  | pints: 8192 points   |   |  | -   |   | Ĩ   |   | 1 |
|   |   | NP1PS-245R   | Program memory capacity 245K steps  |  |   |  | NP1PS-245R  | 0   | 0   | 0                                       | T |
|   |   |  | User ROM/USB adapted, Max. No. of I/O po  | pints: 8192 points   |   |  |   |   | _   | -                                       |   |
| 1   | SPH300EX  | NP1PS-74D  | Program memory capacity 74K steps x 2   | · ·  |   |  | NP1PS-74D   | 0   | 0   |   | T |
|   |   |  | User ROM/USB adapted, Max. No. of I/O po  | pints: 8192 points x 2   |   |  |   |   |   |   |   |
| 1   | SPH2000   | NP1PM-48R  | Program memory capacity 48K steps   | · · · ·  |   | Basic instruction  | NP1PM-48R   | 0   | 0   | 0                                       | ٦ |
|   |   |  | User ROM/USB adapted, Max. No. of I/O po  | pints: 8192 points   |   | Processing   |   |   |   |   |   |
|   |   | NP1PM-48E  | Program memory capacity 48K steps   |  |   | speed<br>30 ns –   | NP1PM-48E   | 0   | 0   | 0                                       |   |
| NP1PM-25           NP1PM-25           SPH3000           NP1PU-04           NP1PU-12   |   | User ROM/USB/Ethernet adapted, Max. No   | . of I/O Points: 8192   |  |   |  |   |   |   | ĺ                                       |   |
|   | NP1PM-256E  | Program memory capacity 256K steps   |   | 1  |   | NP1PM-256E   | 0   | 0   | 0   | 1                                       |   |
|   | ile Interest of the second se |  | User ROM/USB/Ethernet adapted, Max. No  | . of I/O Points: 8192  |   |  |   |   |   |   |   |
|   |   | NP1PM-256H   | Program memory capacity 256K steps, redu  | ndancy function supported  |   |  | NP1PM-256H  | 0   | 0   | 0                                       |   |
| SPH300EX         NP1PS-24           SPH300EX         NP1PS-74           SPH2000         NP1PS-74           SPH2000         NP1PS-74           SPH2000         NP1PM-48           NP1PM-25         NP1PM-25           SPH3000         NP1PU-04           SPH3000         NP1PU-04           SPH3000         NP1PU-04           SPH3000         NP1PU-04           SPH3000D         NP1PU-04           SPH3000D         NP1PU-04           SPH3000D         NP1PU-04           SPH3000D         NP1PU-04           SPH3000E         NP1PU-04           SPH3000E         NP1PU-056           NP1PU-128         NP1PU-128           SPH3000E         NP1PA1C-02           NP1PA1C-12         NP1PA1C-02           NP1PA1C-25         NP1PA1C-02           SPH3000E         NP1PA1C-02           NP1PA1C-25         NP1PA1C-25           SPH3000E         NP1PA1C-02           NP1PA1C-25         NP1PA1C-25           SPH3000E         NP1PA1C-02           NP1PA1C-31         NP1PA1C-31 |   | User ROM/USB adapted, Max. No. of I/O po   | pints: 8192 points  |  |   |  |   |   |   |   |   |
|   | NP1PU-048E  | Program memory capacity 48K steps  |   |  | Basic instruction   | NP1PU-048E   | 0   | 0   |   |   |   |
|   |   | User ROM/USB/Ethernet adapted, Max. No   | . of I/O Points: 8192   |  | Processing  |  |   |   |   |   |   |
|   |   | NP1PU-128E   | Program memory capacity 128K steps  |  |   | speed<br>9 ns –  | NP1PU-128E  | 0   | 0   |   |   |
| BACnet MS/<br>TP*<br>CPU  |   | User ROM/USB/Ethernet adapted, Max. No   | . of I/O Points: 8192   |  |   |  |   |   |   |   |   |
|   | NP1PU-256E  | Program memory capacity 256K steps   |   |  |   | NP1PU-256E   | 0   | 0   |   |   |   |
|   |   | User ROM/USB/Ethernet adapted, Max. No   | . of I/O Points: 8192   |  |   |  |   |   |   |   |   |
| TP*   | NP1PUBM-048C  | Program memory capacity 48K steps,   |   |  | Basic instruction   | NP1PUBM-048C   | 0   | 0   |   |   |   |
| TP*<br>CPU  |   | BACnet protocol  | 05 0040   |  | Processing  |  |   |   |   |   |   |
|   |   | Compliant with ANSI/ASHRAE Standard 1     Operates as MS/TP master   | 35-2012   |  | speed<br>9 ns –   |  |   |   |   | 1                                       |   |
|   |   |  | Operates as MS/TP master     Device profiles support B-ASC functions  |  |   |  |   |   |   |   |   |
|   |   | NP1PU-048EZM   | Program memory capacity 48K steps   |  |   | Basic instruction  | NP1PU-048EZM  | 0   | 0   |   |   |
|   |   |  | User ROM/USB/Ethernet adapted, Max. No  | . of I/O points: 8,192   |   | Processing<br>speed  |   |   |   |   |   |
|   |   | NP1PU-096EZM   | Program memory capacity 96K steps   |  |   | 9 ns –   | NP1PU-096EZM  | 0   | 0   |   |   |
|   |   |  | User ROM/USB/Ethernet adapted, Max. No  | . of I/O points: 8,192   |   |  |   |   |   |   |   |
|   |   | NP1PU-128EZM   | Program memory capacity 128K steps  |  |   |  | NP1PU-128EZM  | 0   | 0   |   |   |
|   |   |  | User ROM/USB/Ethernet adapted, Max. No  | . of I/O points: 8,192   |   |  |   |   |   |   |   |
|   |   | NP1PU-256EZM   | Program memory capacity 256K steps  |  |   |  | NP1PU-256EZM  | 0   | 0   |   |   |
| l   |   |  | User ROM/USB/Ethernet adapted, Max. No  | . of I/O points: 8,192   |   |  |   |   |   |   |   |
|   | SPH3000EC   | NP1PA1C-096E   | Program memory capacity 96K steps   | User ROM/USB/Ethernet  |   | Basic instruction  | NP1PA1C-096E  | 0   |   |   |   |
|   |   | NP1PA1C-128E   | Program memory capacity 128K steps  | (1000BASE-T) compatible  | Instruction manual,<br>SX bus terminating plug (2x),  | Processing   | NP1PA1C-128E  | 0   |   |   |   |
|   |   | NP1PA1C-256E   | Program memory capacity 256K steps  | 73,728   | driver (for CPU No. settings),  | speed<br>4 ns –  | NP1PA1C-256E  | $\bigcirc$                                |   |   |   |
|   |   | NP1PA1C-512E   | Program memory capacity 512K steps  | EtherCAT port 1 -system  | battery holder  |  | NP1PA1C-512E  | 0   |   |   |   |
| er sup  | ply module  | NP1S-22  | Input: 100 to 120 V/ 200 to 240 V AC Output:  | 35 W Accessories: ALM con  | tact connector, line voltage sw   | ritching short bar   | NP1S-22   | $\bigcirc$                                | ○*4   | 0                                       |   |
|   |   | NP1S-91  | Input: 100 to 120 V AC Output: 15 W (1 slo  | ot)  |   |  | NP1S-91   | $\bigcirc$                                | ○*5   |   |   |
|   |   |  | Input: 200 to 240 V AC Output: 15 W (1 slo  |  |   |  | NP1S-81   | $\circ$                                   | $\bigcirc^{*6}$   |   |   |
|   |   | NP1S-81  | 111pul. 200 to 240 V AC Outpul. 15 W (1 Sic   | ot)  |   |  |   | 0   | ○*7   | 0                                       |   |
|   |   |  | Input: 24 V DC Output: 15 Accessories: A  |  |   | -  | NP1S-42   |   |   |   |   |
|   |   | NP1S-42  |   |  |   | Accessories:   | NP1S-42<br>NP1S-22S   | 0   | 0   |   |   |
|   |   | NP1S-42<br>NP1S-22S  | Input: 24 V DC Output: 15 Accessories: A  |  |   | ALM contact  |   | 0   | 0   |   |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S  | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W   |  |   | ALM contact<br>connector   | NP1S-22S<br>NP1S-62S  | 0   | -   | 0                                       |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03  | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots  |  |   | ALM contact<br>connector<br>Accessories:<br>Base board             | NP1S-22S<br>NP1S-62S<br>NP1BS-03  | 0   | 0   | -                                       |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06  | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 4 slots   |  |   | ALM contact<br>connector<br>Accessories:<br>Base board<br>Mounting | NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06  | 0   | 0   | 0                                       |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08  | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 4 slots<br>For 8 slots Processor buses 3 slots  |  |   | ALM contact<br>connector<br>Accessories:<br>Base board             | NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08  | 0<br>0<br>0                               | 0<br>0<br>0<br>0  | 0                                       |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11  | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 4 slots<br>For 8 slots Processor buses 3 slots<br>For 11 slots Processor buses 3 slots  |  |   | ALM contact<br>connector<br>Accessories:<br>Base board<br>Mounting | NP1S-22S           NP1S-62S           NP1BS-03           NP1BS-06           NP1BS-08           NP1BS-11   | 0<br>0<br>0<br>0                          | 0<br>0<br>0<br>0  | 0                                       |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13  | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 4 slots<br>For 8 slots Processor buses 3 slots<br>For 11 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots  |  |   | ALM contact<br>connector<br>Accessories:<br>Base board<br>Mounting | NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13  | 0<br>0<br>0                               | 0<br>0<br>0<br>0  | 000000000000000000000000000000000000000 |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13<br>NP1BP-13  | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 4 slots<br>For 8 slots Processor buses 3 slots<br>For 11 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 10 slots   | LM contact connector   | ses 3 slots   | ALM contact<br>connector<br>Accessories:<br>Base board<br>Mounting | NP1S-22S           NP1S-62S           NP1BS-03           NP1BS-06           NP1BS-108           NP1BS-11           NP1BS-13           NP1BP-13    | 0<br>0<br>0<br>0<br>0<br>0                | 0<br>0<br>0<br>0<br>0<br>0<br>0   | 0                                       |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13<br>NP1BP-13<br>NP1BS-08S   | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 3 slots<br>For 11 slots Processor buses 3 slots<br>For 13 slots Processor buses 10 slots<br>Base board with station number setting swit   | LM contact connector   |   | ALM contact<br>connector<br>Accessories:<br>Base board<br>Mounting | NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13<br>NP1BP-13<br>NP1BS-08S   | 0<br>0<br>0<br>0                          | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0  | 000000000000000000000000000000000000000 |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13<br>NP1BS-08S<br>NP1BS-11S  | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 3 slots<br>For 11 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 10 slots<br>Base board with station number setting swit<br>Base board with station number setting swit  | LM contact connector<br>ch, for 8 slots processor bus<br>ch, for 11 slots processor bus  | ises 3 slots  | ALM contact<br>connector<br>Accessories:<br>Base board<br>Mounting | NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13<br>NP1BS-08S<br>NP1BS-11S  | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0 | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0   | 000000000000000000000000000000000000000 |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13<br>NP1BS-08S<br>NP1BS-11S<br>NP1BS-13S                           | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 3 slots<br>For 11 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 10 slots<br>Base board with station number setting swit<br>Base board with station number setting swit  | ch, for 8 slots processor bus<br>ch, for 11 slots processor bus<br>ch, for 13 slots processor bus  | ises 3 slots<br>ises 3 slots  | ALM contact<br>connector<br>Accessories:<br>Base board<br>Mounting | NP15-22S<br>NP18-62S<br>NP18S-03<br>NP18S-06<br>NP18S-08<br>NP18S-11<br>NP18S-13<br>NP18S-08S<br>NP18S-11S<br>NP18S-13S                           |   | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0                          | 000000000000000000000000000000000000000 |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13<br>NP1BS-13<br>NP1BS-08S<br>NP1BS-11S<br>NP1BS-13S<br>NP1BP-13S  | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 3 slots<br>For 11 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 10 slots<br>Base board with station number setting swit<br>Base board with station number setting swit<br>Base board with station number setting swit   | ch, for 8 slots processor bus<br>ch, for 11 slots processor bus<br>ch, for 13 slots processor bus<br>ch, for 13 slots processor bus  | uses 3 slots<br>uses 3 slots<br>uses 10 slots   | ALM contact<br>connector<br>Accessories:<br>Base board<br>Mounting | NP15-22S<br>NP18-62S<br>NP18S-03<br>NP18S-06<br>NP18S-08<br>NP18S-11<br>NP18S-13<br>NP18S-08S<br>NP18S-11S<br>NP18S-13S<br>NP18P-13S              | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0 | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0 |   |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13<br>NP1BS-08S<br>NP1BS-11S<br>NP1BS-13S<br>NP1BS-13S<br>NP1BS-08D | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 3 slots<br>For 11 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 10 slots<br>Base board with station number setting swit<br>Base board with station number setting swit<br>Hot plug base board with station number setting swit | ch, for 8 slots processor bus<br>ch, for 11 slots processor bus<br>ch, for 13 slots processor bus<br>ch, for 3 slots processor bus               | uses 3 slots<br>uses 3 slots<br>uses 10 slots<br>essor buses 3 slots                                    | ALM contact<br>connector<br>Accessories:<br>Base board<br>Mounting | NP15-22S<br>NP15-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13<br>NP1BS-08S<br>NP1BS-11S<br>NP1BS-13S<br>NP1BS-13S<br>NP1BS-08D |   | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0 |   |   |
| ly moo  | dule  | NP1S-42<br>NP1S-22S<br>NP1S-62S<br>NP1BS-03<br>NP1BS-06<br>NP1BS-08<br>NP1BS-11<br>NP1BS-13<br>NP1BS-13<br>NP1BS-08S<br>NP1BS-11S<br>NP1BS-13S<br>NP1BP-13S  | Input: 24 V DC Output: 15 Accessories: A<br>Input: 100 to 200 V AC, Output: 70 W<br>Input: 110 V DC, Output: 70 W<br>For 3 slots Processor buses 2 slots<br>For 6 slots Processor buses 3 slots<br>For 11 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 3 slots<br>For 13 slots Processor buses 10 slots<br>Base board with station number setting swit<br>Base board with station number setting swit<br>Base board with station number setting swit   | LM contact connector<br>LM contact connector<br>ch, for 8 slots processor bus<br>ch, for 11 slots processor bu<br>ch, for 13 slots processor bu<br>ch, for 13 slots processor bu<br>thing switch, for 8 slots proce<br>on number setting switch, for 1 | uses 3 slots<br>uses 3 slots<br>uses 10 slots<br>assor buses 3 slots<br>1 slots processor buses 3 slots | ALM contact<br>connector<br>Accessories:<br>Base board<br>Mounting | NP15-22S<br>NP18-62S<br>NP18S-03<br>NP18S-06<br>NP18S-08<br>NP18S-11<br>NP18S-13<br>NP18S-08S<br>NP18S-11S<br>NP18S-13S<br>NP18P-13S              |   | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0 |   |   |

O Applicable

- Not applicable

\*Only for Japan's doemestic market
\*1 Any length of cable is applicable. Contact our sales representatives for details.
\*2 The compliance with the CE marking is confirmed for a single unit in the SX series. Be sure to check the compliance with the standard of the final product in which the SX series is built.
\*3 To prevent the vibration, the module must be fixed for each of the base boards.
\*4 The model NP1S-22 B is UL-certified.
\*5 The model NP1S-91 A is UL-Recognition-certified (cUL certification is not obtained).
\*6 The model NP1S-81 A is UL-Recognition-certified (cUL certification is not obtained).
\*7 There is no cUL certification.

| Product name         | Model   | Specifications and names  |  |  | Ordering code                           |                | UL<br>CUL | LR       | NK       |
|----------------------|---|---|--|--|---|----------------|-----------|----------|----------|
| EP-bus-based board   | NP1BE-06  | 6 slots Proc  | cessor buses 3 slots                               | Accessories:                                       | NP1BE-06                                | $\overline{0}$ |           | 3        | -        |
|                      |   | 1100  | 15000M/EC support 3 slots                          | Base board,  | NP1BE-08                                | 0              | 0         |          |          |
|                      | NP1BE-11  | 11 slots  |  | Mounting bracket                                   | NP1BE-11                                | 0              | 0         |          |          |
|                      | NP1BE-13  | 13 slots  |  |  | NP1BE-13                                | 0              | -         | -        |          |
| , j                  |   |   |  | -  |   |                | 0         |          | 4        |
|                      | NP1BX-13  |   | cessor buses 10 slots<br>I5000M/EC support 3 slots |  | NP1BX-13                                | 0              | 0         |          |          |
|                      | NP1C-P3   | 300 mm cable  |  |  | NP1C-P3                                 | -              | 0         | 0        | 0        |
|                      | NP1C-P6   | 600 mm cable  |  |  | NP1C-P6                                 | -              | 0         | 0        | 0        |
| 1                    | NP1C-P8   | 800 mm cable  |  |  | NP1C-P8                                 | -              | 0         | 0        | 0        |
|                      | NP1C-P8         800 mm cable           NP1C-02         2,000 mm cable           NP1C-05         5,000 mm cable           NP1C-10         10,000 mm cable           NP1C-15         15,000 mm cable           NP1C-25         25,000 mm cable           NP1C-25         25,000 mm cable           NP1C-25         25,000 mm cable           T-branch unit         NP8B-TB           SX bus T-branch connecting unit, Accessories: SX bus ter |   |  |  | NP1C-02                                 | -              | 0         | 0        | 0        |
|                      |   | 5,000 mm cable  |  |  | NP1C-05                                 | -              | 0         | 0        | 0        |
|                      |   |   |  |  | NP1C-10                                 | -              | 0         | 0        | 0        |
|                      |   | ,   |  |  | NP1C-15                                 | -              |           |          |          |
|                      |   |   |  |  | NP1C-25                                 | -              | 0         | 0        | 0        |
|                      |   | •   | bus terminating plug 1 piece                       | 1  | NP8B-TB                                 | 0              | 0         | 0        | 0        |
|                      |   |   |  | Screw terminal                                     | NP1X1606-W                              | 0              | 0         | 0        | 0        |
|                      |   |   | -  | Connector  | NP1X3206-W                              | 0              | 0         | 0        | 0        |
|                      |   |   |  | Connector  | NP1X3202-W                              | 0              | 0         | 0        | 0        |
|                      |   |   |  | Connector  | NP1X3206-A                              | 0              | 0         |          |          |
|                      |   |   | ptional connector                                  | Connector  | NP1X6406-W                              | 0              | 0         | 0        | 0        |
|                      | NP1X1607-W  |   |  | Screw terminal                                     | NP1X1607-W                              | 0              | 0         |          |          |
|                      | NP1X0805  |   |  | Screw terminal                                     | NP1X0805                                |                |           | <b>_</b> | f        |
|                      | NP1X0810  |   |  | Screw terminal                                     | NP1X0810                                | 0              | 0         | 0        | 0        |
|                      |   |   |  | Screw terminal                                     | NP1X1610                                | $\overline{0}$ | Ŏ         | ŏ        | ŏ        |
|                      |   |   |  | Screw terminal                                     | NP1X0811                                | 0              | 0         |          | 0        |
|                      |   |   |  | Screw terminal                                     | NP1X1610-RI                             | 0              | 0         |          |          |
|                      |   | 200 to 240 V AC, 16 points, 7 mA, 10 ms   |  | Screw terminal                                     | NP1X1611-RI                             | 0              | 0         |          |          |
|                      |   | Transistor sink , 12 to 24 V DC, 8 points, 2.4 A/point  | nt 8 A/common                                      | Screw terminal                                     | NP1Y08T0902                             | $\overline{0}$ | 0         | 0        | 0        |
|                      |   | Transistor sink, 12 to 24 V DC, 6 points, 2.4 A/point<br>Transistor sink, 12 to 24 V DC, 16 points, 0.6 A/pc  |  | Screw terminal                                     | NP1Y16T09P6                             | B              | 0         | 0        | 6        |
| ·0                   |   | Transistor sink , 12 to 24 v DC, 10 points, 0.12 A/point,<br>Transistor sink , 24 V DC, 32 points, 0.12 A/point,<br>Pulse train output 20 kHz x 4 ch (Built-in), optional | 3.2 A/common                                       | Connector  | NP1Y32T09P1-A                           | 0              | 0         |          |          |
|                      | NP1Y32T09P1   | Transistor sink , 12 to 24V DC, 32 points, 0.12 A/p   | oint, 3.2 A/common, optional connector             | Connector  | NP1Y32T09P1                             | $\bigcirc$     | 0         | 0        | 0        |
|                      | NP1Y64T09P1   | Transistor sink , 12 to 24V DC, 64 points, 0.12 A/p   | oint, 3.2 A/common, optional connector             | Connector  | NP1Y64T09P1                             | 0              | 0         | 0        | 0        |
| NP1Y16T10P           |   | Transistor sink , 48 V DC, 16 points, 0.2 A/point, 1.   | .6 A/common  | Screw terminal                                     | NP1Y16T10P2                             | 0              | 0         |          |          |
|                      | NP1Y08U0902   | Transistor source, 12 to 24 V DC, 8 points, 2.4 A/p   | ooint, 8 A/common                                  | Screw terminal                                     | NP1Y08U0902                             | 0              | 0         | 0        | 0        |
|                      | NP1Y16U09P6   | Transistor source, 12 to 24 V DC, 16 points, 0.6 A/   | /point, 4 A/common                                 | Screw terminal                                     | NP1Y16U09P6                             | 0              | 0         | 0        | 0        |
| Ī                    | NP1Y32U09P1   | Transistor source, 12 to 24V DC, 32 points, 0.12 A  | /point, 3.2 A/common, optional connector           | Connector  | NP1Y32U09P1                             | 0              | 0         | 0        | 0        |
|                      | NP1Y64U09P1   | Transistor source, 12 to 24V DC, 64 points, 0.12 A  | /point, 3.2 A/common, optional connector           | Connector  | NP1Y64U09P1                             | 0              | 0         | 0        | 0        |
|                      | NP1Y08S   | SSR, 100 to 240 V AC, 8 points: all points are inde   | ependent, 2.2 A/point                              | Screw terminal                                     | NP1Y08S                                 |                |           | 0        | 0        |
|                      | NP1Y08R-04  | Ry, 110 V DC, 240 V AC, 8 points, 30 V DC/264 V   | AC: 2.2 A/point, 4 A/common                        | Screw terminal                                     | NP1Y08R-04                              | 0              | 0         | 0        | 0        |
|                      | NP1Y16R-08  | Ry, 110 V DC, 240 V AC, 16 points, 30 V DC/264 V  | V AC: 2.2 A/point, 8 A/common                      | Screw terminal                                     | NP1Y16R-08                              | 0              | 0         | 0        | 0        |
|                      | NP1Y08R-00  | Ry, 110 V DC, 240 V AC, 8 points, 30 V DC/264 V   | AC: 2.2 A/point, independent                       | Screw terminal                                     | NP1Y08R-00                              |                |           | 0        | 0        |
| Digital I/O module   | NP1W1606T   | 24 V DC, 8-point source input, 12 to 24 V DC, 8-po  | oint Tr sink output                                | Screw terminal                                     | NP1W1606T                               | 0              | 0         | 0        | 0        |
|                      |   | 24 V DC, 8-point sink input, 12 to 24 V DC, 8-point   |  | Screw terminal                                     | NP1W1606U                               | 0              | 0         | 0        | 0        |
|                      |   | 24 V DC, 16-point source input, 12 to 24 V DC Tr s  | sink 16-point output, optional connector           | Connector  | NP1W3206T                               | Ō              | Ō         | Ō        | 0        |
|                      |   | 24 V DC 16-point sink input, 12 to 24 V DC Tr sour  |  | Connector  | NP1W3206U                               | Ō              | 0         | 0        | 0        |
|                      |   | 24 V DC, 32-point source input, 12 to 24 V DC Tr s  |  | Connector  | NP1W6406T                               | Õ              | 0         | Ō        | 0        |
|                      |   | 24 V DC, 32-point bidirectional input, 12 to 24 V DC Tr   |  | Connector  | NP1W6406U                               | 0              | Ō         |          | É        |
| Analog input module  |   | Standard type multi-range input 4 ch, resolution: 1   |  | Screw terminal                                     | NP1AX04-MR                              | 0              | 0         | 0        | 0        |
|                      |   | High-speed multi-range input 4 ch, resolution: 14 b   |  | Screw terminal                                     | NP1AXH4-MR                              | 0              | Ō         | Ō        | 0        |
|                      |   | Standard type multi-range input 8 ch, resolution: 1   |  | Screw terminal                                     | NP1AX08V-MR                             | 0              | Ō         | Ō        | Ō        |
|                      |   | Standard type multi-range input 8 ch, resolution: 1   |  | Screw terminal                                     | NP1AX08I-MR                             | 0              | 0         | 0        | 0        |
|                      |   | High-speed multi-range input 8 ch, resolution: 14 b   |  | Screw terminal                                     | NP1AXH8V-MR                             | 0              | Ō         | Ō        | Ō        |
|                      |   | High-speed multi-range input 8 ch, resolution: 14 b   |  | Screw terminal                                     | NP1AXH8I-MR                             | Ō              | Ō         | Õ        | Ō        |
|                      |   | High-speed multi-range input 8 ch, between channels   |  | Screw terminal                                     | NP1AXH8VG-MR                            | 0              | Ō         | ŏ        | Ō        |
|                      |   | High-speed multi-range input 8 ch, between channels   |  | Screw terminal                                     | NP1AXH8IG-MR                            | Õ              | Ŏ         | Õ        | Ō        |
| - F                  |   | Resistance thermometer element input (Pt1 00 Ω/JI   | Pt 100 Ω) 4 ch                                     | Screw terminal                                     | NP1AXH4-PT                              | 0              | 0         | 0        | 0        |
|                      | NP1AXH6G-PT   | Accuracy: ±0.3% (ambient temperature: 18 to 28°C<br>High-accuracy resistance thermometer element input (P<br>Accuracy: ±0.05 to ±0.07% (ambient temperature: 18 to 28     | Pt100Ω/JPt100Ω) 6 ch                               | Screw terminal                                     | NP1AXH6G-PT                             | 0              | 0         | 0        | 0        |
|                      | NP1AXH4-TC  | Thermo-couple input module 4 ch<br>Accuracy: ±0.3% (ambient temperature: 18 to 28°C   |  | Screw terminal                                     | NP1AXH4-TC                              | 0              | 0         | 0        | 0        |
|                      | NP1AXH8G-TC   | High-accuracy thermo-couple input module 8 ch<br>Accuracy: ±0.05 to ±0.26% (ambient temperature: 18 to 28%  |  | Screw terminal                                     | NP1AXH8G-TC                             | 0              | 0         | 0        | 0        |
|                      |   | Distributor module, 4 ch, between channels high diel  |  | Screw terminal                                     | NP1AXH4DG-MR                            | 0              | ○*7       |          |          |
|                      | NP1AXH4DG-MR  | Accuracy: ±0.1% of F.S.R. (ambient temperature: 2   | 25°C)  |  |   |                |           | L        | <u> </u> |
| Analog output module | NP1AY02-MR  | Accuracy: ±0.1% of F.S.R. (ambient temperature: 2<br>Standard type multi-range output 2 ch, resolution  | n: 10 bits   | Screw terminal                                     | NP1AY02-MR                              | 0              | 0         | 0        | 0        |
| Analog output module | NP1AY02-MR<br>NP1AYH2-MR  | Accuracy: ±0.1% of F.S.R. (ambient temperature: 2   | n: 10 bits<br>14 bits                              | Screw terminal<br>Screw terminal<br>Screw terminal | NP1AY02-MR<br>NP1AYH2-MR<br>NP1AYH4V-MR | 000            | 0         | 0        | 0        |

\*8 Connectors (solder type) for digital input, output, I/O mixture and positioning module are separately sold. Applicable connector type: Fujitsu FCN-361J040-AU (connector), FCN-360C040-B (cover), our product type: NP8V-CN

O Applicable - Not applicable

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| Product name             | Model                | Specifications and names   |                 | Ordering code | Stand<br>CE<br>*2 | UL<br>cUL | LR<br>*3 | Nł |
|--------------------------|----------------------|--|-----------------|---------------|-------------------|-----------|----------|----|
| nalog output<br>odule    | NP1AYH4VG-MR         |  | Screw terminal  | NP1AYH4VG-MR  | 0                 | 0         | 0        | 0  |
| loquie                   |                      |  | Screw terminal  | NP1AYH4IG-MR  | 0                 | 0         | 0        | С  |
|                          |                      | High-speed multi-range output 8 ch, resolution: 14 bits (voltage type)   | Screw terminal  | NP1AYH8V-MR   | 0                 | 0         | 0        | C  |
|                          |                      | High-speed multi-range output 8 ch, resolution: 14 bits (current type)   | Screw terminal  | NP1AYH8I-MR   | 0                 | 0         | 0        | C  |
|                          |                      |  | Screw terminal  | NP1AYH8VHR-MR | 0                 | 0*7       |          | _  |
|                          |                      | High-speed multi-range I/O, input 4 ch, output 2 ch, resolution: 14 bits   | Screw terminal  | NP1AWH6-MR    | 0                 | 0         |          |    |
| ommunication             | NP1L-WE1             | Web module 10BASE-T/100BASE-TX Web server function (Japanese version) *9   |                 | NP1L-WE1      | 0                 | 0         |          | +  |
| nodule                   | NP1L-WS1             | Web memory module 10BASE-T/100BASE-TX Web server function/memory data collection function  | on              | NP1L-WS1      |                   |           |          |    |
|                          | NP1L-ET1             | Ethernet interface module 10 BASE-T/100 BASE-TX  |                 | NP1L-ET1      | 0                 | 0         |          |    |
|                          | NP1L-FL3             | FL-net (OPCN-2) module Ver. 3 (10/100 Mbps)  |                 | NP1L-FL3      | 0                 | 0         |          |    |
|                          | NP1L-LW1             | Lon Works interface module (78 kbps) Accessories: Connector for cable connected  |                 | NP1L-LW1      |                   | 0         |          |    |
|                          | NP1L-PL1             | P-link module Accessories: P/PE-link connector   |                 | NP1L-PL1      |                   | 0         |          |    |
|                          | NP1L-PE1             | PE-link module Accessories: P/PE-link connector  |                 | NP1L-PE1      |                   | 0         |          |    |
|                          | NP1L-LE1             | LE-net module  |                 | NP1L-LE1      | 0                 | 0         | 0        | 0  |
|                          | NP1L-LL2             | LE-net loop2 module  |                 | NP1L-LL2      | 0                 | 0         | 0        | 0  |
|                          | NP1L-RS1             | General purpose communication module RS-232C (connector), RS-485 (connector) each 1 ch *11   |                 | NP1L-RS1      | 0                 | 0         | 0        | (  |
|                          | NP1L-RS2             | General purpose communication module RS-232C (connector) 1 ch *11  |                 | NP1L-RS2      | 0                 | 0         | 0        | (  |
|                          | NP1L-RS3             | General purpose communication module RS-232C (connector) 2 ch *11  |                 | NP1L-RS3      | 0                 | 0         |          | t  |
|                          | NP1L-RS4             | General purpose communication module RS-485 (connector) 1 ch *11   |                 | NP1L-RS4      | 0                 | 0         | 0        | 0  |
|                          | NP1L-RS5             | General purpose communication module RS-485 (screw terminal) 2 ch  |                 | NP1L-RS5      | 0                 | 0         | 0        | (  |
|                          | NP1L-JP1             | OPCN-1 master module Accessories: OPCN-1 connector, terminating resistors (2 pieces)   |                 | NP1L-JP1      | 0                 | 0         | 0        | 0  |
|                          | NP1L-JS1             | OPCN-1 slave module Accessories: OPCN-1 connector  |                 | NP1L-JS1      | 0                 | 0         |          | Ť  |
|                          | NP1L-J31<br>NP1L-RJ1 | OPCN-1 stave module Accessories: OPCN-1 connector, SX bus terminating plug (2 pieces)  |                 |               | 0                 | 0         | 0        | 0  |
|                          |                      |  |                 | NP1L-RJ1      | 0                 | 0         |          | -  |
|                          | NP1L-DN1             | DeviceNet master module Accessories: Screw connector (for cable attachment)  |                 | NP1L-DN1      |                   |           |          | 4  |
|                          | NP1L-DS1             | DeviceNet slave module 1 ch Accessories: Screw connector (for cable attachment)  |                 | NP1L-DS1      | 0                 | 0         |          | +  |
|                          | NP1L-PD2             | DeviceNet interface module Accessories: Screw connector (for cable attachment), SX bus terminating   | plug (2 pieces) | NP1L-RD1      | 0                 | 0         |          |    |
|                          | NP1L-TL1             | T-link master module Accessories: T-link connector, T-link terminating resistor (2 pieces)   |                 | NP1L-TL1      | 0                 | 0         | 0        | (  |
|                          | NP1L-RT1             | T-link interface module Accessories: T-link connector, SX bus terminating plug (2 pieces)  |                 | NP1L-RT1      | 0                 | 0         | 0        | 0  |
|                          | NP1L-TS1             | T-link slave module Accessories: T-link connector  |                 | NP1L-TS1      | 0                 | 0         | 0        | (  |
|                          | NP1L-PD1             | PROFIBUS-DP master module Communication standard (IEC 66158, EN 50171, DIN 19245)  |                 | NP1L-PD1      | 0                 | 0         |          |    |
|                          | NP1L-PS1             | PROFIBUS-DP slave module Communication standard (IEC 66158, EN 50171, DIN 19245)   |                 | NP1L-PS1      | 0                 | 0         |          |    |
|                          | NP1L-RP1             | PROFIBUS-DP interface module Communication standard (IEC 66158, EN 50171, DIN 19245)   |                 | NP1L-RP1      | 0                 | 0*7       |          | T  |
|                          | NP1L-MN1             | M-NET communication module M-NET x 1 channel   |                 | NP1L-MN1      |                   |           |          | Τ  |
|                          | NP1L-RM1             | Remote terminal master/slave module  |                 | NP1L-RM1      |                   |           |          | T  |
|                          |                      | Function as a master/slave station of remote terminal RM20/RM21 series   |                 |               |                   |           |          |    |
|                          | NP1L-UC1             | USB communication between the CPU module and the programming support tool  |                 | NP1L-UC1      | 0                 | 0         |          |    |
|                          | NP1L-OL1             | SX bus electrical-optical converter (PCF cable) Accessories: SX bus terminating plug   |                 | NP1L-OL1      | 0                 | 0         |          |    |
|                          | NP1L-OL3             | SX bus electrical-optical converter (Quartz cable) Accessories: SX bus terminating plug  |                 | NP1L-OL3      | 0                 |           |          |    |
|                          | NP2L-OE1             | SX bus electrical-optical converter Accessories: SX bus terminating plug   |                 | NP2L-OE1      | 0                 | 0         |          |    |
|                          | NP2L-RP1             | SX bus electrical-electrical repeater Accessories: SX bus terminating plug   |                 | NP2L-RP1      | 0                 | 0*7       |          |    |
|                          | NP2L-BH1             | SX bus duplex connection unit  |                 | NP2L-BH1      |                   | 0         | 0        | 0  |
|                          | FNC160A-C20          | T-link optical converter Accessories: T-link connector, T-link terminating resistor  |                 | NH5F-OCHTL17  |                   |           |          | Т  |
|                          | FNC360A-C20          | P/PE-link optical converter Accessories: P/PE-link connector, P/PE-link terminating resistor, ferrite  | core            | NH5F-OCHPE17  |                   |           |          | t  |
| ositionina               | NP1F-HC2             | High-speed counter module 500 kHz x 2 ch Input signal voltage: 5 V DC Accessories: Optional cor  | nector          | NP1F-HC2      | 0                 | 0         |          | Т  |
| odule*8                  | NP1F-HC2MR           | High-speed counter module 200 kHz x 2 ch, Input signal voltage: 5/12/24 V DC Accessories: Optic  |                 | NP1F-HC2MR    | 0                 | 0         |          |    |
|                          |                      | High-speed counter module 50 kHz x 2 ch, Input signal voltage: 5/12/24 V DC Accessories: Option  |                 | NP1F-HC2MR1   | 0                 | 0         |          | T  |
|                          | NP1F-HC8             | High-speed counter module 50 kHz x 8 ch Input signal voltage: 5 V DC Accessories: Optional cont  |                 | NP1F-HC8      | 0                 | 0         |          | t  |
|                          | NP1F-HP2             | Two-axis pulse train output positioning module Pulse train instruction 250 kHz x 2 ch Optional con   |                 | NP1F-HP2      | 0                 | 0         |          | ٣  |
|                          | NP1F-MP2             | Two-axis pulse train output positioning module (open collector output): 250 kHz x 2 ch, feedback p   |                 | NP1F-MP2      | 0                 | 0         |          | ÷  |
|                          | NP1F-HD2             | Accessories: Optional connector<br>Two-axis high-speed pulse train positioning control module: (differential output), output pulse: 5MHz         |                 | NP1F-HD2      | 0                 |           |          |    |
|                          | NP1F-HD2A            | pulse: 5MHz, connector (separately sold)<br>Two-axis high-speed pulse train positioning module (differential output), output pulse: 5MHz, feedba | ,               | NP1F-HD2A     | 0                 |           |          |    |
|                          | NP1F-HD4             | 5MHz, connector (separately sold)<br>4-axis high-speed pulse train positioning module (differential output), output pulse: 5MHz, feedback        | pulse: 5MHz,    | NP1F-HD4      | 0                 |           |          | T  |
|                          | NP1F-MA2             | connector (separately sold)<br>Two-axis analog multiple positioning module Feedback pulse: 500 kHz x 2 ch Accessories: Option                    | al connector    | NP1F-MA2      | 0                 | 0         |          | ╞  |
|                          |                      |  | ial connector   |               |                   |           |          |    |
| unction module           |                      | Dummy module   |                 | NP1F-DMY      | 0                 | 0         | 0        | (  |
|                          | NP1F-MU1             | Multi-use communication module RS-232C x 1 ch, RS-485 x 1 ch Communication by the arbitrary p  | 010001          | NP1F-MU1      | 0                 | 0         |          | 4  |
|                          | NP1F-PI4             | Flow meter F/AD conversion module 10 kHz x 4 ch, between channels insulated  |                 | NP1F-PI4      | 0                 | 0*7       |          | 1  |
| ersonal computer         |                      | Programming Support Tool Expert (D300win) software package Version 3 (Japanese/English versio  | ns)             | NP4H-SEDBV3   | -                 | -         | -        | 4  |
| ader *10                 | NP4H-SWN             | Programming Support Tool Standard (Japanese/English versions)  |                 | NP4H-SWN      | -                 | -         | -        | -  |
|                          | NW0H-CA3             | Programming support tool connection cable for personal computer (used with the converter: NP4H-  | CVU)            | NW0H-CA3      | -                 | -         | -        | -  |
| bader connecting         |                      |  |                 |               |                   |           |          |    |
| bader connecting<br>able | NP4H-CC2             | FUJILOG µK connection cable  |                 | NP4H-CC2      | -                 | -         | -        | -  |

\*3 To prevent vibration, the module must be fixed for each of the base boards.

\*7 There is no cUL certification.

\*8 Connectors (solder type) for digital input, output, I/O mixture and positioning module are separately sold.
Applicable connector type: Fujitsu FCN-361J040-AU (connector), FCN-360C040-B (cover), our product type: NP8V-CN
\*9 Ask our sales representative for the English version and the Chinese version.
\*10 The OS and the Japanese conversion software are not included.

\*11 Connector fixing screws can be mounted using metric screws (M2.6). Products using imperial screws are also available. Please contact our sales office for details. (type ends with Z607)

|                |                                     |   |  |          |                            |                    |                | Stan     | dards     |                |    |
|----------------|-------------------------------------|---|--|----------|----------------------------|--------------------|----------------|----------|-----------|----------------|----|
| Product na     | ame                                 | Model   | Specifications and names                                   |          |                            |                    | Ordering code  | CE<br>*2 | UL<br>cUL | LR<br>*3       | NK |
| ROM cass       | sette                               | NP8PMF-16   | User ROM cassette for the SPH200, Capacity: 16 MB          |          |                            |                    | NP8PMF-16      | -        | -         | -              | -  |
|                |                                     | NP8PCF-256  | User ROM card compact flash memory for the SPH300/S        | SPH200   | 00, Capacity: 256 MB       |                    | NP8PCF-256     | -        | -         | -              | -  |
|                |                                     | NP8PSD-002  | User ROM card SD memory card for the SPH3000/SPH3          | 3000 M   | M, Capacity: 2 GB          |                    | NP8PSD-002     | -        | -         | -              | -  |
| Auxiliaries    | 3                                   | NP8P-BT   | Data backup battery (Battery type: Lithium primary batter  | ry)      |                            |                    | NP8P-BT        | -        | -         | -              | -  |
|                |                                     | NP8P-BT1  | Data backup for high-capacity battery (Battery type: Lithi | um prin  | nary battery)              |                    | NP8P-BT1       | -        | -         | -              | -  |
|                |                                     | NP8P-BTS  | Data backup for high-capacity battery box (NP8P-BT1 +      | storage  | e box)                     |                    | NP8P-BTS       | -        | -         | -              | -  |
|                |                                     | NP8B-BP   | SX bus terminating plug (1 piece)                          |          |                            |                    | NP8B-BP        | -        | -         | -              | -  |
|                |                                     | NP8B-ST   | Base board mounting stud (DIN rail type (2 pieces))        |          |                            |                    | NP8B-ST        | -        | -         | -              | -  |
|                |                                     | NP8V-CN   | I/O, positioning control module connector (solder type)    |          |                            |                    | NP8V-CN        | -        | -         | -              | -  |
|                |                                     | FTC120T   | T link/ OPCN-1 connector                                   |          |                            |                    | NH5V-TL1CC     | -        | -         | -              | -  |
|                |                                     | FTC120P   | P/PE link connector  |          |                            |                    | NH5V-PL1CC     | -        | -         | -              | -  |
|                |                                     | FRT120A100  | T link / OPCN-1 terminating resistor                       |          |                            |                    | NH5V-TL1RT     | -        | -         | -              | -  |
|                |                                     | FRT220A75   | P/PE link terminating resistor                             |          |                            |                    | NH5V-PL1RT     | -        | -         | -              | -  |
|                | PCN-1                               | NR1JX-1606DT  | 24 V DC, 16-point bi-directional input, detachable termine | al block | <                          |                    | NR1JX-1606DT   | 0        | 0         |                |    |
| OF<br>NR1 type |                                     | NR1JY-08R07DT   | Ry output 240 V AC / 110 V DC, 8 points, detachable ten    |          |                            |                    | NR1JY-08R07DT  | 0        | 0         |                |    |
| rmi lype       |                                     | NR1JY-16T05DT   | 24 V DC, 16-point Tr sink output, detachable terminal blo  |          |                            |                    | NR1JY-16T05DT  | 0        | 0         |                |    |
| nal            |                                     | NR1JW-16T65DT   | 24 V DC, 8-point source input                              |          |                            |                    | NR1JW-16T65DT  | 0        | 0         |                | -  |
|                |                                     |   | 24 V DC, 8-point Tr sink output, detachable terminal bloc  | •k       |                            |                    |                |          | ľ         |                |    |
| T.I            |                                     | ND1TY-1606DT  | 24 V DC, 16-point H sink output, detachable terminal bloc  |          | /                          |                    | NR1TX-1606DT   | 0        | 0         | 0              | 0  |
| 1-6            | T-LINK NR1TX-1606DT<br>NR1TY-08R07D |   |  |          |                            |                    | NR1TY-08R07DT  | 0        | 0         | 0              | 0  |
|                | NR1TY-16T05DT                       | 24 V DC, 16-point Tr sink output, detachable terminal blo |  |          |                            | NR1TY-16T05DT      | 0              | 0        | 0         | 0              |    |
|                |                                     |   | 24 V DC, 10-point in sink output, detachable terminal bic  | JCK      |                            |                    | NR1TW-16T65DT  | 0        | 0         | 0              | 0  |
|                |                                     | NH11W-10105D1   |  | d.       |                            |                    |                |          | ľ         | Г <sup>0</sup> |    |
|                | ( buo                               | NR1SX-1606DT  | 24 V DC, 8-point Tr sink output, detachable terminal bloc  |          |                            |                    | NR1SX-1606DT   | 0        | 0         |                |    |
| 54             | Cous                                | NR1SX-1606D1  | 24 V DC, 16-point bi-directional input, detachable termin  |          |                            |                    | NR1SY-08R07DT  | 0        | 0         |                |    |
|                |                                     |   | Ry output 240 V AC / 110 V DC, 8 points, detachable ten    |          | IUCK                       |                    |                | 0        | 0         |                | +  |
|                |                                     | NR1SY-16T05DT   | 24 V DC, 16-point Tr sink output, detachable terminal blo  | OCK      |                            |                    | NR1SY-16T05DT  |          |           |                |    |
|                |                                     | NR15W-16165D1   | 24 V DC, 8-point source input                              |          |                            |                    | NR1SW-16T65DT  | 0        | 0         |                |    |
|                |                                     |   | 24 V DC, 8-point Tr sink output, detachable terminal bloc  |          |                            |                    |                | 0        |           |                | +  |
|                |                                     | NR1SF-HP4DT   | Pulse train output, pulse train command: 250 kHz 4 axes    |          | ,                          |                    | NR1SF-HP4DT    | 0        |           |                |    |
| LON            | NWORKS                              | NR1LX-1606DT  | 24 V DC, 16-point (including the 4 pulse input points), de |          |                            | Accessories:       | NR1LX-1606DT   |          |           |                |    |
|                |                                     | NR1LY-08R07DT   | Ry output 240 V AC / 110 V DC, 8 points, detachable ter    |          |                            | Neuron             | NR1LY-08R07DT  |          |           |                |    |
|                |                                     | NR1LW-11R80DT   | Source input 24 V DC, 9 points (including the 4 pulse inp  | •        |                            | ID seal            | NR1LW-11R80DT  |          |           |                |    |
|                |                                     |   | Ry output 240 V AC / 110 V DC, 2 points, detachable ter    | minal b  | lock                       |                    |                |          |           |                | _  |
| ·              | otion                               | NR1XV-CB1   | Common extension bar (9 pins)                              |          |                            |                    | NR1XV-CB1      | -        |           |                |    |
| CPU boar       | ď                                   | NP3PS-SX1PCS32  | PCI-Bus-based SPH300 CPU Board                             | Access   | sories:                    |                    | NP3PS-SX1PCS32 | 0        |           |                |    |
|                |                                     |   | Program memory capacity: 32 K steps                        | Drive    | er CD, memory backup ba    | attery             |                |          |           |                |    |
|                |                                     | NP3PS-SX1PCS74  | PCI-Bus-based SPH300 CPU Board                             |          | us terminating plug (2 pie |                    | NP3PS-SX1PCS74 | 0        |           |                |    |
|                |                                     |   | Program memory capacity: 74K steps                         | CPU      | mode switching key, usa    | ige nameplate seal |                |          |           |                |    |
| nterface b     | board                               | NP3L-LL2PCS   | PCI-bus-based LE-net loop 2 board                          |          | Accessories:               |                    | NP3L-LL2PCS    |          |           | 0              | 0  |
|                |                                     |   |  |          | Driver (CD version)        |                    |                |          |           |                |    |
|                |                                     | NP3L-FL3PCS   | PCI-bus-based FL-net (OPCN-2) Ver. 2.0 board               |          | Accessories:               |                    | NP3L-FL3PCS    |          |           |                |    |
|                |                                     |   | (10/100 Mbps)  |          | Driver (CD version), na    | ame and use seal   |                |          |           |                |    |
| ower Sup       | ply Unit                            | NP8S-LC1  | 100 to 200 V AC input, board-mounting type, supply of p    | ower to  | FLT-ASFKA through a lo     | ader cable         | NP8S-LC1       |          |           |                |    |
| or FLT-ASE     | FKA                                 | NP8S-LC2  | 100 to 200 V AC input, tabletop-mounting type, supply of   | power    | to FLT-ASFKA through a     | loader cable       | NP8S-LC2       |          |           |                |    |

O Applicable - Not applicable

| duct nar   | me   | Model                                 | Specifications and names   | Ordering code  | CE<br>*2 | UL<br>cUL | LR<br>*3 |   |
|--|--|---------------------------------------|--|----------------|----------|-----------|----------|---|
| F120S-   | Frame set  | NP8REFSS-02                           | NP8REFSB-02 x 1 unit, NP8REFSF-02 x 1 unit   | NP8REFSS-02    |          |           |          |   |
| F150S  | (SPH mounting  | NP8REFSS-04                           | NP8REFSB-04 x 1 unit, NP8REFSF-04 x 1 unit   | NP8REFSS-04    |          |           |          | ī |
| F150S<br>F250<br>F120H<br>F80H<br>Base 1<br>Base 1<br>Base 1<br>Base 1<br>Conve<br>cable<br>(Cable<br>600 m<br>F70<br>Base 1<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>cable<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conve<br>Conv |  | NP8REFSS-06                           | NP8REFSB-06 x 1 unit, NP8REFSF-06 x 1 unit   | NP8REFSS-06    |          |           |          | i |
| F120S-<br>(SPH m<br>boardF150S<br>F150A<br>F120HF80HSPH r<br>boardBase r<br>adaptaConve<br>adapteConve<br>cable<br>(Unit for<br>convers<br>adapterF70Base r<br>adapterF55Base r<br>adapterConve<br>adapterF55Base r<br>adapterConve<br>adapterF70Base r<br>adapterConve<br>adapterF70Base r<br>adapterConve<br>adapterF55Base r<br>adapterConve<br>adapterF55Base r<br>adapterF55Base r<br>adapterConve<br>adapterF55Base r<br>adapterF55Base r<br>adapterConve<br>adapterF55Base r<br>adapterF55Base r<br>adapterF55Base r<br>adapterF55Base r<br>adapterF55Base r<br>adapterF55Base r<br>adapterF55Base r<br>adapterF55Base r<br>adapterF55<   | unit)  | NP8REFSS-08                           | NP8REFSB-08 x 1 unit, NP8REFSF-08 x 1 unit   | NP8REFSS-08    |          |           |          | i |
|  | SPH mounting   | NP8REFSF-02                           | Base unit for NP8REFSF-02 (spacer, screw, washer, and nut included, four pieces each)  | NP8REFSF-02    |          |           |          | 1 |
|  |  | NP8REFSF-04                           |  |                |          |           |          | ſ |
| Provention       F120S-<br>F150S<br>F150S<br>F120H       Frame<br>(SPH m<br>board         Rame       SPH r<br>board         Base r       Convector         Convector       Convector         Adapted       F55         Base r       Convector         Convector       Convector         Adapted       F55         Base r       Convector         Convector       Convector         Adapted       F55         Base r       Convector         Convector       Convector         Convector       Convector         Rome       Convector         F1       Base r         Convector       Convector         Convector       Convector         Convector       Convector         Convector       Convector         Convector   |  |                                       | Base unit for NP8REFSF-04 (spacer, screw, washer, and nut included, four pieces each)  | NP8REFSF-04    |          |           |          | 1 |
|  |  | NP8REFSF-06                           | Base unit for NP8REFSF-06 (spacer, screw, washer, and nut included, four pieces each)  | NP8REFSF-06    |          |           |          |   |
|  |  | NP8REFSF-08                           | Base unit for NP8REFSF-08 (spacer, screw, washer, and nut included, four pieces each)  | NP8REFSF-08    |          |           | -        |   |
| P       F120S-<br>(SPH mounting)<br>board + base<br>unit)         F120H       SPH mounting)<br>board + base<br>unit)         SPH mounting<br>board       SPH mounting<br>board         Base unit       Conversion<br>adapter<br>(Unit for mounting<br>conversion<br>adapter)         Conversion<br>adapter)       Conversion<br>cable<br>(Cable length:<br>600 mm)         F70       Base adapter         F70       Base adapter         Conversion<br>adapter       Conversion<br>adapter         F55       Base adapter         Conversion<br>adapter       Conversion<br>adapter  | Base unit  | NP8REFSB-02                           | Attachable base: For FSB084H   | NP8REFSB-02    |          |           |          |   |
|  | NP8REFSB-04  | Attachable base: For FSB124H, FSB086H | NP8REFSB-04  |                |          |           |          |   |
|  |  | NP8REFSB-06                           | Attachable base: For FSB126H, FSB088H  | NP8REFSB-06    |          | -         |          |   |
|  |  | NP8REFSB-08                           | Attachable base: For FSB128H, FSB156S-2, FSB154S-4, FSB110H  | NP8REFSB-08    |          |           |          |   |
|  | 120S-<br>150S       Frame set<br>(SPH mounting<br>board + base<br>unit)         80H       SPH mounting<br>board         80H       SPH mounting<br>board         Base unit       Conversion<br>adapter<br>(Unit for mounting<br>conversion<br>adapter)         Conversion<br>cable<br>(Cable length:<br>600 mm)       Conversion<br>adapter         70       Base adapter         55       Base adapter         Conversion<br>adapter       Conversion<br>cable         55       Base adapter         Conversion<br>adapter       Conversion<br>adapter | NP8REFSA-204                          | 20-pole terminal block, for DC signals   | NP8REFSA-204   |          |           |          | _ |
|  |  | NP8REFSA-202                          | 20-pole terminal block, for AC signals   | NP8REFSA-202   |          |           |          |   |
|  |  | NP8REFSA-384                          | 38-pole terminal block, for DC signals   | NP8REFSA-384   |          |           |          |   |
|  |  | NP8REFSA-382                          | 38-pole terminal block, for AC signals   | NP8REFSA-382   |          |           |          |   |
|  |  | NP8REFSC-164X1                        | 16 points, for DC input (SPH side: Terminal block)   | NP8REFSC-164X1 |          |           |          |   |
|  |  | NP8REFSC-164Y1                        | 16 points, for DC output (SPH side: Terminal block)  | NP8REFSC-164Y1 |          |           |          |   |
|  |  | NP8REFSC-164Y2                        | 16 points, for DC output (SPH side: Terminal block)  | NP8REFSC-164Y2 |          |           |          |   |
|  | PS-<br>S-<br>S-<br>S-<br>S-<br>S-<br>S-<br>S-<br>S-<br>S-<br>S-<br>S-<br>S-<br>S   | NP8REFSC-162W1                        | For both input and output, for analog signals (SPH side: Terminal block)   | NP8REFSC-162W1 |          |           |          | ſ |
|  |  |                                       | For DC input (SPH side: Terminal block)  | NP8REFSC-324X1 |          |           |          | ī |
|  |  |                                       | For DC input (SPH side: Connector)   | NP8REFSC-324X2 |          |           |          | ľ |
|  |  |                                       | 32 points, for DC output (SPH side: Connector)   | NP8REFSC-324Y1 |          |           |          | Ĩ |
|  |  |                                       | 32 points, for DC output (SPH side: Connector)   | NP8REFSC-324W2 |          |           |          | ī |
|  |  |                                       | 16 points, for relay independent-output (SPH side: Terminal block)   | NP8REFSC-164W1 |          |           |          | i |
|  |  |                                       | 32 points, for both input and output (SPH side: Connector) (Cable length: 200 mm)  | NP8REFSC-324W1 |          |           |          | ſ |
|  |  |                                       |  |                |          |           |          |   |
|  | Base adapter   |                                       | 32 points, for AC input (SPH side: Terminal block)   | NP8REFSC-322X1 |          |           |          |   |
| 570  | 70 Base adapter  |                                       | 32 points, for AC output (SPH side: Terminal block)  | NP8REFSC-322Y1 |          |           | -        |   |
| F70  | Base adapter   | NP8RE70B-02                           | For NC1B02 (Mounting screws included)  | NP8RE70B-02    |          |           |          |   |
|  |  | NP8RE70B-04                           | For NC1B04, NC1B02 (Mounting screws included)  | NP8RE70B-04    |          |           |          |   |
|  |  | NP8RE70B-06                           | For NC1B06, NC1B04, NC1B02 (Mounting screws included)  | NP8RE70B-06    |          |           |          |   |
|  | Conversion   | NP8RE70B-08                           | For NC1B8, NC1B06, NC1B04 (Mounting screws included)   | NP8RE70B-08    |          |           |          |   |
|  |  | NP8RE70B-10                           | For NC1B10, NC1B08, NC1B06 (Mounting screws included)  | NP8RE70B-10    |          |           |          | _ |
|  |  | NP8RE70A-201                          | 16 points, for DC input/output (Terminal cover included)   | NP8RE70A-201   |          |           |          |   |
| Conversion   | adapter  | NP8RE70A-202                          | 16 points, for AC input/output (Terminal cover included)   | NP8RE70A-202   |          |           |          |   |
|  |  | NP8RE70A-203                          | 8 points, for relay independent-output (Terminal cover included)   | NP8RE70A-203   |          |           |          |   |
|  |  | NP8RE70A-204                          | 2 points/ 4 points, for analog input (Terminal cover included)   | NP8RE70A-204   |          |           |          |   |
|  |  | NP8RE70A-205                          | 2 points, for analog output (Terminal cover included)  | NP8RE70A-205   |          |           |          |   |
|  | Conversion<br>adapter NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70<br>NP8RE70  | NP8RE70A-401                          | 32 points, for DC input/output   | NP8RE70A-401   |          |           |          | Î |
|  |  | NP8RE70A-402                          | 64 points, for DC input/output   | NP8RE70A-402   |          |           |          | I |
| F55  | Base adapter   | NP8RE55B-04                           | For NV1P-042, NV1P-044, NV1E-042, NV1E-044 (Mounting screws included)  | NP8RE55B-04    |          |           |          | Î |
|  |  | NP8RE55B-06                           | For NV1P-062, NV1P-064, NV1E-062, NV1E-064 (Mounting screws included)  | NP8RE55B-06    |          |           |          | ľ |
|  |  | NP8RE55B-08                           | For NV1P-082, NV1P-084, NV1E-082, NV1E-084 (Mounting screws included)  | NP8RE55B-08    |          |           |          | Î |
|  |  | NP8RE55B-08L                          | For NV1P-082, NV1P-084, NV1E-082, NV1E-084 (Mounting screws included)  | NP8RE55B-08L   |          |           |          | j |
|  | Conversion   | NP8RE55A-181                          |  | NP8RE55A-181   |          |           |          | 1 |
|  |  |                                       | 16 points, for DC input and relay output (8 points x 2 common)   |                |          |           |          | , |
|  |  | NP8RE55A-182                          | 16 points, for DC output   | NP8RE55A-182   |          |           |          | 1 |
|  |  | NP8RE55A-183                          | 8 points, for relay independent-output   | NP8RE55A-183   |          |           |          |   |
|  |  | NP8RE55A-184                          | 8 points, for AC input   | NP8RE55A-184   |          |           |          |   |
|  |  | NP8RE55A-185                          | 8 points, for SSR output   | NP8RE55A-185   |          |           |          |   |
|  |  | NP8RE55A-186                          | 4 points, for analog input   | NP8RE55A-186   |          |           |          |   |
|  |  | NP8RE55A-187                          | 2 points, for analog voltage output  | NP8RE55A-187   |          |           |          |   |
|  |  | NP8RE55A-188                          | 2 points, for analog current output  | NP8RE55A-188   |          |           |          | ĺ |
|  |  | NP8RE70A-401                          | 32 points, for DC input/output   | NP8RE70A-401   |          |           |          | ĺ |
|  |  | NP8RE55A-402                          | 32 points, for DC input/output   | NP8RE55A-402   |          |           |          | ĺ |
| NJ   | Base adapter   | NP8RENJB-03                           | For NJ-BP3-Z400 (NJ-BP3), NJ-BE3-Z400(NJ-BE3) (Mounting screws included)   | NP8RENJB-03    |          |           |          | Ĩ |
|  |  | NP8RENJB-05                           | For NJ-BP5-Z400 (NJ-BP5), NJ-BT5-Z400 (NJ-BT5), NJ-BE5-Z400 (NJ-BE5) (Mounting screws included)                                      | NP8RENJB-05    |          |           |          | j |
|  |  | NP8RENJB-08                           | For NJ-BP8-Z400 (NJ-BP8), NJ-BT8-Z400 (NJ-BT8), NJ-BE8-Z400 (NJ-BE8) (Mounting screws included)                                      | NP8RENJB-08    |          |           |          | 1 |
|  |  | NP8RENJB-08L                          | For NJ-BP8-Z400 (NJ-BP8), NJ-BT8-Z400 (NJ-BT8), NJ-BE8-Z400 (NJ-BE8) (Mounting screws included)                                      | NP8RENJB-08L   |          |           |          | į |
|  | Conversion   | NP8RENJA-181                          |  |                |          |           |          | 1 |
|  |  |                                       | 16 points, for DC input and relay output (Mounting brackets, one conversion PC board, terminal labels, junction connectors included) | NP8RENJA-181   |          |           |          | į |
|  |  | NP8RENJA-182                          | 16 points, for DC output (Mounting brackets, one conversion PC board, terminal labels, junction connectors included)                 |                |          |           |          | 1 |
|  | 1  | NP8RENJA-183                          | 8 points, for relay output (Mounting brackets, one conversion PC board, terminal labels, junction connectors included)               | NP8RENJA-183   |          | 1         |          |   |
|  |  | NP8RENJA-184                          | For multi-range analog input (Mounting brackets, one conversion PC board, terminal labels, junction connectors included)             | NP8RENJA-184   |          |           |          |   |

O Applicable - Not applicable

| Product name         Model         Specifications and names         Ordering code         C           Perfunction         MICREX-F Size<br>I/O Module         NP8X-120ZC         FTU120C (32DI) -equivalent I/O module<br>The bit order of the terminal block is switched by the switch         NP8X-120ZC         NP8X-120ZC |             | Stan  | dards         |          |           |          |   |
|--|-------------|---|---------------|----------|-----------|----------|---|
|  |             | Specifications and names  | Ordering code | CE<br>*2 | UL<br>cUL | LR<br>*3 | Ν |
| MICREX-F Size  | NP8X-120ZC  |   | NP8X-120ZC    |          |           |          |   |
| I/O Module   | NP8X-123ZC  | FTU123C (24 V DC input, 32DI) -equivalent I/O module  | NP8X-123ZC    |          |           |          | t |
|  | NP8X-155ZC  | FTU155C (32DI, 100 V AC) -equivalent I/O module   | NP8X-155ZC    |          |           |          |   |
|  | NP8X-165ZC  | The bit order of the terminal block is switched by the switch. Depth: Standard model  | NP8X-165ZC    |          |           |          |   |
|  |             | The bit order of the terminal block is switched by the switch. Depth: Low-profile model   |               |          |           |          |   |
|  |             | The bit order of the terminal block is switched by the switch. Depth: Low-profile model   |               |          |           |          |   |
|  | NP8Y-221ZC  | FTU221C (24 V DC output, 32DO) -equivalent I/O module<br>The bit order of the terminal block is switched by the switch. Depth: Standard model                       | NP8Y-221ZC    |          |           |          |   |
|  | NP8Y-223ZC  | FTU223B (48 V DC output, 32DO) -equivalent I/O module<br>The bit order of the terminal block is switched by the switch. Depth: Standard model                       | NP8Y-223ZC    |          |           |          |   |
|  | NP8Y-226ZC  | FTU226B (32DO source) -equivalent I/O module<br>The bit order of the terminal block is switched by the switch. Depth: Standard model                                | NP8Y-226ZC    |          |           |          | _ |
|  | NP8Y-250ZC  | FTU250B (16SSR) -equivalent I/O module<br>The bit order of the terminal block is switched by the switch   | NP8Y-250ZC    |          |           |          |   |
|  | NP8Y-257ZC  | FTU257B (SSR32 points) -equivalent I/O module<br>The bit order of the terminal block is switched by the switch. Depth: Standard model                               | NP8Y-257ZC    |          |           |          | _ |
|  | NP8Y-266ZC  | FTU266B (32Ry) -equivalent I/O module<br>The bit order of the terminal block is switched by the switch  | NP8Y-266ZC    |          |           |          |   |
|  | NP8Y-263ZC  | FTU263B (16DO, all-point relay-independent contacts) -equivalent I/O module<br>The bit order of the terminal block is switched by the switch. Depth: Standard model | NP8Y-263ZC    |          |           |          | _ |
|  | NP8SY-260ZC | FTU260B (16 points Ry, 8 points common) -equivalent I/O module<br>The bit order of the terminal block is switched by the switch. Depth: Low-profile model           | NP8SY-260ZC   |          |           |          |   |
|  | NP8SY-263ZC | FTU263B (16Ry, all-point independent contacts) -equivalent I/O module<br>The bit order of the terminal block is switched by the switch. Depth: Low-profile model    | NP8SY-263ZC   |          |           |          |   |
|  | NP8AX-340MR | FTU340A/341A/342A/343A (voltage, 8AI) -equivalent I/O module<br>Depth: Standard model   | NP8AX-340MR   |          |           |          |   |
|  | NP8AX-344   | FTU344A (current, 8AI) -equivalent I/O module<br>Depth: Standard model  | NP8AX-344     |          |           |          | _ |
|  | NP8AY-440MR | FTU440A/441A/442A/443A (voltage, 8AO) -equivalent I/O module<br>Depth: Standard model   | NP8AX-440MR   |          |           |          |   |
|  | NP8B-06     | For 6-slot base of MICREX-SX  | NP8B-06       |          |           |          | _ |
|  | NP8B-08     | For 8-slot base of MICREX-SX  | NP8B-08       |          |           |          |   |
|  | NP8B-11     | For 11-slot base of MICREX-SX   | NP8B-11       |          |           |          | _ |
|  | NP8B-13     | For 13-slot base of MICREX-SX   | NP8B-13       |          |           |          |   |

O Applicable - Not applicable

#### Product warranty

## **Dear Customer**

#### Implied consent when you place an order

When you place an order for a product described in this document, in addition to the quotation, agreement, brochure, operation manual, user's manual and other documentation, please be aware that use of the product is based on your consent to the following items, especially those related to the warranty and application.

#### 1. Warranty Period and warranty coverage

#### 1-1 Warranty period

- (1) The warranty period is for one year from the date of purchase, or for 18 months from the date of manufacture printed on the nameplate, whichever is earlier.
- (2) Note that the warranty for parts which Fuji Electric's service department repaired is effective for six months from the date of the repair.

#### 1-2 Warranty coverage

- (1) If Fuji Electric is responsible for a malfunction occurring during the warranty period, we will replace or repair the failed part and deliver it free of charge to the location where it was installed or purchased. However, the warranty will not cover the following cases:
  - The malfunction occurs due to usage that impacts the product lifetime under inappropriate conditions, environment, handling, or excessive usage not described in the brochure, instruction manual, and user's manual.
  - 2) The malfunction is due to a cause not related to the purchased or delivered product.
  - 3) The malfunction is due to a cause not related to Fuji Electric's products, such as the customer's equipment and software design.
  - 4) As for our programmable products, the malfunction is caused by programs programmed by a company or person other than Fuji Electric.
  - 5) The malfunction is caused by any modification or repair made by a company or person other than Fuji Electric.
  - 6) The malfunction is caused because the consumable parts described in the operation manual and brochure have not been maintained and replaced properly.
  - 7) The cause cannot be foreseen from the perspective of science and technology as relates to the practical use of the product at the time of purchase or delivery.
  - 8) The malfunction is caused by a factor for which Fuji Electric is not responsible, such as a natural disaster or fire resulting from earthquakes, thunder, floods, etc., and external forces beyond control including abnormal voltage.
- (2) Note that the warranty is applicable only to the purchased or delivered goods alone.
- (3) The warranty covers only the products described in section 1-2 (1). The warranty does not cover any damages, such as the damage, loss, or lost profit of machinery, that may be induced by the purchased or delivered goods.

#### 1-3 Fault diagnosis

In principle, please make a primary fault diagnosis. However, Fuji Electric or our service department can perform the fault diagnosis for a fee upon the customer's request. In such a case, you are asked to bear the expenses charged in accordance with our fee schedule.

#### 2. Application

When using products described in this document, please make sure that the use of the products does not lead to a serious accident in the event that a failure or malfunction occurs in the products, and in cases of failure or malfunction, safety measures, such as a redundant design, malfunction preventive design, fail safe design, and foolproof design, should be adopted outside of the products in the system as standard operating conditions for the products.

Also, do not use the products under conditions or environments which are not described in the operation manual or user's manual. When using the products under the following conditions, please consult Fuji Electric in advance.

Generating stations including nuclear power, radiation-relevant facilities, railways, space / airline facilities Life line facilities such as gas, water lines, electricity, and communication, medical equipment, automobiles Combustion / fuel systems, amusement machines, data centers, charging or settlement systems Others (applications which have a large impact on life, the human body, community, important properties or rights)

#### 3. Repair period and supply period (maintenance period) of spare parts after discontinuation

When a model (product) is discontinued, its repair is conducted for seven years after the discontinued date. Also, main spare parts for repairs are supplied for seven years after the discontinued date. However, since electronic parts have a short life cycle and the procurement or production of electronic parts may be assumed to be difficult, the repair and supply of spare parts may become difficult even in the warranty period. For more information, please contact your Fuji Electric sales representative or service desk.

#### 4. Delivery conditions

For standard products which do not require application based settings or adjustments, the delivery will be completed when the products are transported to the customer. We are not responsible for field adjustment or trial operation.

#### 5. Service costs

The price of purchased or delivered goods does not include service costs such as fees for dispatching engineers. For more information, please contact your Fuji Electric sales representative or service desk.

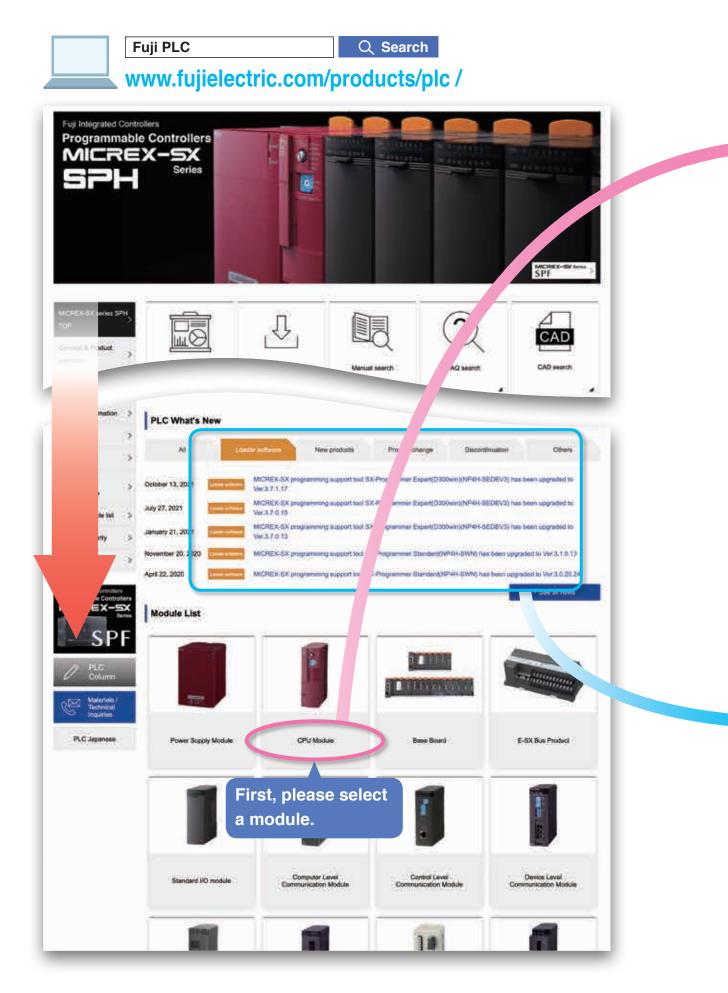
#### 6. Scope of services

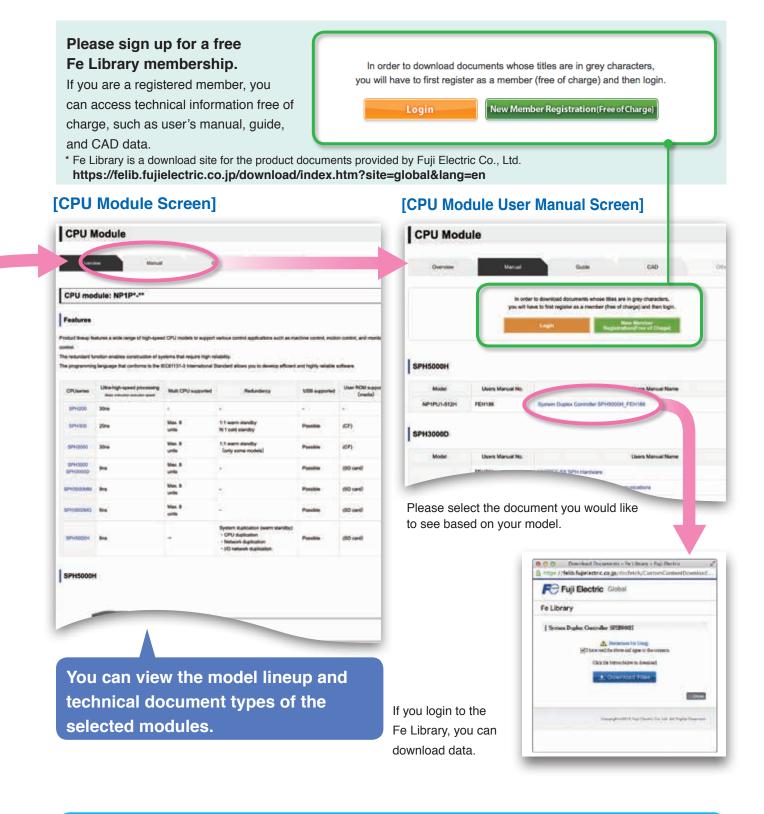
The description above assumes the products are sold and used in Japan. For information on products sold and used outside of Japan, please consult your product dealer or Fuji Electric.

# MEMO

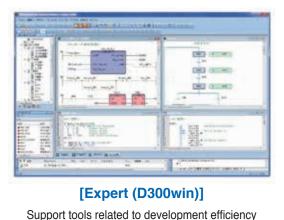
# **Guide to MICREX-SX Series Website**

On the MICREX-SX series website, you can quickly access the information you want. You can also download the latest technical information.





You can download the upgraded (latest) version of the programming support tool.



[Standard] Support tools related to operability

# MEMO

## ▲ Safety Precautions

- Before using this product, read the "Instruction Manual" and "User manual" carefully or consult with the retailer you purchased this product from and use this product correctly.
- The product described in this catalog has not been designed and produced to be used for equipment or systems which could endanger human life.
- Contact your dealer if you are considering using the product described in this catalog for any applications which have a large impact on life, the human body, community, important assets or rights (e.g., for power stations, radiation-related facilities, railways, space/airline facilities, lifeline facilities, or medical equipment).
- Please make sure that the use of the products does not lead to a serious accident in the event that a failure or malfunction occurs in the products described in this catalog. And in cases of failure or malfunction, safety measures should be prepared using external devices in a systematic manner as standard operating conditions for the products.
- For safe use, this product must be connected by those with specialized skills (in electric work, wiring work, etc.).
- Use a power supply which is reinforced and isolated from an AC power supply for an external power supply to connect to DC I/O (such as 24 V DC power supply). (You are recommended to use a power supply that conforms to EN60950.) Otherwise, an accident or breakdown may result.

#### Before purchasing this product

- For the details, price, and installation fee of the products included in this catalog, contact the retailer or Fuji Electric Co., Ltd.
- Please note that for product improvement, the appearance and specifications may be subject to change without prior notice.
- Please note in advance that printed and actual colors may differ slightly.
- Appearance and specifications are subject to change without prior notice for the purpose of product improvement.

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